

# **STD878 STN878**

# High current, high performance, low voltage NPN transistors

#### **Features**

- Very low collector to emitter saturation voltage
- DC current gain, h<sub>FE</sub> >100
- 5 A continuous collector current

## **Applications**

- Power management in portable equipment
- Voltage regulation in bias supply circuits
- Switching regulator in battery charger applications
- Heavy load driver

### **Description**

The devices are manufactured in low voltage NPN planar technology with "base island" layout, the resulting transistor shows exceptional high gain performance coupled with very low saturation voltage.

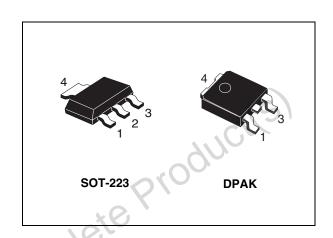


Figure 1. Internal schematic diagram

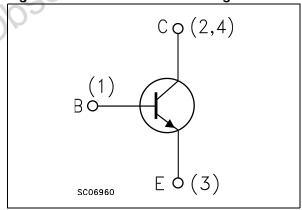


Table 1. Device summary

Order codes	Markings	Packages	Packaging	
STD878T4	D878	DPAK	Tape and reel	
STN878	N878	SOT-223	Tape and reel	

August 2010 Doc ID 13836 Rev 2 1/10

**Electrical ratings** STD878, STN878

#### **Electrical ratings** 1

**Absolute maximum ratings** Table 2.

Symbol	Parameter	Value	Unit
V <sub>CBO</sub>	Collector-base voltage (I <sub>E</sub> = 0)	45	V
V <sub>CEO</sub>	Collector-emitter voltage (I <sub>B</sub> = 0)	30	V
V <sub>EBO</sub>	Emitter-base voltage (I <sub>C</sub> = 0)	6	V
I <sub>C</sub>	Collector current	5	Α
I <sub>CM</sub>	Collector peak current (t <sub>P</sub> < 5 ms)	10	Α
В	Total dissipation at T <sub>C</sub> = 25 °C for STD878	15	W
P <sub>TOT</sub>	Total dissipation at T <sub>amb</sub> = 25 °C for STN878	1.6	VV
T <sub>STG</sub>	Storage temperature	-65 to 150	°C
TJ	Max. operating junction temperature 15		°C
Table 3.	Thermal data		
Symbol	Parameter	Value	Unit

Table 3. Thermal data

Symbol	Parameter		Value	Unit
R <sub>thJC</sub>	Thermal resistance junction-case for STD878	max	8.3	°C/W
R <sub>thJA</sub>	Thermal resistance junction-ambient for STN878 (1)	max	78	°C/W

Obsolete Produc 1. Device mounted on PCB area of 1 cm<sup>2</sup>.

# 2 Electrical characteristics

 $T_{case}$  = 25 °C unless otherwise specified.

Table 4. Electrical characteristics

	Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
	I <sub>CBO</sub>	Collector cut-off current (I <sub>E</sub> = 0)	V <sub>CB</sub> = 30 V V <sub>CB</sub> = 30 V; T <sub>C</sub> = 100 °C			10 100	μ <b>Α</b> μ <b>Α</b>
	I <sub>EBO</sub>	Emitter cut-off current (I <sub>C</sub> = 0)	V <sub>EB</sub> = 6 V			10	μA
	V <sub>(BR)CEO</sub> (1)	Collector-emitter breakdown voltage (I <sub>B</sub> = 0)	I <sub>C</sub> = 10 mA	30		316	V
	V <sub>(BR)CBO</sub>	Collector-base breakdown voltage (I <sub>E</sub> = 0)	Ι <sub>C</sub> = 100 μΑ	45	9.0		V
	V <sub>(BR)EBO</sub>	Emitter-base breakdown voltage $(I_C = 0)$	Ι <sub>Ε</sub> = 100 μΑ	6			٧
	V <sub>CE(sat)</sub> (1)	Collector-emitter saturation voltage	$\begin{split} I_C &= 0.5 \text{ A} & I_B &= 5 \text{ mA} \\ I_C &= 2 \text{ A} & I_B &= 50 \text{ mA} \\ I_C &= 5 \text{ A} & I_B &= 0.25 \text{ A} \\ I_C &= 6 \text{ A} & I_B &= 0.25 \text{ A} \\ I_C &= 8 \text{ A} & I_B &= 0.4 \text{ A} \\ I_C &= 10 \text{ A} & I_B &= 0.5 \text{ A} \end{split}$		0.7 1 1.2	0.15 0.35 0.7	V V V V
	V <sub>BE(sat)</sub> (1)	Base-emitter saturation voltage	$I_C = 2 A$ $I_B = 50 mA$ $I_C = 6 A$ $I_B = 0.25 A$		1.2	1.1	V V
05018	h <sub>FE</sub> <sup>(1)</sup>	DC current gain	$\begin{split} & I_{C} = 10 \text{ mA} & V_{CE} = 1 \text{ V} \\ & I_{C} = 500 \text{ mA} & V_{CE} = 1 \text{ V} \\ & I_{C} = 5 \text{ A} & V_{CE} = 1 \text{ V} \\ & I_{C} = 5 \text{ A} & V_{CE} = 1 \text{ V} \\ & T_{c} = 100 \text{ °C} & \\ & I_{C} = 8 \text{ A} & V_{CE} = 1 \text{ V} \\ & I_{C} = 10 \text{ A} & V_{CE} = 1 \text{ V} \end{split}$	120 100 70	200 200 100 100 55 35	300	
	t <sub>d</sub> t <sub>r</sub> t <sub>s</sub> t <sub>f</sub>	Resistive load Delay time Rise time Storage time Fall time	$I_C = 3 \text{ A}$ $V_{CC} = 20 \text{ V}$ $I_{B1} = -I_{B2} = 60 \text{ mA}$ see <i>Figure 8</i>		180 160 250 80	220 210 300 100	ns ns ns

<sup>1.</sup> Pulse test: pulse duration  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %

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Electrical characteristics STD878, STN878

## 2.1 Electrical characteristics (curves)

Figure 2. DC current gain ( $V_{CE} = 1 \text{ V}$ ) Figure 3. DC current gain ( $V_{CE} = 3 \text{ V}$ )

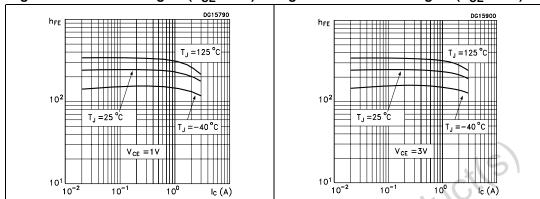


Figure 4. Collector-emitter saturation Figure voltage

Figure 5. Base-emitter saturation voltage

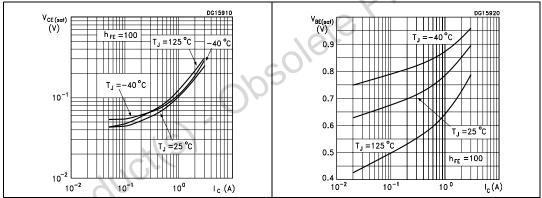
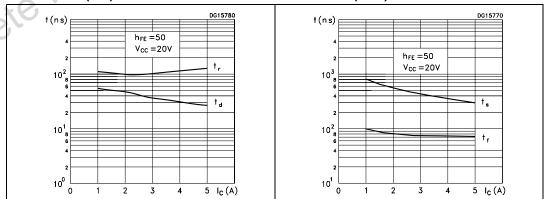
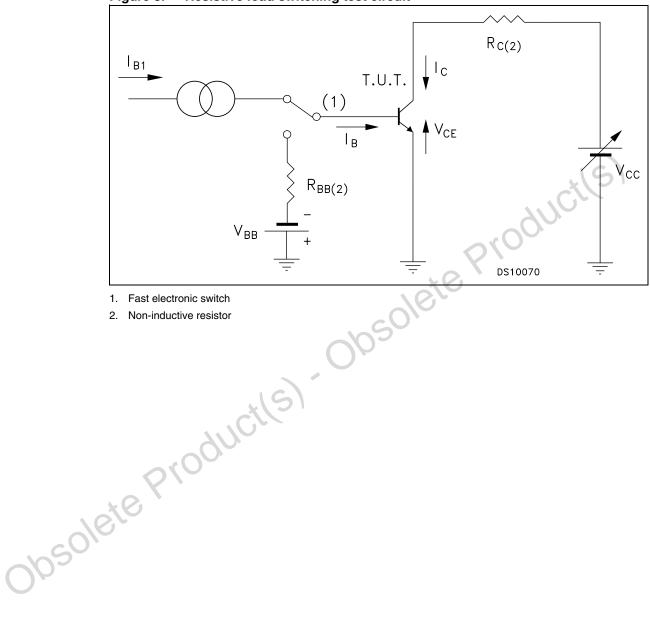


Figure 6. Resistive load switching time Figure 7. Resistive load switching time (ON) (OFF)



#### 2.2 **Test circuits**

Resistive load switching test circuit Figure 8.



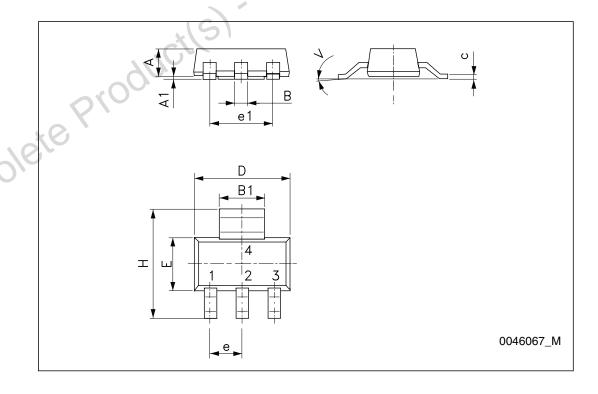
## 3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: <a href="https://www.st.com">www.st.com</a>. ECOPACK<sup>®</sup> is an ST trademark.

Obsolete Product(s). Obsolete Product(s)

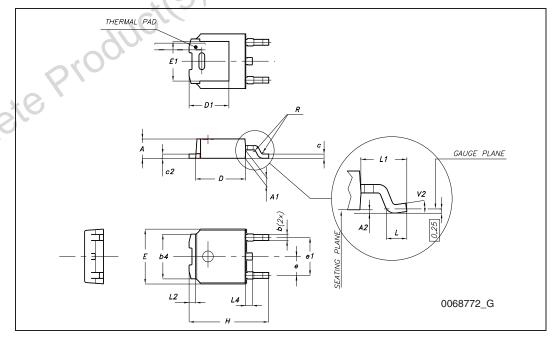
#### SOT-223 mechanical data

Dim	mm.				
Dim.	Min.	Тур.	Max.		
Α			1.80		
A1	0.02		0.1		
В	0.60	0.70	0.85		
B1	2.90	3.00	3.15		
С	0.24	0.26	0.35		
D	6.30	6.50	6.70		
е		2.30	(0		
e1		4.60			
Е	3.30	3.50	3.70		
Н	6.70	7.00	7.30		
V		70-	10 °		



TO-252	(DPAK)	mechanical	data
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DIM.	mm.			
DIIVI.	min.	typ	max.	
А	2.20		2.40	
A1	0.90		1.10	
A2	0.03		0.23	
b	0.64		0.90	
b4	5.20		5.40	
С	0.45		0.60	
c2	0.48		0.60	
D	6.00		6.20	
D1		5.10	70	
E	6.40		6.60	
E1		4.70		
е		2.28		
e1	4.40	*6	4.60	
Н	9.35	10,0	10.10	
L	1			
L1		2.80		
L2		0.80		
L4	0.60	) -	1	
R		0.20		
V2	0 °		8 °	



STD878, STN878 Revision history

# 4 Revision history

Table 5. Document revision history

Date	Revision	Changes	
21-Aug-2007	1	Initial release.	
30-Aug-2010	2	Inserted STD878T4 order code Table 1 on page 1.	

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