## STEVAL-ILL067V1

## Six-channel ALED7707-based LED driver with embedded boost converter for automotive interior lighting and TFT backlighting

Data brief



## Features

- Wide DC input voltage (6 V-32 V)
- $\quad$ Six 70 mA output channels (capable of 30 mA to 85 mA each)
- PWM brightness control ( $10 \mu \mathrm{~s}$ minimum dimming on-time)
- Up to 10 white LEDs per channel (36 V OVP threshold)
- $\quad>90 \%$ efficiency $\left(\mathrm{V}_{\mathrm{IN}}=12 \mathrm{~V}, \mathrm{~V}_{\text {Boost }}=30 \mathrm{~V}\right.$, $\mathrm{f}_{\mathrm{sw}}=1 \mathrm{MHz}$, no snubber)
- On-board input filter for conducted EMI reduction
- RoHS compliant
- All automotive-grade components


## Description

The purpose of this evaluation board is to provide an application example of a six-channel, mediumcurrent LED driver using the ALED7707 chip. The monolithic boost converter provides the required LED supply voltage starting from a single supply rail, while the brightness of the LED strings connected to the six outputs is controlled through a PWM signal. Open LED and LED short-circuit fault conditions are detected and managed.

The board has been designed to provide a compact reference solution for automotive applications involving several LEDs arranged in multiple strings (e.g. interior lighting, infotainment LCD backlighting, etc.).

## 1

 Board description and efficiency measurementFigure 1: Basic connection of the STEVAL-ILL067V1 evaluation board


For a quick evaluation of the STEVAL-ILL067V1 performance, just connect a DC power supply (e.g. 12 V \& 3 A current capability) between the VIN (J1) and GND (J2) terminals and a set of suitable LED strings between the $\mathrm{V}_{\text {вооst }}(\mathrm{J} 3) \&$ ROW1-ROW6 (J10 through J15) terminals. The SW1 \& SW2 jumpers should be both set in the lower postion, while the SW4 jumper should be in the right position (FSW \& MODE pins forced high, DIM pin high, see Table 3 for details) as per default setting. As soon as the supply input is powered, the EN pin (floating) is internally pulled-up, a soft-start sequence takes place and the LED strings are powered at full-brightness. By pressing SW3 (EN pin tied low), the device shutsdown. This pushbutton allows to easily reset the device in case a latched turn-off occurs as
a consequence of a faulty condition (LED short-circuit, open string). To evaluate the PWM dimming capability, simply connect a pulse generator between the DIM terminal and ground (see figure 1) and remove the SW4 jumper to avoid any conflict between the external PWM signal and the +5 V rail. The frequency of the PWM signal should be in the $100 \mathrm{~Hz}-20 \mathrm{kHz}$ range, with a 10us minimum pulse duration and a 3.0 V to 5 V amplitude.

Figure 2: STEVAL-ILL067V1 evaluation board PCB, top view


Figure 3: STEVAL-ILL067V1 evaluation board PCB, bottom view


Table 1: STEVAL-ILL067V1 connectors

$\left.$| Connector | Name | Function |
| :--- | :--- | :--- |
| J1 | VIN+ | Power supply input, positive terminal |
| J2 | GND | Ground. This terminal has to be preferred as return for the power supply <br> input. |
| J3 | VBOOST | Boost converter output. Connect this terminal to the common anode of <br> the LED strings to be driven. |
| J4 | GND | Quiet ground terminal. To be used as reference ground for all control <br> signals. |
| J5 | FAULT | Switching frequency synchronization output. A synchronization clock is <br> available at this pin. |
| J6 | Faulty condition indicator. Open drain output, tied low by the device <br> when a faulty condition is detected. Also used to drive the D2 LED. |  |
| J7 | EN | PWM dimming control input. The output current generators are activated <br> according to this pin to perform a PWM brightness control of the LEDs. |
| J8 | FSW | Device Enable input. Internally pulled-up. If tied low the device turns-off. |
| S9 | Switching frequency synchronization input. An external clock (30\% max <br> duty-cycle) can be provided at this terminal to synchronize the boost <br> converter. |  |
| J10 | ROW1 | ROW2 | | Output terminals (channels). A low-side current generator is connected to |
| :--- | \right\rvert\, | each output. Connect to the kathode of the LED strings. Unused channels |
| :--- |
| can be left floating. |

Board description and efficiency measurement

| Connector | Name | Function |
| :--- | :--- | :--- |
| J14 | ROW5 |  |
| J15 | ROW6 |  |

Table 2: STEVAL-ILL067V1 test points

| Test point | Function |
| :--- | :--- |
| AVCC | +5 V LDO output monitor (device supply rail). |
| COMP | COMP pin monitor (output of the trans-conductance amplifier of the control loop). |

Table 3: STEVAL-ILL067V1 jumpers and switches

| Jumper / <br> switch | Function |
| :--- | :--- |
| SW1 | FSW pin assignment. When the jumper is set in the upper position (FSW to R5), the <br> switching frequency of the boost converter is set by R5 (1 MHz). In the lower position <br> (FSW to VCC), the FSW pin is tied high and the 630 kHz fixed switching frequency is <br> set. In case an external synchronization clock has to be applied at the FSW pin, this <br> jumper must be in the upper position. |
| SW2 | MODE pin setting. This jumper allows to set high (VCC) or low (GND) the MODE pin <br> in order to select the desired LED fault management. I case some channels are not <br> used, the MODE pin must be set high. |
| SW3 | EN low pushbutton. When pressed the EN pin (internally pulled-up) is tied to ground. <br> Used to restart the device in case a latched turn-off due to a faulty condition. |
| SW4 | DIM pin setting. If the jumper is set in the right position (VCC), the DIM pin is held <br> high and the LEDs are driven at full-brightness as soon as the EN pin goes high. If the <br> jumper is set in the left position (D3), the EN pin is pulled-down by R12 and activated <br> by the PWM signal applied at the DIM pin through D3. This way a single-wire control <br> is achieved (automatic device turn-on as soon as PWM dimming is applied). Note:D3 <br> (BAT54K) \& R12 (220k) are natively not mounted. |

Figure 4: STEVAL-ILL067V1 efficiency (fsw=1 MHz)


Figure 5: STEVAL-ILL067V1 efficiency with and w/o the R13-C14 snubber (fsw=1 MHz)


2 Schematic diagram

Figure 6: STEVAL-ILL067V1 circuit schematic


## 3 Revision history

Table 4: Document revision history

| Date | Version | Changes |
| :--- | :--- | :--- |
|  | 12 -Jun-2015 | 1 |

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