

White LED power supply for large display backlight

Features

- Inductor switches boost controller
- PFM mode control
- High efficiency over wide range of input voltage from 3.0 V to 5.5 V
- Over voltage protection with automatic restart
- Adjustable peak current limit
- Enable pin with possibility of PWM dimming control
- Low shutdown current < 1 µA</p>
- Small external component
- Good load and line regulation
- QFN8 3 x 3 mm

Applications

- White LED supply for LCD backlight
- Mobile phone
- PDA and organizers
- Any handsets powered form 3.0 V to 5.5 V

Description

STLD40D is a boost converter that operates from 3.0 V to 5.5 V. It can provide an output voltage as high as 37 V and can drive up to 10 white LEDs in series. The converter is a PFM (pulse frequency modulation) inductor switch that can work in discontinuous (DCM) mode operation. A minimum OFF time of the embedded boost switch T_{SW} is fixed internally and allows limiting the switching frequency. The output current capability is 20 mA with an output voltage of 37 V. The regulation is done by sensing the LED current through the



resistor R_{LED} . The device can be turned ON/OFF through the logic enable signal pin EN. By applying a low frequency PWM signal the LEDs can be dimmed. The maximum peak inductor current can be programmed by connected a resistor R_{SET} to the pin R_{SET} .

Table 1. Device summary

Order code	Package	Packaging	
STLD40DPUR	QFN8 (3 x 3 mm)	3000 parts per reel	

Contents STLD40D

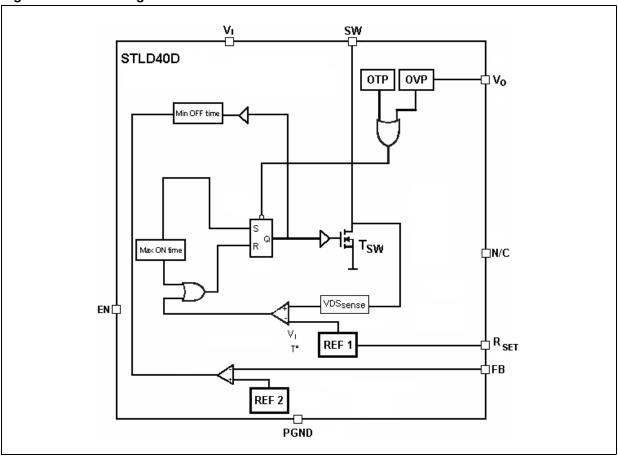
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STLD40D Block diagram

1 Block diagram

Figure 1. Block diagram



Pin configuration STLD40D

2 Pin configuration

Figure 2. Pin connections (top through view)

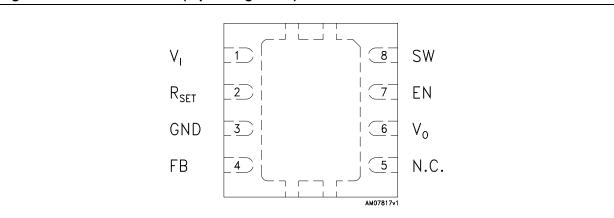


Table 2. Pin description

Pin n°	Symbol	Note	
1	VI	Supply voltage	
2	R _{SET}	Peak inductor current adjust	
3	GND	Analog ground	
4	FB	Feedback for the LED current regulation	
5	N.C.	Not connected	
6	V _O	Output voltage for LED supply	
7	EN	IC enable signal	
8	SW	Boost switch drain	
	PGND	Power ground	

STLD40D Maximum ratings

3 Maximum ratings

Table 3. Absolute maximum ratings

Symbol	Parameter	Value	Unit
VB _{SW} , VB _O	Breakdown voltage at V _O and SW pin	44	V
EN	Maximum voltage applied on pin EN	V _I	V
V _I	Supply voltage range	6	V
V _{ESD}	ESD ratings - (HBM MIL STD 883C)	2	kV
T _{OP}	Operating temperature	- 40 to 85	°C
T _{STG}	Storage temperature range	- 65 to 150	°C

Table 4. Thermal data

Symbol	Parameter	Value	Unit
R _{thJA}	Thermal resistance junction-ambient	52	°C/W

Electrical characteristics STLD40D

4 Electrical characteristics

 T_J = - 40 °C to 85 °C, V_I = 3.6 V, V_{EN} = 3 V, C_I = 2.2 $\mu\text{F},\,C_O$ = 4.7 $\mu\text{F},\,L$ = 4.7 $\mu\text{H},\,R_{LED}$ = 8 Ω V $_O$ = 32 V, typ. values @ 25 °C, unless otherwise specified.

Table 5. Electrical characteristics

	Test conditions	Min.	Тур.	Max.	Unit
Input voltage range		3.0		5.5	V
Output voltage range		VI		37	V
Regulated output current	V _O = 36 V (10 white LEDs)	20			mA
Stand by ourrant	$V_{EN} = Low, V_{I} = 3.6 V, T_{J} = 25 °C$		1	3	
$V_{EN} = Low, V_I = 3 V to 4.2 V$				10	μA
Quicocont current	V _I = 3 V to 4.2 V, T _J = 25 °C		0.4	0.8	mA
Quiescent current	V _I = 5.5 V, T _J = 25 °C		0.8	1.2	IIIA
	V _I = 5.5 V, I = 100 mA		0.4		
Boost switch R _{DSON}	V _I = 4.2 V, I = 100 mA		0.4		Ω
	V _I = 3.0 V, I = 100 mA		0.5		
BVDS breakdown voltage		38			V
Maximum peak inductor limit adjust range (1)	R_{SET} = 12 kΩ to 100 kΩ	0.2		1	Α
Maximum peak inductor limit when R _{SET} = V _I ⁽¹⁾	V _I = 3 V to 5.5 V	0.75		1.1	Α
Feedback voltage	V _I = 3.6 V	130	165	200	mV
Line feedback voltage	V _I = 3 V to 5.5 V		5	35	mV
Maximum ON time	V _I = 4.2 V		5.5		μs
Minimum OFF time	V _I = 4.2 V		250		ns
Efficiency at V _I = 3.0 V _{DC} ⁽¹⁾	$I_{O} = 20 \text{ mA}, V_{O} = 36 V_{DC}$	75			0/
Efficiency at V _I = 5.5 V _{DC} ⁽¹⁾	$I_{O} = 20 \text{ mA}, V_{O} = 36 V_{DC}$	80			%
Over voltage protection		36		42	V
Over voltage hysteresis			1.5		V
Enable input logic low	Disable Low V _{IL}			0.3	\/
Enable input logic high	Enable High V _{IH}	1.2			V
	Output voltage range Regulated output current Stand-by current Quiescent current Boost switch R _{DSON} BVDS breakdown voltage Maximum peak inductor limit adjust range (1) Maximum peak inductor limit when R _{SET} = V _I (1) Feedback voltage Line feedback voltage Maximum ON time Minimum OFF time Efficiency at V _I = 3.0 V _{DC} (1) Efficiency at V _I = 5.5 V _{DC} (1) Over voltage protection Over voltage hysteresis Enable input logic low	Dutput voltage range V _O = 36 V (10 white LEDs) Regulated output current $V_{EN} = Low, V_{I} = 3.6 V, T_{J} = 25 °C$ Stand-by current $V_{EN} = Low, V_{I} = 3 V to 4.2 V$ Quiescent current $V_{I} = 3.5 V, T_{J} = 25 °C$ $V_{I} = 5.5 V, T_{J} = 25 °C$ $V_{I} = 5.5 V, I = 100 mA$ Boost switch R _{DSON} $V_{I} = 3.0 V, I = 100 mA$ BVDS breakdown voltage $V_{I} = 3.0 V, I = 100 mA$ Maximum peak inductor limit adjust range (1) $V_{I} = 3.0 V to 5.5 V$ Maximum peak inductor limit when $R_{SET} = V_{I}$ (1) $V_{I} = 3 V to 5.5 V$ Feedback voltage $V_{I} = 3.6 V$ Line feedback voltage $V_{I} = 3.6 V$ Maximum ON time $V_{I} = 4.2 V$ Minimum OFF time $V_{I} = 4.2 V$ Efficiency at $V_{I} = 3.0 V_{DC}$ (1) $I_{O} = 20 mA, V_{O} = 36 V_{DC}$ Efficiency at $V_{I} = 5.5 V_{DC}$ (1) $I_{O} = 20 mA, V_{O} = 36 V_{DC}$ Over voltage protection Disable Low V_{IL}	Output voltage range V_1 Regulated output current $V_0 = 36 \text{ V } (10 \text{ white LEDs})$ 20 Stand-by current VEN = Low, $V_1 = 3.6 \text{ V}$, $T_3 = 25 \text{ °C}$ V _I = 3 V to 4.2 V, $T_3 = 25 \text{ °C}$ V _I = 3.6 V, $T_3 = 25 \text{ °C}$ V _I = 5.5 V, $I = 100 \text{ mA}$ V _I = 5.5 V, $I = 100 \text{ mA}$ V _I = 4.2 V, $I = 100 \text{ mA}$ Waximum peak inductor limit adjust range (1) Maximum peak inductor limit when $R_{SET} = V_1$ (1) V _I = 3 V to 5.5 V 0.75 Feedback voltage V _I = 3.6 V Jan V to 5.5 V Waximum ON time V _I = 4.2 V Winimum OFF time Efficiency at $V_1 = 3.0 \text{ V}_{DC}$ (1) I _O = 20 mA, $V_O = 36 \text{ V}_{DC}$ 75 Efficiency at $V_1 = 5.5 \text{ V}_{DC}$ (1) Disable Low V_{IL}	Dutput voltage range $V_{O} = 36 \text{ V} (10 \text{ white LEDs})$ $V_{O} = 36 \text{ V} (10 \text{ white LEDs})$ Begulated output current $V_{O} = 36 \text{ V} (10 \text{ white LEDs})$ $20 \text{ MeV}_{O} = 36 \text{ V} (10 \text{ white LEDs})$ Stand-by current $V_{EN} = \text{Low}, V_{I} = 3 \text{ V to } 4.2 \text{ V}$ $V_{I} = 3 \text{ V to } 4.2 \text{ V}$ Quiescent current $V_{I} = 3.0 \text{ V} = 3.0 \text{ V}_{I} = 25 \text{ °C}$ $0.4 \text{ V}_{I} = 3.0 \text{ V}_{I} = 25 \text{ °C}$ Boost switch RDSON $V_{I} = 5.5 \text{ V}, I = 100 \text{ mA}$ $0.4 \text{ V}_{I} = 3.0 \text{ V}_{I} = 100 \text{ mA}$ BOOST switch RDSON $V_{I} = 3.0 \text{ V}, I = 100 \text{ mA}$ $0.4 \text{ V}_{I} = 3.0 \text{ V}_{I} = 100 \text{ mA}$ BOOST switch RDSON $V_{I} = 3.0 \text{ V}, I = 100 \text{ mA}$ $0.4 \text{ V}_{I} = 3.0 \text{ V}_{I} = 100 \text{ mA}$ BOOST switch RDSON $V_{I} = 3.0 \text{ V}, I = 100 \text{ mA}$ $0.4 \text{ V}_{I} = 3.0 \text{ V}_{I} = 100 \text{ mA}$ BOOST switch RDSON $V_{I} = 3.0 \text{ V}_{I} = 100 \text{ mA}$ $0.4 \text{ V}_{I} = 3.0 \text{ V}_{I} = 100 \text{ mA}$ BOOST switch RDSON $V_{I} = 3.0 \text{ V}_{I} = 100 \text{ mA}$ $0.4 \text{ V}_{I} = 100 \text{ mA}$ BOOST switch RDSON $V_{I} = 3.0 \text{ V}_{I} = 100 \text{ mA}$ $0.2 \text{ V}_{I} = 100 \text{ mA}$ BOOST switch RDSON $V_{I} = 3.0 \text{ V}_{I} = 100 \text{ mA}$ $0.2 \text{ V}_{I} = 100 \text{ mA}$ BOOST	Dutput voltage range V _I 37 Regulated output current V _O = 36 V (10 white LEDs) 20 Stand-by current $V_{EN} = Low, V_I = 3.6 \text{ V}, T_J = 25 \text{ °C}$ 1 3 Quiescent current $V_{I} = 3 \text{ V to } 4.2 \text{ V}$ 10 0.8 1.2 Quiescent current $V_{I} = 3 \text{ V to } 4.2 \text{ V}, T_{J} = 25 \text{ °C}$ 0.4 0.8 1.2 Quiescent current $V_{I} = 5.5 \text{ V}, T_{J} = 25 \text{ °C}$ 0.8 1.2 Quiescent current $V_{I} = 5.5 \text{ V}, T_{J} = 25 \text{ °C}$ 0.4 0.8 Quiescent current $V_{I} = 5.5 \text{ V}, T_{J} = 25 \text{ °C}$ 0.4 0.4 Quiescent current $V_{I} = 5.5 \text{ V}, T_{J} = 25 \text{ °C}$ 0.4 0.8 Quiescent current $V_{I} = 5.5 \text{ V}, T_{J} = 25 \text{ °C}$ 0.4 0.8 Quiescent current $V_{I} = 3.0 \text{ V} = 25 \text{ °C}$ 0.4 0.4 Quiescent current $V_{I} = 5.5 \text{ V}, T_{J} = 25 \text{ °C}$ 0.4 0.4 Quiescent current $V_{I} = 3.0 \text{ V}, I = 100 \text{ mA}$ 0.4 0.4 Quiescent current $V_{I} = 2.0 \text{ V}, I = 100 \text{ mA}$

^{1.} Guaranteed by design.

5 Functional description

5.1 Boost controller

The STLD40D is a boost converter operating in PFM (pulsed frequency modulation) mode.

The converter monitors the LED current through the resistor R_{LED} and when the feedback voltage falls below the reference voltage REF2, the boost switch T_{SW} turns ON and the current ramps up. The inductor current is measured by sensing the temperature compensated drain voltage of the boost MOSFET. The boost turns off when its drain voltage reaches the internally reference REF1, the main switch remains off until the minimum off time (250 ns typical) has passed and the feedback voltage is below the reference again. A maximum ON time of 4 μs typical prevents the switch T_{SW} to stay ON during a too long period of time.

5.2 Adjustable peak inductor current limit

The peak inductor current is monitored by sensing the drain voltage of the switch T_{SW} . Since it exceeds the temperature compensated and supply voltage compensated reference REF1, the RS flip flop is reset and T_{SW} is turned OFF. By connecting a resistance to the pin R_{SET} the peak current limit can be adjusted from 200 mA to 1 A. When R_{SET} resistor value is about 12 $k\Omega$ is connected directly to GND, the default value is 1 A.

5.3 Enable

The ENABLE pin is a high logic input signal and allows turning on/off the controller without cutting the input voltage from the boost regulator circuit. The pin ENABLE can be used to dim the LED by applying a low frequency PWM signal.

5.4 **OVP**

If the regulation loop is cut, there is no signal at the feedback pin, the PFM controller will then continue to switch without control and generate an output voltage at the SW, and V_O pin exceeding the breakdown value V_{BSW} , and V_{BO} .

The over voltage protection (OVP) senses the voltage at the $V_{\rm O}$ pin. When the voltage exceed 38 V min. the controller is automatically turned OFF.

A hysteresis control allows the device restarting automatically since the output voltage drops down of 1.5 V.

Typical application STLD40D

6 Typical application

Figure 3. Application circuit

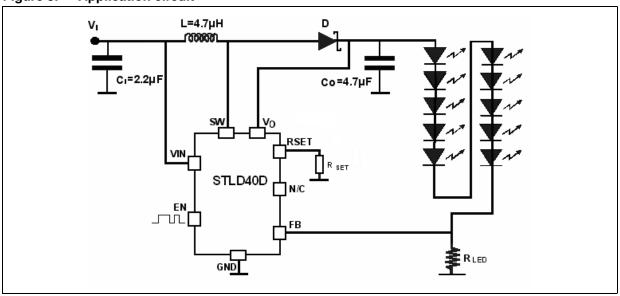


Table 6. External component proposal (see Figure 3)

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
		VRRM	40			V
	Boost schottky diode	V_F at I_F = 300 mA, T_J = 25 °C			0.5	V
Б		I_R at V_R = 10 V, T_J = 25 °C			30	μΑ
D		VRRM	40			V
	STPS1L40M	V_F at $I_F = 1$ A, $T_J = 25$ °C			0.46	V
		I_R at V_R = 20 V, T_J = 25 °C			21	μΑ
R _{LED}	Feedback LED current regulation resistor	I _{LED} = 20 mA		8		Ω
R _{SET}	Current peak setting resistor	I _{PEAK} = 0.2 A to 1 A	12		100	kΩ
C _I	Input filtering capacitor	Ceramic Type		2.2		μF
	Output capacitance: ceramic low ESR	Capacitance	4.7			μF
Co		Voltage	35			V
		ESR			1.6	Ω
		Inductance			4.7	μΗ
L	Boost inductor (height < 2 mm)	DCR			1	Ω
		I _{SATRSET} = GND			1	Α

Note:

The external components proposal should be considered as a design reference guide. The performances mentioned in the electrical characteristics table are not guaranteed for all the possible electrical parameters of the components included in this list. On an other hand the operation of STLD40D is not limited with the use of components included in this list.

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7 Typical performance characteristics

 V_I = 3.6 V, V_{EN} = 3 V, C_I = 2.2 μF, C_O = 4.7 μF, L = 4.7 μH, R_{LED} = 8 Ω V_O = 32 V, 10 W LEDs load (V_O = 32 V) typ. values @ 25 °C, unless otherwise specified.

Figure 4. I_{LED} vs. R_{LED}

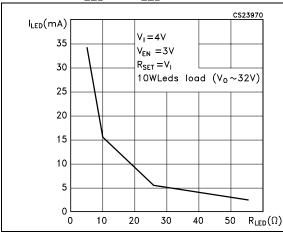


Figure 5. I_{LED} vs. V_I

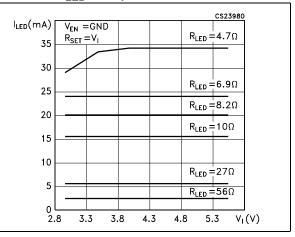


Figure 6. I_Q vs. temperature

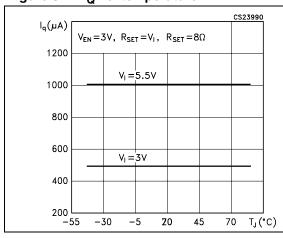


Figure 7. I_{SD} vs. V_I

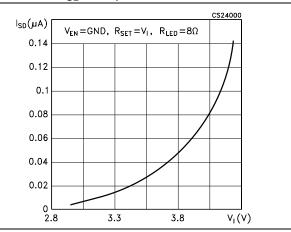


Figure 8. R_{DSON} vs.V_I

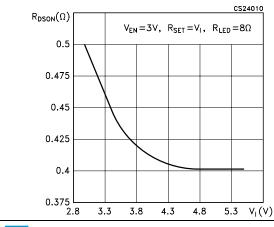
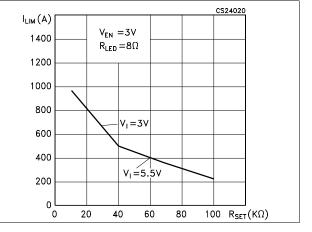


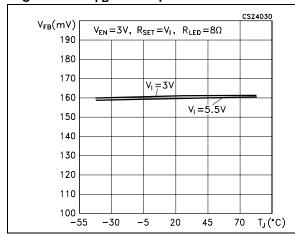
Figure 9. I_{LIMIT} vs. R_{SET}



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Figure 10. V_{FB} vs. temperature

Figure 11. V_{EN} vs. temperature



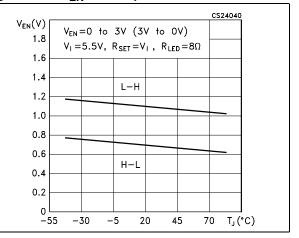
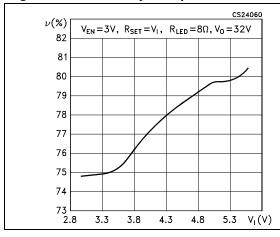


Figure 12. Efficiency vs. V_I

Figure 13. V_{OVP} vs. temperature



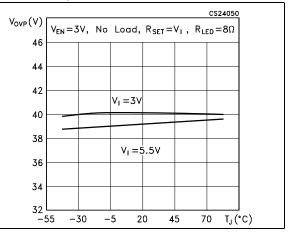
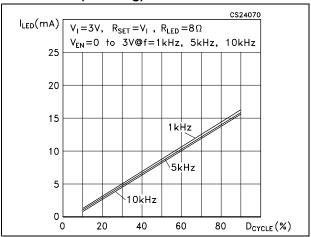


Figure 14. I_{LED} vs. duty cycle EN pin (dimming)



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8 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: www.st.com. ECOPACK[®] is an ST trademark.

Table 7. QFN8 (3 x 3 mm) mechanical data

Dim		mm.				
Dim.	Min.	Тур.	Max.			
Α	0.80	0.90	1.00			
A1	0	0.02	0.05			
A3		0.20				
b	0.25	0.30	0.35			
D	2.85	3.00	3.15			
D2	2.49	2.64	2.74			
E	2.85	3.00	3.15			
E2	1.75	1.90	2.00			
е		0.65				
L	0.30	0.40	0.50			

BOTTOM VIEW D2 PIN 1 ID EXPOSED PAD 0.44 0.23 т 5 8 6 b (8x) // 0.1 C -*A3* SEATING PLANE A 1 C 0.08 C LEADS COPLANARITY 5 8 6 E/23 PIN 1 ID -D/2-TOP VIEW 8057023_B

Figure 15. Drawing dimension QFN8 (3 x 3 mm)

Tape & reel QFNxx/DFNxx (3x3) mechanical data

Dim.		mm.		inch.		
Dilli.	Min.	Тур.	Max.	Min.	Тур.	Max.
А			180			7.087
С	12.8		13.2	0.504		0.519
D	20.2			0.795		
N	60			2.362		
Т			14.4			0.567
Ao		3.3			0.130	
Во		3.3			0.130	
Ko		1.1			0.043	
Po		4			0.157	
Р		8			0.315	

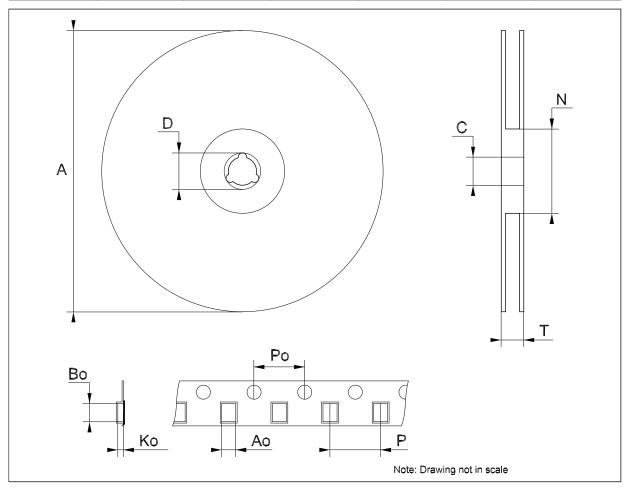


Figure 16. QFN8 (3x3) footprint recommended data

STLD40D Revision history

9 Revision history

Table 8. Document revision history

Date	Revision	Changes
20-Mar-2006	1	Initial release.
04-Apr-2006	2	Add R _{SET} in table 4 and fig 2 has been updated.
27-Feb-2009	3	Modified mechanical data.
03-Mar-2009	4	Modified packaging Table 1 on page 1.
11-Mar-2009	5	Modified: Figure 2 on page 4 and added Figure 16 on page 14.
01-Jul-2010	6	Modified: Table 7 on page 11, Figure 15 on page 12 and Figure 16 on page 14.
31-May-2011	7	Modified: Table 6 on page 8.

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