

STN93003

High voltage fast-switching PNP power transistor

Features

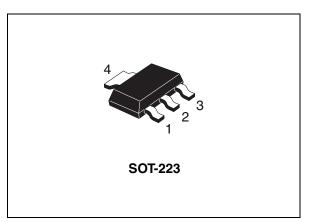
- High voltage capability
- Very high switching speed

Application

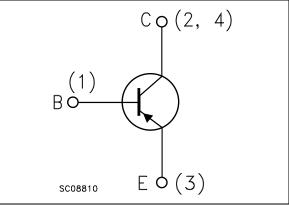
Electronics ballasts for fluorescent lighting

Description

The device is manufactured using high voltage multi-epitaxial planar technology for high switching speeds and high voltage capability. It uses a cellular emitter structure with planar edge termination to enhance switching speeds while maintaining the wide RBSOA. The STN93003 is expressly designed for a new solution to be used in compact fluorescent lamps, where it is coupled with the STN83003, its complementary NPN transistor.







Part Number	Marking	Package	Packaging
STN93003	N93003	SOT-223	Tape and reel

Doc ID 12329 Rev 2

Contents

1	Electrical ratings			
2	Electrical characteristics			
	2.1 Electrical characteristics (curves) 5			
	2.2 Test circuits			
3	Package mechanical data 8			
4	Revision history			



1 Electrical ratings

Table 2. Absolute maximum ratings	Table 2.	Absolute	maximum	ratings
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Symbol	Parameter	Value	Unit
V _{CES}	Collector-emitter voltage (V _{BE} = 0)	-500	V
V _{CEO}	Collector-emitter voltage ($I_B = 0$)	-400	V
V _{EBO}	Emitter-base voltage ($I_C = 0$, $I_B = 0.75$ A, $t_P < 10 \ \mu$ s)	V _{(BR)EBO}	v
Ι _C	Collector current	-1.5	А
I _{CM}	Collector peak current (t _P < 5 ms)	-3	А
Ι _Β	Base current	-0.75	А
I _{BM}	Base peak current (t _P < 5 ms)	-1.5	А
P _{TOT}	Total dissipation at $T_a = 25 \ ^{\circ}C$	1.6	W
T _{STG}	Storage temperature	-65 to 150	°C
TJ	Max. operating junction temperature	150	°C

Table 3.Thermal data

Symbol	Parameter	Value	Unit
R _{thJA}	Thermal resistance junction-ambient ⁽¹⁾ max	78	°C/W

1. Device mounted on PCB area of 1 cm².



2 Electrical characteristics

 $T_{case} = 25$ °C unless otherwise specified.

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
1	Collector cut-off current	V _{CE} = -500 V			-1	mA
ICES	(V _{BE} = 0)	$V_{CE} = -500 V$ $T_{C} = 125 °C$			-5	mA
V _{(BR)EBO}	Emitter-base breakdown voltage (I _C = 0)	I _E = -10 mA	-5		-10	v
V _{CE(sus)} ⁽¹⁾	Collector-emitter sustaining voltage $(I_B = 0)$	I _C = -10 mA	-400			v
v (1)	Collector-emitter	$I_{\rm C} = -0.35 {\rm A}$ $I_{\rm B} = -50 {\rm mA}$			-0.5	V
V _{CE(sat)} ⁽¹⁾	saturation voltage	I _C = -0.5 A I _B = -0.1 A			-0.5	V
V _{BE(sat)} ⁽¹⁾	Base-emitter saturation voltage	I _C = -0.5 A I _B = -0.1 A			-1	V
		$I_{C} = -10 \text{ mA}$ $V_{CE} = -5 \text{ V}$	10			
h _{FE}	DC current gain	$I_{\rm C} = -0.35 \text{ A}$ $V_{\rm CE} = -5 \text{ V}$		25	32	
		$I_{\rm C} = -1 \ {\rm A}$ $V_{\rm CE} = -5 \ {\rm V}$	4			
	Resistive load					
t _r	Rise time	$I_{\rm C} = -0.35 \ {\rm A}$ $V_{\rm CC} = 125 \ {\rm V}$		90		ns
t _s	Storage time	I _{B1} = - I _{B2} = -70 mA	1.5	2.2	2.9	μs
t _f	Fall time	t _P ≥ 25 μs		0.1		μs
	Inductive load	$I_{\rm C} = 0.5 {\rm A}$ $I_{\rm B1} = 0.1 {\rm A}$				
t _s	Storage time	$V_{BE(off)} = -5 V$ L = 10 mH		400		ns
t _f	Fall time	V _{Clamp} = 300 V		40		ns
E _{sb}	Avalanche energy		12			mJ

Table 4. Electrical characteristics

1. Pulse test: pulse duration \leq 300 µs, duty cycle \leq 2 %.



57

2.1 Electrical characteristics (curves)

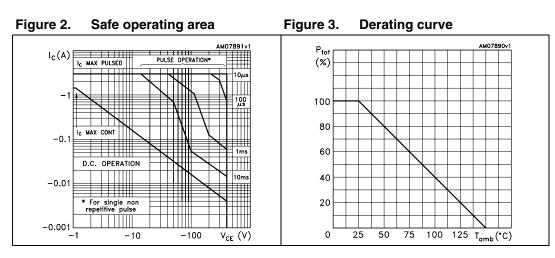
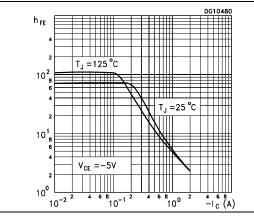
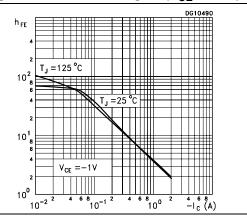
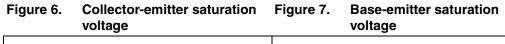
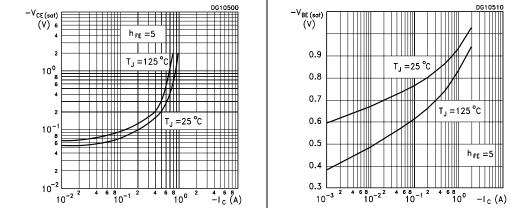


Figure 4. DC current gain ($V_{CE} = -5 V$) Figure 5. DC current gain ($V_{CE} = -1 V$)









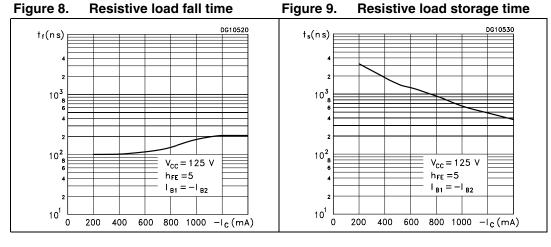
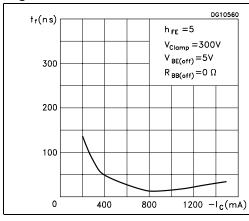


Figure 10. Inductive load fall time





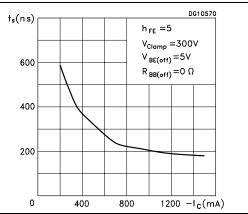
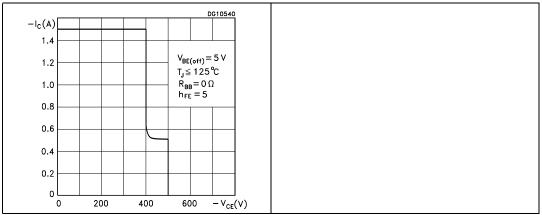


Figure 12. Reverse biased SOA





2.2 Test circuits

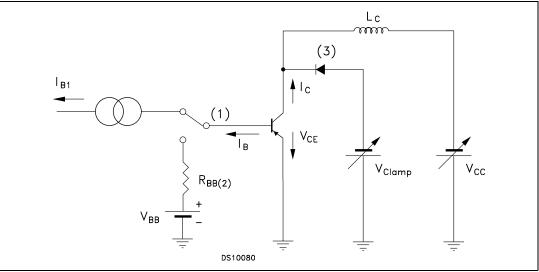
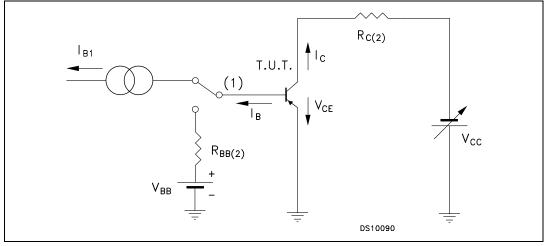


Figure 13. Inductive load switching test circuit

- 1. Fast electronic switching
- 2. Non-inductive resistor
- 3. Fast recovery rectifier

Figure 14. Resistive load switching test circuit



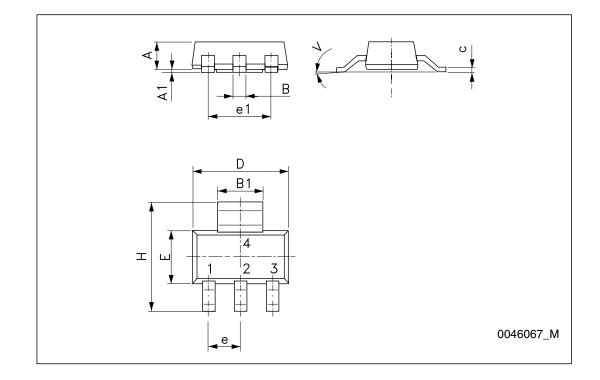
- 1. Fast electronic switching
- 2. Non-inductive resistor

3 Package mechanical data

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SOT-223 mechanical data			
Dim.		mm.	
	Min.	Тур.	Max.
A			1.80
A1	0.02		0.1
В	0.60	0.70	0.85
B1	2.90	3.00	3.15
с	0.24	0.26	0.35
D	6.30	6.50	6.70
е		2.30	
e1		4.60	
E	3.30	3.50	3.70
н	6.70	7.00	7.30
V			10 ^o



57

Doc ID 12329 Rev 2

4 Revision history

Table 5. Revision histo

Date	Revision	Changes
11-May-2006	1	Initial release.
29-Nov-2010	2	Updated package mechanical data on page 9.



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