

STP160N3LL

N-channel 30 V, 2.5 mΩ typ., 120 A STripFET™ H6 Power MOSFET in a TO-220 package

Datasheet - production data

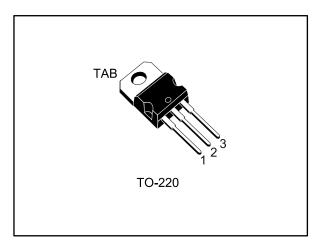
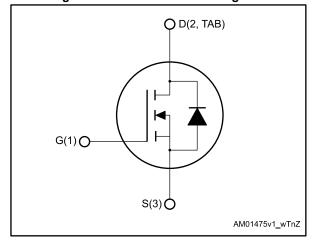


Figure 1: Internal schematic diagram



Features

Order code	V _{DS}	R _{DS(on)} max.	I _D	P _{TOT}
STP160N3LL	30 V	$3.2~\text{m}\Omega$	120 A	136 W

- Very low on-resistance
- Very low gate charge
- High avalanche ruggedness
- Low gate drive power loss

Applications

Switching applications

Description

This device is an N-channel Power MOSFET developed using the STripFETTM H6 technology with a new trench gate structure. The resulting Power MOSFET exhibits very low $R_{DS(on)}$ in all packages.

Table 1: Device summary

Order code	Marking	Package	Packing	
STP160N3LL	160N3LL	TO-220	Tube	

Contents STP160N3LL

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STP160N3LL Electrical ratings

1 Electrical ratings

Table 2: Absolute maximum ratings

Symbol	Parameter	Value	Unit	
V _{DS}	Drain-source voltage	30	V	
V_{GS}	Gate-source voltage	±20	V	
I _D ⁽¹⁾	Drain current (continuous) at T _{case} = 25 °C	120		
I _D	Drain current (continuous) at T _{case} = 100 °C	112	Α	
I _{DM} ⁽²⁾	Drain current (pulsed)	480		
Ртот	Total dissipation at T _{case} = 25 °C	136	W	
E _{AS} ⁽³⁾	Single pulse avalanche energy	150	mJ	
T _{stg}	Storage temperature	FF to 17F	°C	
Tj	Operating junction temperature			

Notes:

Table 3: Thermal data

Symbol	Parameter	Value	Unit
R _{thj-case}	Thermal resistance junction-case	1.1	9 0 AA7
R _{thj-amb}	Thermal resistance junction-ambient	62.5	°C/W

⁽¹⁾ Current is limited by package.

 $^{^{\}left(2\right) }$ Pulse width is limited by safe operating area.

 $^{^{(3)}}$ starting T_j = 25 °C, I_D = 40 A

Electrical characteristics STP160N3LL

2 Electrical characteristics

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(T_{case} = 25 °C unless otherwise specified)

Table 4: Static

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V _{(BR)DSS}	Drain-source breakdown voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30			>
	Zoro goto voltago droin	$V_{GS} = 0 \text{ V}, V_{DS} = 30 \text{ V}$			1	
I _{DSS}	Zero gate voltage drain current	$V_{GS} = 0 \text{ V}, V_{DS} = 30 \text{ V},$ $T_{case} = 125 \text{ °C}$			10	μΑ
I _{GSS}	Gate-body leakage current	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA
V _{GS(th)}	Gate threshold voltage	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1		2.5	V
В	Static drain-source on-	V _{GS} = 10 V, I _D = 60 A		2.5	3.2	mΩ
R _{DS(on)} resistance	$V_{GS} = 4.5 \text{ V}, I_D = 60 \text{ A}$		3.2	4.2	11122	

Table 5: Dynamic

Symbol	Parameter Test conditions		Min.	Тур.	Max.	Unit
C _{iss}	Input capacitance		-	3500	-	
Coss	Output capacitance	V _{DS} = 25 V, f = 1 MHz, V _{GS} = 0 V	-	400	ı	pF
C _{rss}	Reverse transfer capacitance	753 - 25 7, 1 - 1 1111 12, 763 - 5 7	-	380	-	ρ.
Qg	Total gate charge	V _{DD} = 15 V, I _D = 120 A,	-	42	ı	
Q_{gs}	Gate-source charge	V _{GS} = 4.5 V (see <i>Figure 14:</i> " <i>Gate</i>	-	9	-	nC
Q_{gd}	Gate-drain charge	charge test circuit")	-	18	-	
R_{G}	Intrinsic gate resistance	$f = 1$ MHz, $I_D = 0$ A, gate DC bias = 0 V, magnitude of alternative signal = 20 mV	-	1	-	Ω

Table 6: Switching times

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
t _{d(on)}	Turn-on delay time	$V_{DD} = 15 \text{ V}, I_D = 60 \text{ A R}_G = 4.7 \Omega,$	ı	19	ı	
t _r	Rise time	V _{GS} = 5 V (see Figure 13: "Switching times test circuit for	-	91	-	
t _{d(off)}	Turn-off delay time	resistive load" and Figure 18:	-	24.5	-	ns
t _f	Fall time	"Switching time waveform")	-	23.4	-	

Table 7: Source-drain diode

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V _{SD} ⁽¹⁾	Forward on voltage	V _{GS} = 0 V, I _{SD} = 60 A	-		1.1	V
t _{rr}	Reverse recovery time		-	28.6		ns
Q _{rr}	Reverse recovery charge	I _{SD} = 120 A, di/dt = 100 A/μs, V _{DD} = 24 V (see <i>Figure 15: "Test</i> circuit for inductive load switching	-	22.8		nC
I _{RRM}	Reverse recovery current	and diode recovery times")	-	1.6		Α

Notes:

 $^{^{(1)}}$ Pulse test: pulse duration = 300 $\mu s,$ duty cycle 1.5%.

2.1 Electrical characteristics (curves)

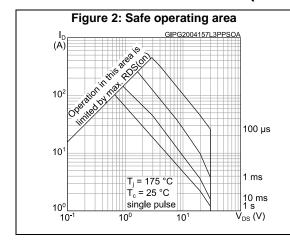


Figure 3: Thermal impedance K GIPG20041571.3PPZTH δ =0.5 δ =0.2 δ =0.1 δ =0.05 δ =0.01 δ =0.01 δ =0.01 δ =0.01 δ =0.01 δ =10.71 δ =10.5 δ single pulse δ =10.5 δ =10.1 δ =10.2 δ =10.4 δ =10.3 δ =10.4 δ =10.5 δ =10.5

Figure 5: Transfer characteristics

(A)

300

250

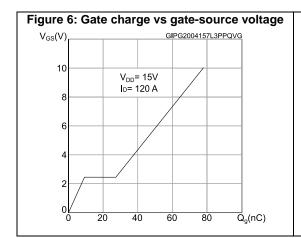
V_{DS} = 1V

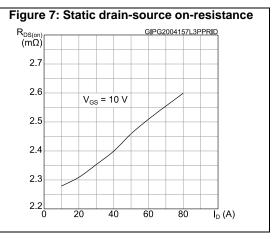
200

150

0

1 2 3 4 V_{GS}(V)





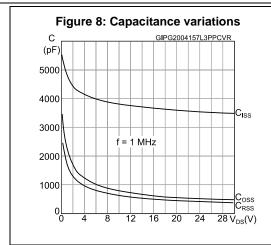


Figure 9: Normalized gate threshold voltage vs temperature

V(GS)(h)
(norm.)

1.2

I_D= 250 µA

0.6

0.4

0.2

0.55 -5 45 95 145 T_j(°C)

Figure 10: Normalized on-resistance vs temperature

R_{DS(on)} GIPG2004157L3PPRON

1.8

1.6

V_{GS}= 10 V

I_D= 40 A

1.2

1.0

0.8

0.6

-55

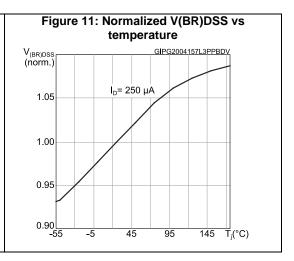
-5

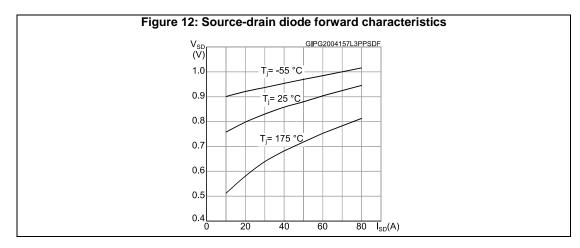
45

95

145

T_j(°C)

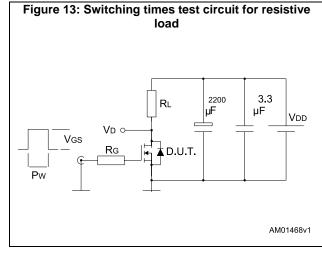




Test circuits STP160N3LL

3 Test circuits

3 Test circuits



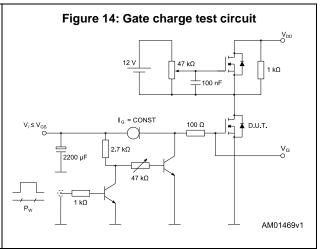


Figure 15: Test circuit for inductive load switching and diode recovery times

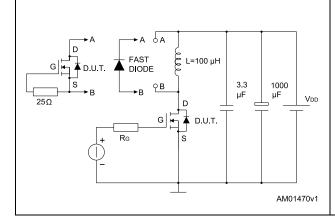
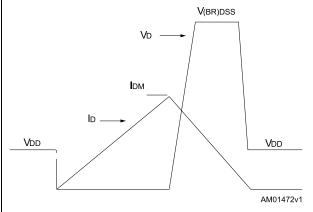
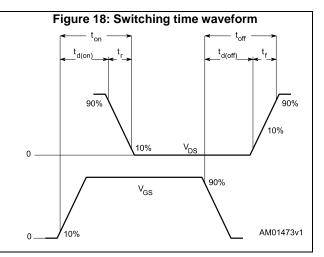


Figure 16: Unclamped inductive load test circuit

Figure 17: Unclamped inductive waveform





4 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: **www.st.com**. ECOPACK® is an ST trademark.



4.1 TO-220 type A package information

Figure 19: TO-220 type A package outline

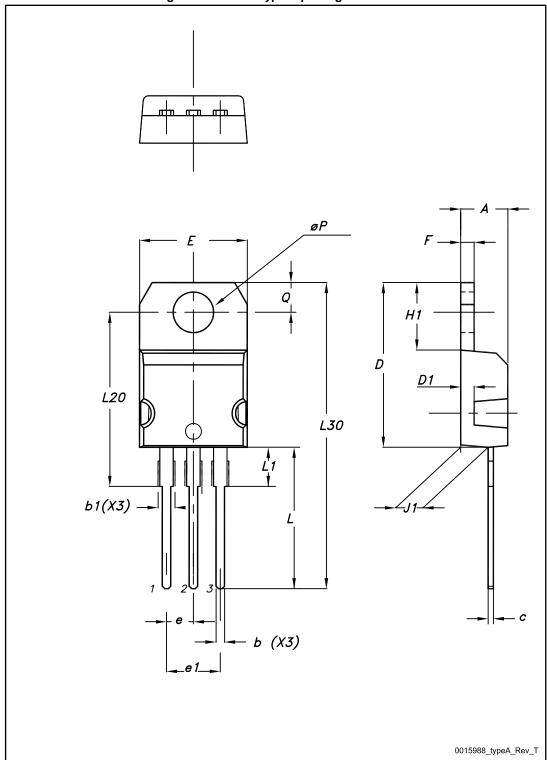


Table 8: TO-220 type A mechanical data

Dim		mm	
Dim.	Min.	Тур.	Max.
A	4.40		4.60
b	0.61		0.88
b1	1.14		1.70
С	0.48		0.70
D	15.25		15.75
D1		1.27	
Е	10		10.40
е	2.40		2.70
e1	4.95		5.15
F	1.23		1.32
H1	6.20		6.60
J1	2.40		2.72
L	13		14
L1	3.50		3.93
L20		16.40	
L30		28.90	
øΡ	3.75		3.85
Q	2.65		2.95

Revision history STP160N3LL

Revision history 5

Table 9: Document revision history

Date	Revision	Changes
31-Jul-2013	1	First release.
04-Jun-2015	2	Text edits and formatting changes throughout document In section 2 Electrical characteristics: - updated Table 4 Static - updated Table 5 Dynamic - updated Table 7 Source-drain diode - added Section 2.1 Electrical charateristics (curves) Updated and renamed Section 4 Package information (was Package mechanical data)
26-Jun-2015	3	On cover page: - updated Title and Description In Section Electrical ratings: - updated Table Absolute maximum ratings

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