

STPS1L20MF

Low drop power Schottky rectifier in flat package

Features

- Very low profile package: 0.85 mm
- Backward compatible with standard STmite footprint
- Very small conduction losses
- Negligible switching losses
- Extremely fast switching
- Low forward voltage drop for higher efficiency and extended battery life
- Low thermal resistance
- Avalanche capability specified

Description

Single Schottky rectifier suited for switch mode power supplies and high frequency dc to dc converters.

Packaged in STmite flat, this device is intended for use in low voltage, high frequency inverters, free wheeling and polarity protection applications. Due to the very small size of the package this device fits battery powered equipment (cellular, notebook, PDA's, printers) as well as chargers and PCMCIA cards.

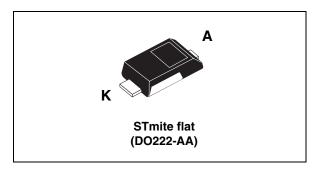


Table 1. Device summary

| Symbol | Value |
|----------------------|--------|
| I _{F(AV)} | 1 A |
| V_{RRM} | 20 V |
| T _j (max) | 150 °C |
| V _F (max) | 0.37 V |

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1 Characteristics

Table 2. Absolute ratings (limiting values)

| Symbol | Parameter | Value | Unit | |
|---------------------|---|---------------|------|---|
| V _{RRM} | Repetitive peak reverse voltage | 20 | V | |
| I _{F(RMS)} | Forward current rms | 2 | Α | |
| I _{F(AV)} | Average forward current $T_c = 140 ^{\circ}\text{C} \delta = 0.5$ | | 1 | Α |
| I _{FSM} | Surge non repetitive forward current $t_p = 10 \text{ ms sinusoidal}$ | | 50 | Α |
| P _{ARM} | Repetitive peak avalanche power | 1400 | W | |
| T _{stg} | Storage temperature range | - 65 to + 150 | °C | |
| T _j | Maximum operating junction temperature ⁽¹⁾ | 150 | °C | |
| dV/dt | Critical rate of rise of reverse voltage (rate | 10000 | V/µs | |

^{1.} $\frac{dPtot}{dTj} < \frac{1}{Rth(j-a)}$ condition to avoid thermal runaway for a diode on its own heatsink

Table 3. Thermal resistance

| Symbol | Parameter | Value | Unit |
|-------------------------------------|---------------------|-------|------|
| R _{th(j-c)} | Junction to case | 20 | °C/W |
| R _{th(j-a)} ⁽¹⁾ | Junction to ambient | 250 | °C/W |

^{1.} Mounted with minimum recommended pad size, PC board FR4

Table 4. Static electrical characteristics

| Symbol | Parameter | Tests conditions | | Min. | Тур. | Max. | Unit |
|--|---------------------------------|--|-----------------------|------|-------|-------|------|
| | | T _j = 25 °C | $V_R = V_{RRM}$ | | 0.015 | 0.075 | |
| | | T _j = 85 °C | | | 0.90 | 4.50 | |
| I _R ⁽¹⁾ | L (1) Deverse legicore guirrant | T _j = 25 °C | V _R = 10 V | | 0.005 | 0.035 | mA |
| I _R (1) Reverse leakage current | T _j = 85 °C | v _R = 10 v | | 0.45 | 2.50 | IIIA | |
| | | T _j = 25 °C | V _R = 5 V | | 0.003 | 0.025 | |
| | | T _j = 85 °C | v _R = 5 v | | 0.30 | 1.60 | |
| | y (1) Farmed with an disc | T _j = 25 °C | I _F = 1 A | | 0.38 | 0.43 | |
| | | T _j = 85 °C | | | 0.32 | 0.37 | |
| | | T _j = 25 °C | I _F = 2 A | | 0.42 | 0.47 | |
| V _F ⁽¹⁾ | | T _j = 85 °C | | | 0.37 | 0.42 | V |
| V _F ⁽¹⁾ Forward voltage drop | T _j = 25 °C | I _F = 3 A | | 0.46 | 0.53 | V | |
| | T _j = 85 °C | | | 0.42 | 0.49 | | |
| | | $T_j = 25 ^{\circ}\text{C}$ $T_i = 85 ^{\circ}\text{C}$ | I _F = 4 A | | 0.50 | 0.60 | |
| | | T _j = 85 °C | | | 0.46 | 0.56 | |

^{1.} Pulse test: = 380 μ s, δ < 2%

To evaluate the conduction losses use the following equation:

$$P = 0.32 \text{ x } I_{F(AV)} + 0.05 I_{F}^{2}_{(RMS)}$$

STPS1L20MF Characteristics

Figure 1. Conduction losses versus average Figure 2. Average forward current versus ambient temperature (δ = 0.5)

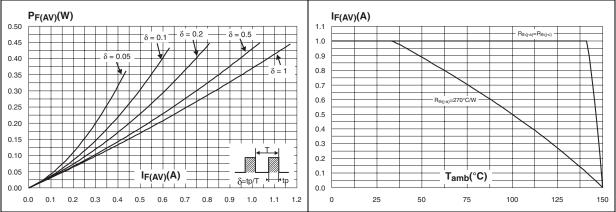


Figure 3. Normalized avalanche power derating versus pulse duration

Figure 4. Normalized avalanche power derating versus junction temperature

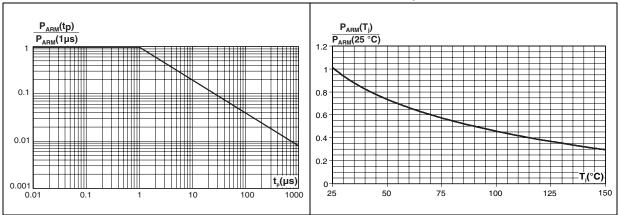
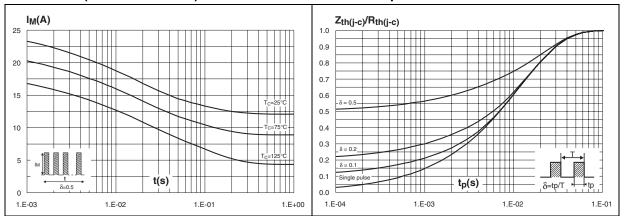


Figure 5. Non repetitive surge peak forward current versus overload duration (maximum values)

Figure 6. Relative variation of thermal impedance junction to case versus pulse duration



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Figure 7. Reverse leakage currrent versus reverse voltage applied (typical values)

Figure 8. Reverse leakage currrent versus junction temperature (typical values)

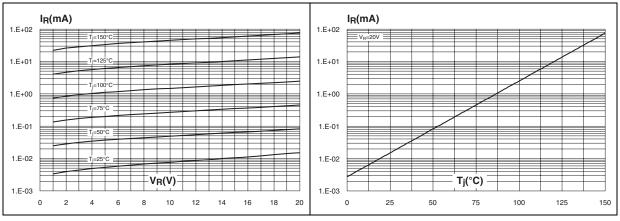


Figure 9. Junction capacitance versus reverse voltage applied (typical values)

Figure 10. Forward voltage drop versus forward current

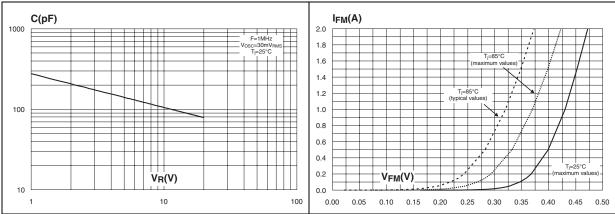
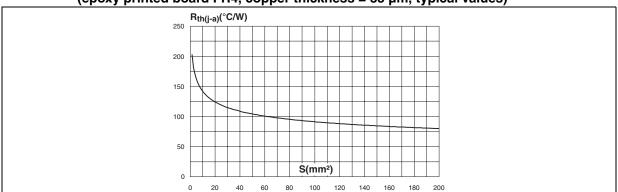


Figure 11. Thermal resistance junction to ambient versus copper surface under tab (epoxy printed board FR4, copper thickness = 35 µm, typical values)



2 Package information

- Epoxy meets UL94, V0
- Lead-free packages

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Table 5. STmite flat dimensions

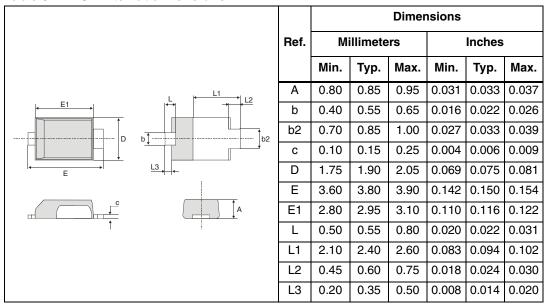
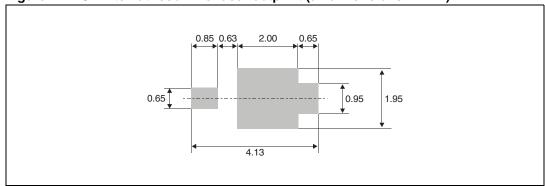


Figure 12. STmite flat recommended footprint (all dimensions in mm)



Ordering information STPS1L20MF

3 Ordering information

Table 6. Ordering information

| Order code | Marking | Package | Weight | Base qty | Delivery mode |
|------------|---------|-------------|--------|----------|---------------|
| STPS1L20MF | F1L2 | STmite flat | 16 mg | 12000 | Tape and reel |

4 Revision history

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Table 7. Document revision history

| Date | Revision | Changes |
|-------------|----------|---|
| 21-Aug-2006 | 1 | First issue. |
| 07-Jul-2011 | 2 | Reformatted to current standards. Updated caption for Figure 6. |

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