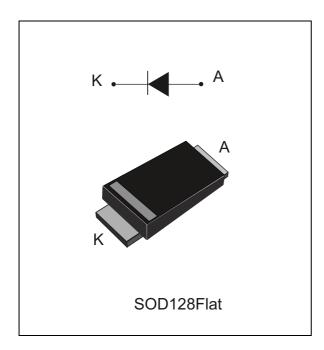


# STTH2R02AF-Y

### Automotive ultrafast rectifier

Datasheet - production data



## **Description**

The STTH2R02AF-Y, implementing ST's new 200 V planar technology, is especially suited for switching mode base drive and transistor circuits. The device is also intended for use as a free wheeling diode in power supplies and other power switching applications in automotive functions.

**Table 1. Device summary** 

Symbol	Value
I <sub>F(AV)</sub>	2 A
$V_{RRM}$	200 V
T <sub>j</sub> (max)	175 °C
V <sub>F</sub> (typ)	0.72 V
T <sub>rr</sub> (typ)	15 ns

### **Features**

- · Low conduction losses
- · Negligible switching losses
- Low forward and reverse recovery times
- High junction temperature
- AEC-Q101 qualified
- ECOPACK®2 compliant component
- PPAP capable

Characteristics STTH2R02AF-Y

## 1 Characteristics

Table 2. Absolute ratings (limiting values at  $T_i = 25$  °C, unless otherwise specified)

Symbol	Parameter	Value	Unit		
$V_{RRM}$	Repetitive peak reverse voltage	T <sub>j</sub> = -40 °C	200	V	
I <sub>F(AV)</sub>	Average forward current, square waveform	2	Α		
I <sub>FSM</sub>	Surge current non repetitive forward current	50	Α		
T <sub>stg</sub>	Storage temperature range	-65 to + 175	°C		
T <sub>j</sub> <sup>(1)</sup>	Operating temperature range -40 to + 1				

<sup>1.</sup>  $\frac{dPtot}{dTj} < \frac{1}{Rth(j-a)}$  condition to avoid thermal runaway for a diode on its own heatsink

Table 3. Thermal resistance

Symbol	Parameter	Тур.	Max.	Unit
R <sub>th(j-l)</sub>	Junction to lead	16	24	°C/W

**Table 4. Static electrical characteristics** 

Symbol	Parameter	Tests conditions		Min.	Тур.	Max.	Unit
I <sub>R</sub> <sup>(1)</sup>	Reverse leakage current	T <sub>j</sub> = 25 °C	V - V			0.8	μA
'R`	Reverse leakage current	T <sub>j</sub> = 125 °C	$V_R = V_{RRM}$		1	8	μΑ
V <sub>E</sub> (2)	Forward voltage drop	T <sub>j</sub> = 25 °C	I <sub>F</sub> = 2 A		0.91	1.02	V
<b>v</b> F` ′		T <sub>j</sub> = 150 °C	1 IF - 2 A		0.72	0.83	V

<sup>1.</sup> Pulse test: tp = 5 ms,  $\delta < 2\%$ 

To evaluate the conduction losses use the following equation:

$$P = 0.71 \times I_{F(AV)} + 0.06 \times I_{F^2(RMS)}$$

Table 5. Dynamic electrical characteristics

Symbol	Parameter	Tests conditions			Тур.	Max.	Unit
	t <sub>rr</sub> Reverse recovery time	T <sub>j</sub> = 25 °C	$I_F = 1 \text{ A}, dI_F/dt = -100 \text{ A/}\mu\text{s}$ $V_R = 30 \text{ V}$		15	20	
t <sub>rr</sub>		1	$V_{\rm F} = 1 \text{ A, dI}_{\rm F}/{\rm dt} = 50 \text{ A/}\mu{\rm s}$ 22			ns	
		T <sub>j</sub> = 125 °C	$I_F = 2 \text{ A}, dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_R = 160 \text{ V}$		22		
Q <sub>RR</sub>	Reverse recovery charge	T 125 °C	$I_F = 2 \text{ A}, dI_F/dt = -200 \text{ A/}\mu\text{s},$ $V_R = 160 \text{ V}$		40		nC
I <sub>RM</sub>	Reverse recovery current	1 <sub>1</sub> = 125 C			3		Α



<sup>2.</sup> Pulse test:  $tp = 380 \mu s$ ,  $\delta < 2\%$ 

STTH2R02AF-Y Characteristics

Figure 1. Average forward power dissipation versus average forward current

 $P_{F(AV)}(W)$ 

2.4

2.0

1.6

0.8

average forward current  $\delta = 0.05 \quad \delta = 0.1 \quad \delta = 0.2 \quad \delta = 0.5 \quad \delta = 1$ 

Figure 2. Forward voltage drop versus forward current (typical values)

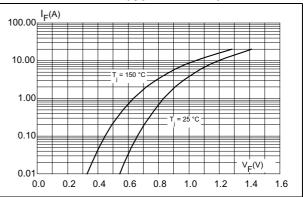
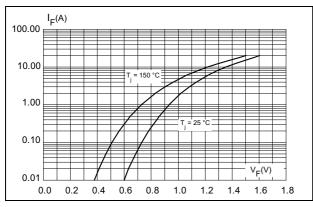


Figure 3. Forward voltage drop versus forward current (maximum values)

0.0 0.2 0.4 0.6 0.8 1.0 1.2 1.4 1.6 1.8 2.0 2.2 2.4

δ= tp/T

Figure 4. Relative variation of thermal impedance junction to lead versus pulse duration



Z<sub>th(j-l)</sub>/R<sub>th(j-l)</sub>

1.0

0.9

0.8

0.7

0.6

0.5

0.4

0.3

0.2

Single pulse

1.E-04

1.E-03

1.E-02

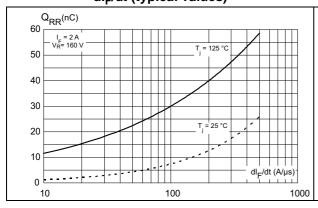
1.E-01

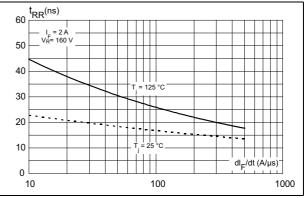
1.E+00

1.E+01

Figure 5. Reverse recovery charges versus dl<sub>F</sub>/dt (typical values)

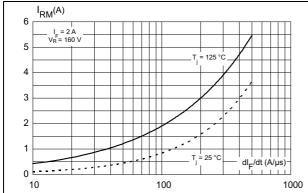
Figure 6. Reverse recovery time versus dl<sub>F</sub>/dt (typical values)





Characteristics STTH2R02AF-Y

Figure 7. Peak reverse recovery current versus Figure 8. Dynamic parameters versus junction dl<sub>F</sub>/dt (typical values) temperature



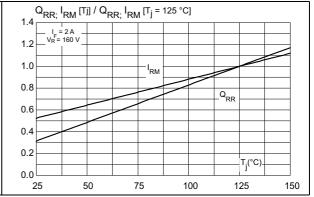
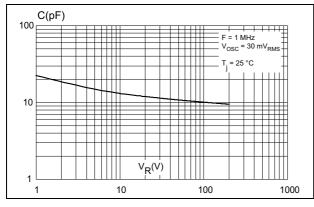
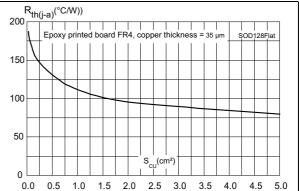


Figure 9. Junction capacitance versus reverse voltage applied (typical values)

Figure 10. Thermal resistance junction to ambient versus copper surface under each lead (typical values)





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#### **Package information** 2

- Epoxy meets UL94,V0
- Lead-free package

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: www.st.com. ECOPACK® is an ST trademark.

**1** L1 L2 D E E1

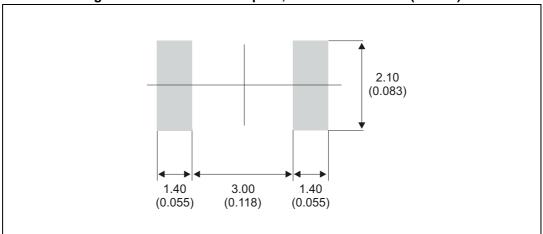
Figure 11. SOD128Flat package outline

Package information STTH2R02AF-Y

Table 6. SOD128Flat package mechanical data

	Dimensions						
Ref.		Millimeters	rs Inches				
	Min.	Тур.	Max.	Min.	Тур.	Max.	
Α	0.93		1.03	0.037		0.041	
b	1.69		1.81	0.067		0.071	
С	0.10		0.22	0.004		0.009	
D	2.30		2.50	0.091		0.098	
Е	4.60		4.80	0.181		0.189	
E1	3.70		3.90	0.146		0.154	
L	0.55		0.85	0.026		0.033	
L1		0.30			0.012		
L2		0.45			0.018		

Figure 12. SOD128Flat footprint, dimensions in mm (inches)



# 3 Ordering information

**Table 7. Ordering information** 

Order codes	Marking	Package	Weight	Base qty	Delivery mode
STTH2R02AFY	2R2AY	SOD128Flat	26.4 mg	3000	Tape and reel

# 4 Revision history

**Table 8. Document revision history** 

Date	Revision	Changes
27-Feb-2015	1	Initial release.

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