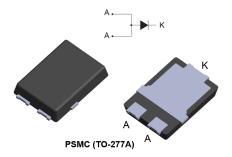


200 V ultrafast recovery diode



Features

- Low profile design package typical height of 1.1 mm typ.
- Wettable flanks for automatic visual inspection
- Very low conduction losses
- Negligible switching losses
- · High junction temperature capability
- ECOPACK[®]2 compliant

Applications

- DC/DC converter
- · High frequency inverter
- Snubber
- Boost function
- · Freewheeling diode

Description

This device is an ultrafast recovery diode optimized for switching mode base drive and transistor circuits.

Packaged in PSMC (TO-277A), the STTH802SF provides a high level of performance in a compact and flat package which can withstand very high operating junction temperature.

Product status link					
STTH802SF					
Product summary					
Symbol	Value				
I _{F(AV)}	8 A				
V _{RRM}	200 V				
T _j (max.)	175 °C				
V _F (typ.)	0.79 V				
t _{rr} (typ.)	17 ns				



1 Characteristics

Table 1. Absolute ratings (limiting values at 25 °C, unless otherwise specified, anode terminals short-circuited)

Symbol	Parameter	Value	Unit	
V _{RRM}	Repetitive peak reverse voltage	200	V	
I _{F(AV)}	Average forward current, δ = 0.5 square pulse	8	А	
I _{FSM}	Surge non repetitive forward current	150	Α	
T _{stg}	Storage temperature range	-65 to +175	°C	
T _j	Maximum operating junction temperature		+175	°C

Table 2. Thermal resistance parameters

Symbol	Parameter	Typ. value	Unit
R _{th(j-c)}	Junction to case	2.4	°C/W

For more information, please refer to the following application note:

AN5088: Rectifiers thermal management, handling and mounting recommendations

Table 3. Static electrical characteristics (anode terminals short-circuited)

Symbol	Parameter	Test conditions		Min.	Тур.	Max.	Unit
I _R ⁽¹⁾ Reverse leakage current		T _j = 25 °C	V _R = V _{RRM}	-		6	
'R`	I _R ⁽¹⁾ Reverse leakage current	T _j = 125 °C	VR - VRRM	-	6	60	μA
V _F ⁽²⁾	V-(2) Famuerd veltage drep		I _F = 8 A	-	0.94	1.08	V
VF Forward vo	Forward voltage drop	T _j = 125 °C	IF - 0 A	-	0.79	0.91	V

- 1. Pulse test: $t_p = 5$ ms, $\delta < 2\%$
- 2. Pulse test: t_p = 380 μ s, δ < 2%

To evaluate the conduction losses, use the following equation: $P = 0.77 \times I_{F(AV)} + 0.018 \times I_{F^2(RMS)}$

For more information, please refer to the following application notes related to the power losses:

- AN604: Calculation of conduction losses in a power rectifier
- AN4021: Calculation of reverse losses in a power diode

Table 4. Dynamic electrical characteristics

Symbol	Parameter	Test conditions			Тур.	Max.	Unit
t _{rr}	Povorso rocovory timo	T _i = 25 °C	$I_F = 1.0 \text{ A}, dI_F/dt = -50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$	-		35	ns
Yrr	t _{rr} Reverse recovery time	1, - 25 0	$I_F = 1.0 \text{ A}, dI_F/dt = -100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$	-	17	22	115
I _{RM}	Reverse recovery current	T _i = 125 °C	T _i = 125 °C		5.8	7.5	Α
Q _{rr}	Reverse recovery charge	1, - 125 C	I _F = 6 A, αI _F /αι = -200 A/μS, V _R = 160 V	-	100		nC

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1.1 Characteristics (curves)

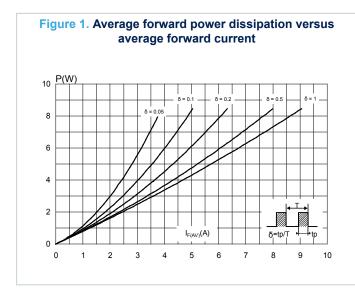


Figure 3. Forward voltage drop versus forward current (maximum values)

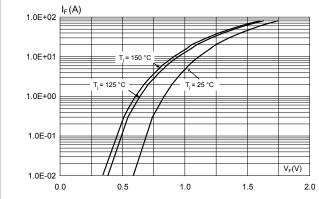
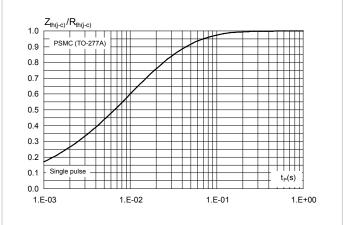


Figure 4. Relative variation of thermal impedance junction to case versus pulse duration



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Figure 5. Peak reverse recovery current versus dl_F/dt (typical values)

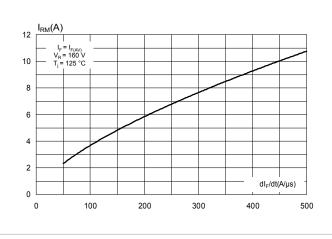


Figure 6. Reverse recovery time versus dl_F/dt (typical values)

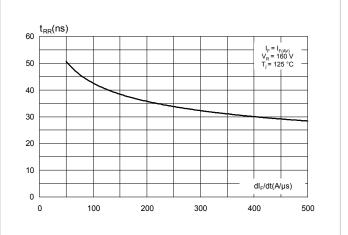


Figure 7. Reverse recovery charges versus dl_F/dt (typical values)

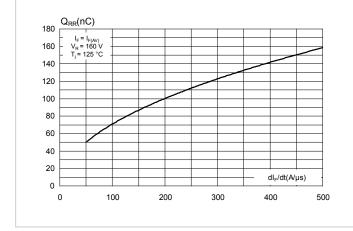


Figure 8. Reverse recovery softness factor versus dl_F/dt (typical values)

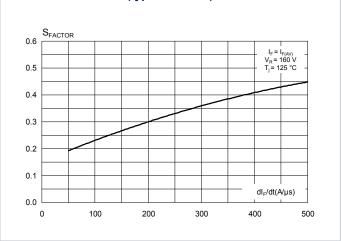


Figure 9. Relative variations of dynamic parameters versus junction temperature

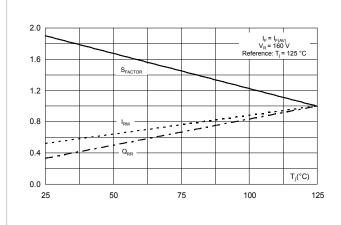
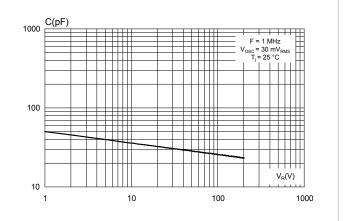


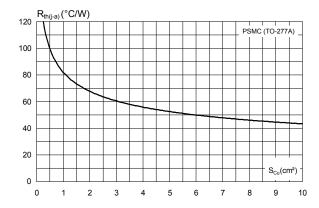
Figure 10. Junction capacitance versus reverse voltage applied (typical values)



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Figure 11. Thermal resistance junction to ambient versus copper surface under tab (typical values, epoxy printed board FR4, e_{Cu} = 35 μ m) (PSMC (TO-277A))



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2 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: www.st.com. ECOPACK[®] is an ST trademark.

2.1 PSMC (TO-277A) package information

- Epoxy meets UL94,V0
- Cooling method : by conduction (C)

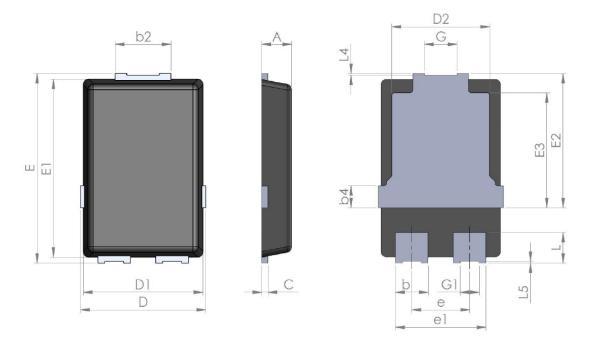


Figure 12. PSMC (TO-277A) package outline

Table 5. PSMC (TO-277A) package mechanical data

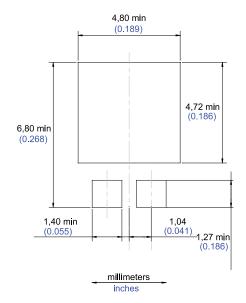
			Dimer	nsions			
Ref.		Millimeters		Inches (for reference only)			
	Min.	Тур.	Max.	Min.	Тур.	Max.	
А	1.00	1.10	1.20	0.039	0.043	0.047	
b	1.05	1.20	1.35	0.041	0.047	0.053	
b2	1.90	2.05	2.20	0.075	0.081	0.087	
b4		0.75			0.029		
С	0.15	0.23	0.40	0.006	0.009	0.016	
D	4.45	4.60	4.75	0.175	0.181	0.187	
D1	4.25	4.40	4.45	0.167	0.173	0.175	
D2	3.40	3.60	3.70	0.134	0.142	0.146	

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			Dime	nsions		
Ref.		Millimeters		Inch	es (for reference o	only)
	Min.	Тур.	Max.	Min.	Тур.	Max.
E	6.35	6.50	6.65	0.250	0.256	0.262
E1	6.05	6.10	6.15	0.238	0.240	0.242
E2	4.50	4.60	4.70	0.177	0.181	0.185
E3		3.94			1.55	
е		2.13			0.084	
e1		3.33			0.131	
G		1.20			0.047	
G1		0.70			0.027	
L	0.90	1.05	1.24	0.035	0.041	0.049
L4	0.02			0.0008		
L5	0.02			0.0008		

Figure 13. PSMC (TO-277A) package footprint in mm (in inches)



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3 Ordering information

Table 6. Ordering information

Order code	Marking	Package	Weight	Base qty.	Delivery mode
STTH802SF	TH802	PSMC (TO-277A)	90 mg	6000	Tape and Reel

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Revision history

Table 7. Document revision history

Date	Version	Changes
04-Jul-2018	1	Initial release.
10-Jul-2018	2	Updated Table 1. Absolute ratings (limiting values at 25 °C, unless otherwise specified, anode terminals short-circuited).

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