

# STW45N60DM2AG

# Automotive-grade N-channel 600 V, 0.085 Ω typ., 34 A MDmesh<sup>™</sup> DM2 Power MOSFET in a TO-247 package

Datasheet - production data

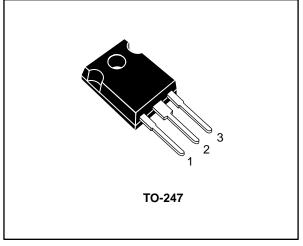
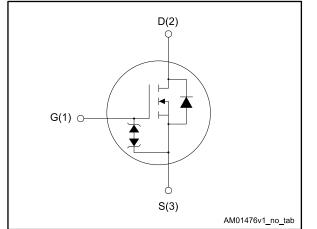


Figure 1: Internal schematic diagram



### **Features**

Order code	V <sub>DS</sub> @ T <sub>Jmax.</sub>	R <sub>DS(on)</sub> max.	ID	Ртот
STW45N60DM2AG	650 V	0.093 Ω	34 A	250 W

- Designed for automotive applications and AEC-Q101 qualified
- Fast-recovery body diode
- Extremely low gate charge and input capacitance
- Low on-resistance
- 100% avalanche tested
- Extremely high dv/dt ruggedness
- Zener-protected

### **Applications**

Switching applications

### Description

This high voltage N-channel Power MOSFET is part of the MDmesh<sup>TM</sup> DM2 fast recovery diode series. It offers very low recovery charge ( $Q_{rr}$ ) and time ( $t_{rr}$ ) combined with low  $R_{DS(on)}$ , rendering it suitable for the most demanding high efficiency converters and ideal for bridge topologies and ZVS phase-shift converters.

Table 1: Device summary

Order code	Marking	Package	Packing
STW45N60DM2AG	45N60DM2	TO-247	Tube

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This is information on a product in full production.

### Contents

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# 1 Electrical ratings

Table 2: Absolute maximum ratings

Symbol	Parameter	Value	Unit
V <sub>GS</sub>	Gate-source voltage ±25		V
1-	Drain current (continuous) at T <sub>case</sub> = 25 °C		А
ID	Drain current (continuous) at T <sub>case</sub> = 100 °C	21	A
I <sub>DM</sub> <sup>(1)</sup>	Drain current (pulsed)	136 A	
P <sub>TOT</sub>	Total dissipation at $T_{case} = 25 \text{ °C}$	250 W	
dv/dt <sup>(2)</sup>	Peak diode recovery voltage slope	50	
dv/dt <sup>(3)</sup>	MOSFET dv/dt ruggedness	50	V/ns
T <sub>stg</sub>	Storage temperature	55 to 150	°C
Tj	Operating junction temperature	-55 to 150	C

#### Notes:

 $^{\left( 1\right) }$  Pulse width is limited by safe operating area.

 $^{(2)}$  I\_{SD}  $\leq$  34 A, di/dt=800 A/µs; V\_{DS} peak < V\_{(BR)DSS}, V\_{DD} = ~80% V\_{(BR)DSS}.

<sup>(3)</sup>  $V_{DS} \le 480 \text{ V}.$ 

#### Table 3: Thermal data

Symbol	Parameter	Value	Unit	
R <sub>thj-case</sub>	Thermal resistance junction-case	0.50	°C/W	
R <sub>thj-amb</sub>	Thermal resistance junction-ambient	50		

#### Table 4: Avalanche characteristics

Symbol	Parameter	Value	Unit
I <sub>AR</sub>	Avalanche current, repetitive or not repetitive	6	А
E <sub>AS</sub> <sup>(1)</sup>	Single pulse avalanche energy	800	mJ

#### Notes:

 $^{(1)}$  starting  $T_{j}$  = 25 °C,  $I_{D}$  =  $I_{AR},\,V_{DD}$  = 50 V.



# 2 Electrical characteristics

(T<sub>case</sub> = 25 °C unless otherwise specified)

		Table 5: Static				
Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V <sub>(BR)DSS</sub>	Drain-source breakdown voltage	$V_{GS}$ = 0 V, $I_D$ = 1 mA	600			V
	Zero gate voltage drain current	$V_{GS} = 0 V, V_{DS} = 600 V$			1	
I <sub>DSS</sub>		$V_{GS} = 0 V, V_{DS} = 600 V,$ $T_{case} = 125 \text{ °C}$			100	μA
I <sub>GSS</sub>	Gate-body leakage current	$V_{DS} = 0 V, V_{GS} = \pm 25 V$			±5	μA
$V_{GS(th)}$	Gate threshold voltage	$V_{DS} = V_{GS}$ , $I_D = 250 \ \mu A$	3	4	5	V
R <sub>DS(on)</sub>	Static drain-source on-resistance	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 17 \text{ A}$		0.085	0.093	Ω

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
Ciss	Input capacitance		-	2500	-	
C <sub>oss</sub>	Output capacitance	$V_{DS} = 100 V, f = 1 MHz,$	-	120	-	pF
C <sub>rss</sub>	Reverse transfer capacitance	$V_{GS} = 0 V$	-	3	-	P
C <sub>oss eq.</sub> <sup>(1)</sup>	Equivalent output capacitance	$V_{\text{DS}}$ = 0 to 480 V, $V_{\text{GS}}$ = 0 V	-	200	-	pF
$R_G$	Intrinsic gate resistance	$f = 1 \text{ MHz}, I_D = 0 \text{ A}$	-	4	-	Ω
Qg	Total gate charge	V <sub>DD</sub> = 480 V, I <sub>D</sub> = 34 A,	-	56	-	
Q <sub>gs</sub>	Gate-source charge	$V_{GS}$ = 10 V (see <i>Figure 15</i> :	-	13	-	nC
Q <sub>gd</sub>	Gate-drain charge	"Gate charge test circuit")	-	30	-	

#### Table 6: Dynamic

#### Notes:

 $^{(1)}$   $C_{oss\ eq.}$  is defined as a constant equivalent capacitance giving the same charging time as  $C_{oss}$  when  $V_{DS}$  increases from 0 to 80%  $V_{DSS}$ .

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
t <sub>d(on)</sub>	Turn-on delay time	$V_{DD} = 300 \text{ V}, \text{ I}_{D} = 25 \text{ A}$	-	29	-	
tr	Rise time	$R_G = 4.7 \Omega$ , $V_{GS} = 10 V$ (see Figure 14: "Switching	-	27	-	
t <sub>d(off)</sub>	Turn-off delay time	times test circuit for	-	85	-	ns
t <sub>f</sub>	Fall time	resistive load" and Figure 19: "Switching time waveform")	-	6	-	

#### Table 7: Switching times



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#### Electrical characteristics

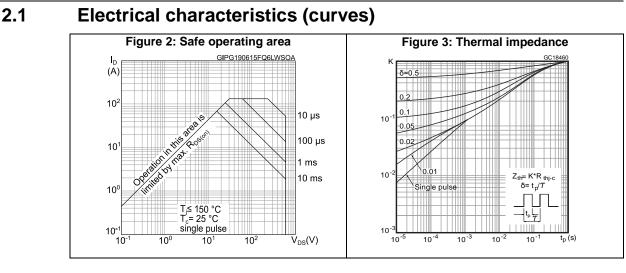
DIVIZAG					anacteri	31103
		Table 8: Source-drain diode			r	
Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I <sub>SD</sub>	Source-drain current		-		34	А
I <sub>SDM</sub> <sup>(1)</sup>	Source-drain current (pulsed)		-		136	А
V <sub>SD</sub> <sup>(2)</sup>	Forward on voltage	$V_{GS} = 0 V, I_{SD} = 34 A$	-		1.6	V
t <sub>rr</sub>	Reverse recovery time	I <sub>SD</sub> = 34 A,	-	120		ns
Q <sub>rr</sub>	Reverse recovery charge	di/dt = 100 A/ $\mu$ s, V <sub>DD</sub> = 60 V (see Figure 16: "Test circuit for inductive	-	0.6		μC
I <sub>RRM</sub>	Reverse recovery current	load switching and diode recovery times")	-	10.4		А
t <sub>rr</sub>	Reverse recovery time	I <sub>SD</sub> = 34 A,	-	240		ns
Q <sub>rr</sub>	Reverse recovery charge	di/dt = 100 A/µs, V <sub>DD</sub> = 60 V, T <sub>j</sub> = 150 °C (see <i>Figure 16: "Test</i>	-	2.4		μC
I <sub>RRM</sub>	Reverse recovery current	circuit for inductive load switching and diode recovery times")	-	20.5		A

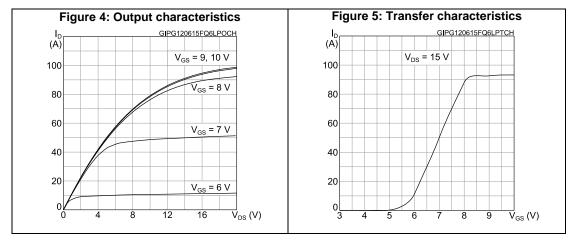
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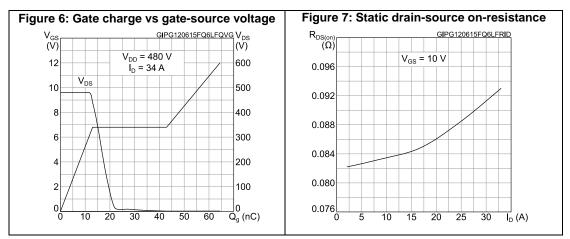
 $^{\left( 1\right) }$  Pulse width is limited by safe operating area.

<sup>(2)</sup> Pulse test: pulse duration = 300  $\mu$ s, duty cycle 1.5%.



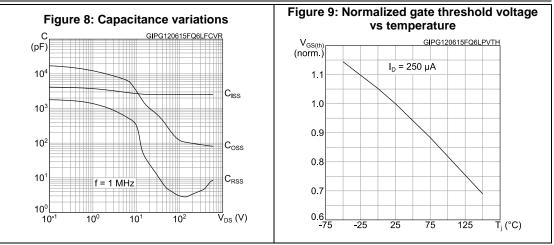


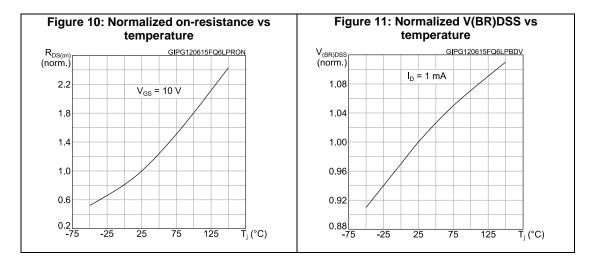


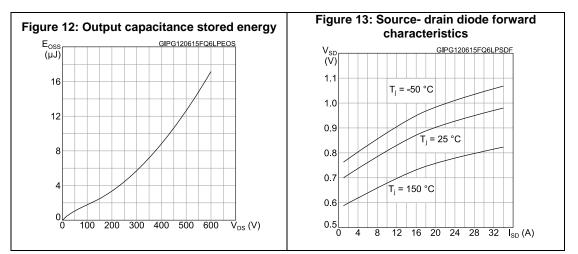




#### **Electrical characteristics**



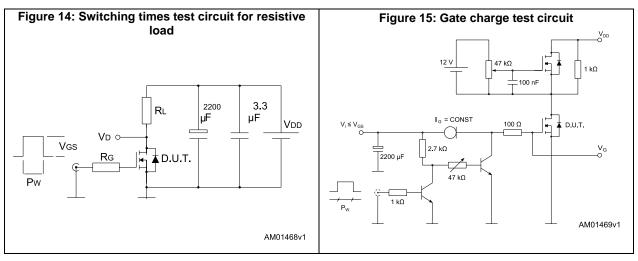


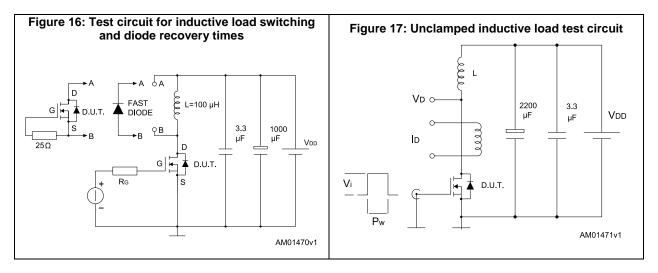


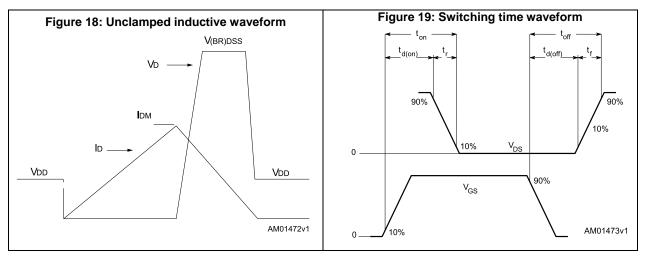
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### 3 Test circuits







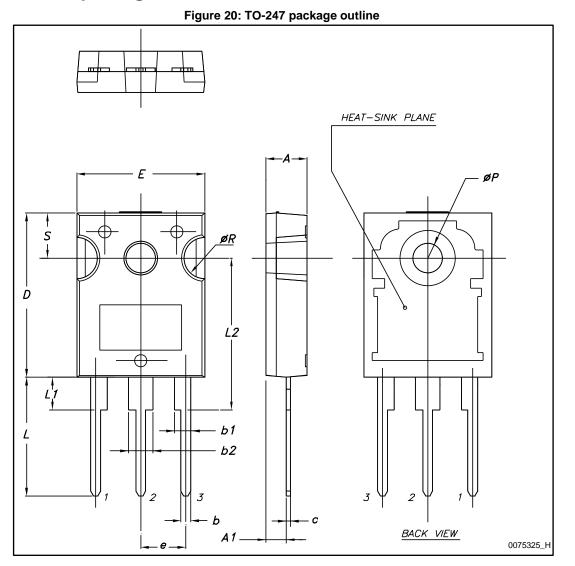
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### 4 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: *www.st.com*. ECOPACK<sup>®</sup> is an ST trademark.

### 4.1 TO-247 package information





#### Package information

#### STW45N60DM2AG

Table 9: TO-247 package mechanical data				
Dim.	mm.			
	Min.	Тур.	Max.	
A	4.85		5.15	
A1	2.20		2.60	
b	1.0		1.40	
b1	2.0		2.40	
b2	3.0		3.40	
С	0.40		0.80	
D	19.85		20.15	
E	15.45		15.75	
е	5.30	5.45	5.60	
L	14.20		14.80	
L1	3.70		4.30	
L2		18.50		
ØP	3.55		3.65	
ØR	4.50		5.50	
S	5.30	5.50	5.70	

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# 5 Revision history

Table 10: Document revision history

Date	Revision	Changes
03-Jul-2015	1	Initial release.



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