

STW58N60DM2AG

Automotive-grade N-channel 600 V, 0.052 Ω typ., 50 A MDmesh™ DM2 Power MOSFET in a TO-247 package

Datasheet - production data

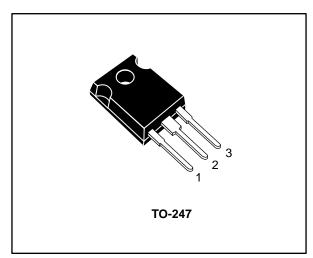
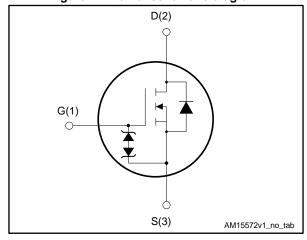


Figure 1: Internal schematic diagram



Features

Order code	V _{DS}	R _{DS(on)} max.	I _D	P _{TOT}
STW58N60DM2AG	600 V	0.060 Ω	50 A	360 W

- Designed for automotive applications and AEC-Q101 qualified
- Fast-recovery body diode
- Extremely low gate charge and input capacitance
- Low on-resistance
- 100% avalanche tested
- Extremely high dv/dt ruggedness
- Zener-protected

Applications

Switching applications

Description

This high voltage N-channel Power MOSFET is part of the MDmesh $^{\text{TM}}$ DM2 fast recovery diode series. It offers very low recovery charge (Q_{rr}) and time (t_{rr}) combined with low R_{DS(on)}, rendering it suitable for the most demanding high efficiency converters and ideal for bridge topologies and ZVS phase-shift converters.

Table 1: Device summary

Order code	Marking	Package	Packing
STW58N60DM2AG	58N60DM2	TO-247	Tube

Contents STW58N60DM2AG

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STW58N60DM2AG Electrical ratings

1 Electrical ratings

Table 2: Absolute maximum ratings

Symbol	Parameter	Value	Unit
V _{GS}	Gate-source voltage	±25	V
	Drain current (continuous) at T _{case} = 25 °C	50	۸
I _D	Drain current (continuous) at T _{case} = 100 °C	31	Α
I _{DM} ⁽¹⁾	Drain current (pulsed) 200		Α
P _{TOT}	Total dissipation at T _{case} = 25 °C		W
dv/dt ⁽²⁾	Peak diode recovery voltage slope 50		V/ns
dv/dt ⁽³⁾	MOSFET dv/dt ruggedness	50	V/IIS
T _{stg}	Storage temperature		°C
T _j	Operating junction temperature	-55 to 150	Ü

Notes:

Table 3: Thermal data

Symbol	Parameter	Value	Unit
R _{thj-case}	Thermal resistance junction-case	0.35	°C/W
R _{thj-amb}	Thermal resistance junction-ambient	50 °C	

Table 4: Avalanche characteristics

Symbol	Parameter	Value	Unit
I _{AS} ⁽¹⁾	Avalanche current, repetitive or not repetitive	12	Α
E _{AS} ⁽²⁾	Single pulse avalanche energy	800	mJ

Notes:

 $^{^{\}left(1\right)}$ Pulse width is limited by safe operating area.

 $^{^{(2)}}$ $I_{SD} \leq$ 50 A, di/dt=800 A/µs; V_{DS} peak < $V_{(BR)DSS},$ V_{DD} = 80% $V_{(BR)DSS}.$

 $^{^{(3)}} V_{DS} \le 480 V.$

 $^{^{(1)}}$ Pulse width limited by T_{jmax} .

 $^{^{(2)}}$ starting $T_j = 25$ °C, $I_D = I_{AS}, \, V_{DD} = 50$ V.

2 Electrical characteristics

(T_{case} = 25 °C unless otherwise specified)

Table 5: Static

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
$V_{(BR)DSS}$	Drain-source breakdown voltage	$V_{GS} = 0 \text{ V}, I_D = 1 \text{ mA}$	600			V
	Zoro goto voltago drain	$V_{GS} = 0 \text{ V}, V_{DS} = 600 \text{ V}$			10	
I _{DSS}	Zero gate voltage drain current	$V_{GS} = 0 \text{ V}, V_{DS} = 600 \text{ V},$ $T_{case} = 125 \text{ °C}$			100	μΑ
I _{GSS}	Gate-body leakage current	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 25 \text{ V}$			±5	μΑ
$V_{GS(th)}$	Gate threshold voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	3	4	5	V
R _{DS(on)}	Static drain-source on- resistance	V _{GS} = 10 V, I _D = 25 A		0.052	0.060	Ω

Table 6: Dynamic

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
C _{iss}	Input capacitance		-	4100	•	
C _{oss}	Output capacitance	V _{DS} = 100 V, f = 1 MHz, V _{GS} = 0 V	-	190	ı	pF
C _{rss}	Reverse transfer capacitance		-	3.2	-	
C _{oss eq.}	Equivalent output capacitance	$V_{DS} = 0$ to 480 V, $V_{GS} = 0$ V	-	325	ı	pF
R _G	Intrinsic gate resistance	f = 1 MHz, I _D = 0 A	-	4.2	ı	Ω
Qg	Total gate charge		-	90	ı	
Q _{gs}	Gate-source charge	V _{DD} = 480 V, I _D = 50 A, V _{GS} = 10 V (see <i>Figure 15: "Gate charge test circuit"</i>)	_	18	-	nC
Q_{gd}	Gate-drain charge	on out ,	-	44	-	

Table 7: Switching times

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
t _{d(on)}	Turn-on delay time	V 200 V I 25 A P 47 O (200	-	24	-	
t _r	Rise time	$V_{DD} = 300 \text{ V}, I_D = 25 \text{ A R}_G = 4.7 \Omega \text{ (see}$ Figure 14: "Switching times test circuit for	ı	60	ı	
t _{d(off)}	Turn-off delay time	resistive load" and Figure 19: "Switching time waveform")	- 1	130	ı	ns
t _f	Fall time		-	12		

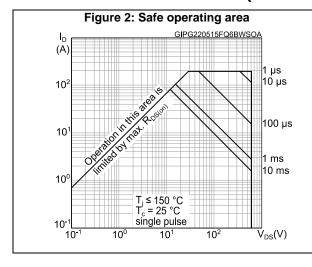
Table 8: Source-drain diode

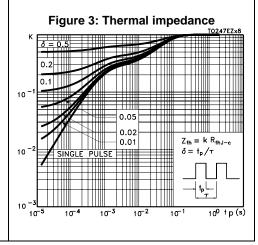
Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I _{SD}	Source-drain current		1		50	Α
I _{SDM}	Source-drain current (pulsed)		ı		200	Α
V _{SD} ⁽¹⁾	Forward on voltage	V _{GS} = 0 V, I _{SD} = 50 A	1		1.6	٧
t _{rr}	Reverse recovery time		1	140		ns
Q _{rr}	Reverse recovery charge	I _{SD} = 50 A, di/dt = 100 A/μs, V _{DD} = 60 V (see Figure 16: "Test circuit for inductive load switching and diode recovery times")	-	0.7		μC
I _{RRM}	Reverse recovery current	Toad switching and diode recovery times)		10.6		Α
t _{rr}	Reverse recovery time		1	245		ns
Q _{rr}	Reverse recovery charge	I_{SD} = 50 A, di/dt = 100 A/µs, V_{DD} = 60 V, T_{j} = 150 °C (see Figure 16: "Test circuit for inductive load switching and diode		2.6		μC
I _{RRM}	Reverse recovery current	recovery times")	-	21		А

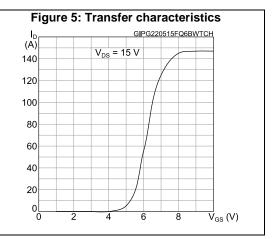
Notes:

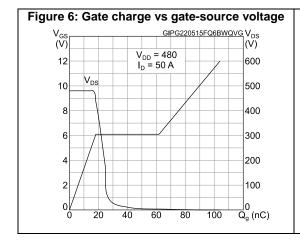
 $^{^{(1)}}$ Pulse test: pulse duration = 300 $\mu s,$ duty cycle 1.5%.

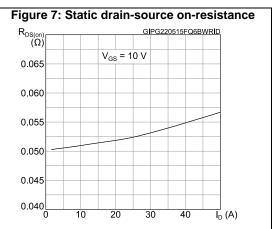
2.1 Electrical characteristics (curves)











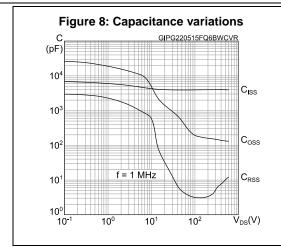


Figure 10: Normalized on-resistance vs temperature

R_{DS(on)} GIPG220515FQ6BWRON
(norm.)

2.2

1.8

1.4

1.0

0.6

0.2

-75

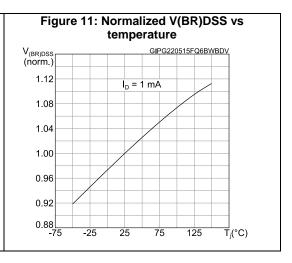
-25

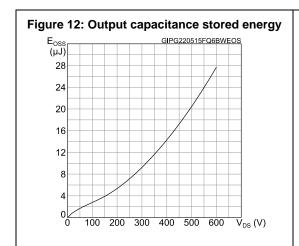
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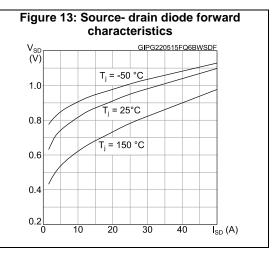
75

125

T_j (°C)

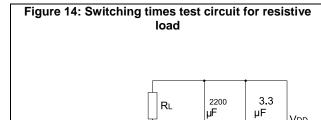






Test circuits STW58N60DM2AG

3 **Test circuits**





Vdd

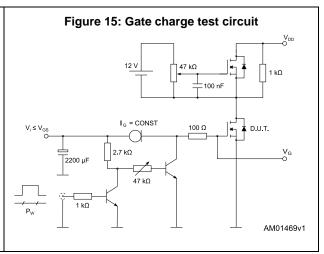


Figure 16: Test circuit for inductive load switching and diode recovery times

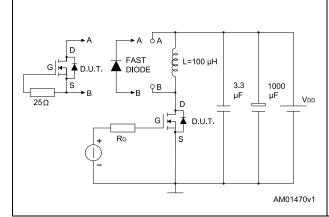
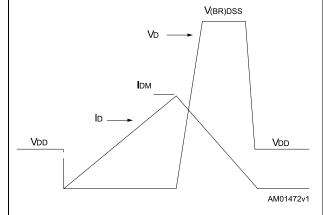
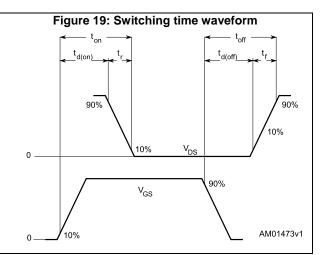


Figure 17: Unclamped inductive load test circuit 2200 3.3 VDD AM01471v1

Figure 18: Unclamped inductive waveform





4 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: **www.st.com**. ECOPACK® is an ST trademark.

4.1 TO-247 package information

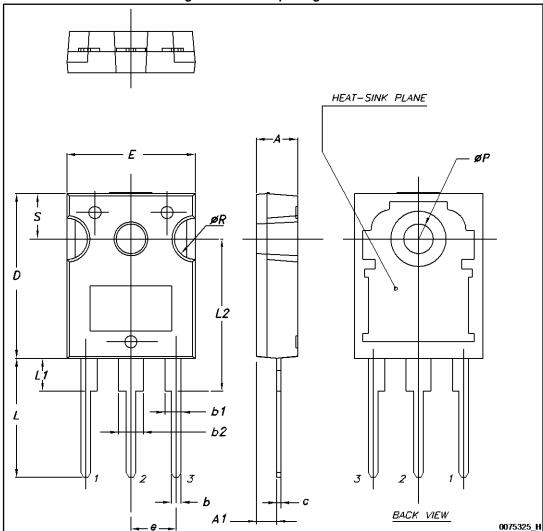


Figure 20: TO-247 package outline

Table 9: TO-247 package mechanical data

Dim		mm.	
Dim.	Min.	Тур.	Max.
А	4.85		5.15
A1	2.20		2.60
b	1.0		1.40
b1	2.0		2.40
b2	3.0		3.40
С	0.40		0.80
D	19.85		20.15
Е	15.45		15.75
е	5.30	5.45	5.60
L	14.20		14.80
L1	3.70		4.30
L2		18.50	
ØP	3.55		3.65
ØR	4.50		5.50
S	5.30	5.50	5.70

STW58N60DM2AG Revision history

5 Revision history

Table 10: Document revision history

Date	Revision	Changes
12-Jun-2015	1	First release.
20-Jul-2015	2	Updated title and features. Minor text changes.

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