

Medium current, high performance, low voltage PNP transistor

Features

- Very low collector to emitter saturation voltage
- DC current gain, h_{FF} > 100
- 3 A continuous collector current
- 40 V breakdown voltage V_{(BR)CER}

Applications

- Power management in portable equipment
- Voltage regulation in bias supply circuits
- Switching regulator in battery charger applications
- Heavy load driver

Description

The devices are manufactured in low voltage PNP planar technology by using a "Base Island" layout. The resulting transistor shows exceptional high gain performance coupled with very low saturation voltage. The STX790AG-AP is supplied using halogen-free molding compound.

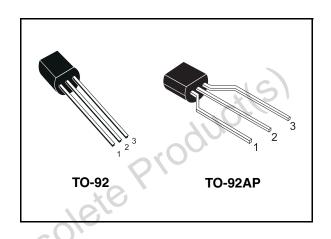


Figure 1. Internal schematic diagram

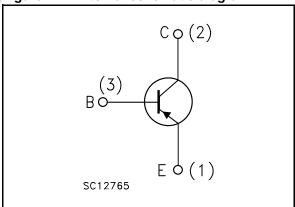


Table 1. Device summary

Order codes	Marking	Packages	Packaging
STX790A	X790A	TO-92	Bulk
STX790A-AP	X790A	TO-92 AP	Ammopack
STX790AG-AP	X790AG	TO-92 AP	Ammopack

Electrical ratings STX790A

Electrical ratings 1

Table 2. **Absolute maximum ratings**

Symbol	Parameter	Value	Unit
V _{CBO}	Collector-base voltage (I _E = 0)	-40	V
V _{CER}	Collector-emitter voltage ($R_{BE} = 47 \Omega$)	-40	V
V _{CEO}	Collector-emitter voltage (I _B = 0)	-30	V
V _{EBO}	Emitter-base voltage (I _C = 0)	-5 LC	V
I _C	Collector current	-3	Α
I _{CM}	Collector peak current (t _P < 5 ms)	-6	Α
P _{tot}	Total dissipation at T _{amb} = 25 °C	0.9	W
T _{stg}	Storage temperature	-65 to 150	°C
T _J	Max. operating junction temperature 150		
Table 3.	Thermal data		
Symbol	Parameter	Value	Unit

Table 3. Thermal data

	Symbol	Parameter		Value	Unit	
	R _{thj-case}	Thermal resistance junction-case max		44.6	°C/W	
	R _{thj-amb}	Thermal resistance junction-ambient max		139	°C/W	
Obsole	teP	KOOIN				

2 Electrical characteristics

 $(T_{case} = 25 \, ^{\circ}C \text{ unless otherwise specified})$

Table 4. Electrical characteristics

Symbol Parameter		Test cor	nditions	Min.	Тур.	Max.	Unit	
	I _{CBO}	Collector cut-off current (I _E = 0)	$V_{CB} = -30 \text{ V}$ $V_{CB} = -30 \text{ V}$;	T _C = 100 °C			-10 -100	μ Α μ Α
	I _{EBO}	Emitter cut-off current (I _C = 0)	V _{EB} = -4 V				-10	μА
	V _{(BR)CEO} (1)	Collector-emitter breakdown voltage (I _B = 0)	I _C = -10 mA		-30	9111		V
	V _{(BR)CER} (1)	Collector-emitter breakdown voltage (R _{BE} = 47 Ω)	I _C = -10 mA	cie	-40			V
	V _{(BR)CBO}	Collector-base breakdown voltage (I _E = 0)	I _C = -100 μA		-40			V
	V _{(BR)EBO}	Emitter-base breakdown voltage $(I_C = 0)$	I _E = -100 μA		-5			V
	V _{CE(sat)} (1)	Collector-emitter saturation voltage	$I_C = -0.5 \text{ A}$ $I_C = -1.2 \text{ A}$ $I_C = -2 \text{ A}$ $I_C = -3 \text{ A}$ $I_C = -3 \text{ A}$ $I_C = -3 \text{ A}$	$I_B = -20 \text{ mA}$ $I_B = -20 \text{ mA}$ $I_B = -100 \text{ mA}$			-0.15 -0.25 -0.5 -0.7	V V V
60/6	V _{BE(sat)} (1)	Base-emitter saturation voltage	I _C = -1A	I _B = -10mA		-0.8	-1	V
0/02	V _{BE(on)} (1)	Base-emitter on voltage	I _C = -1A	V _{CE} = -2V		-0.8	-1	V
	h _{FE} ⁽¹⁾	DC current gain	I _C = -2A	V _{CE} = -2V	100 100 100 100 90	200 200 160 130	400 400	

Electrical characteristics STX790A

		,				
Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
f _t	Transition frequency	$I_C = -50 \text{ mA}$ $V_{CE} = -5 \text{ V}$ $f = 50 \text{ MHz}$		100		MHz
t _d	Resistive load Delay time	$I_{C} = -3 \text{ A}$ $V_{CC} = -20 \text{ V}$		180	220	ns
t _r	Rise time	$I_{B1} = -I_{B2} = -60 \text{ mA}$		160	210	ns
t _s	Storage time	see Figure 8		250	300	ns
t _f	Fall time			80	100	ns

Table 4. Electrical characteristics (continued)

2.1 Electrical characteristics (curves)

Figure 2. DC current gain

Figure 3. DC current gain

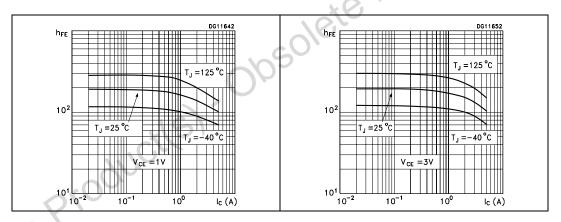
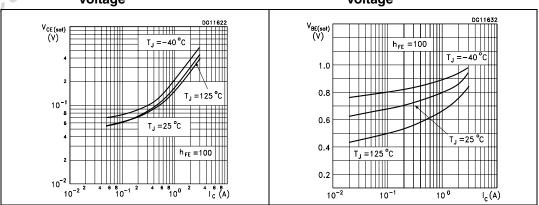


Figure 4. Collector-emitter saturation Figure 5. Base-emitter saturation voltage



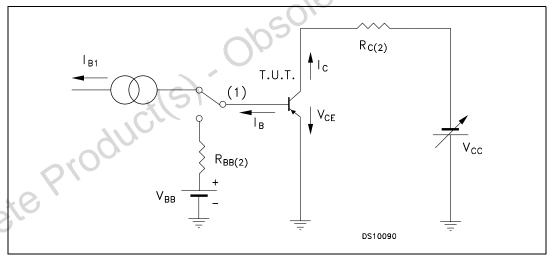
^{1.} Pulse duration = 300 μ s, duty cycle \leq 1.5%

DG11670 DG11680 t(ns)t(ns) $V_{CC} = 20 V$ $V_{CC} = 20 \text{ V}$ $h_{FE} = 50$ $t_p = 40 \mu s$ $h_{FE} = 50$ $t_p = 40 \mu s$ 500 500 400 400 ts 300 300 200 200 t _f 100 100 I_C(A) I_C(A) 0 0 0.5 0.5 1.5 2.0 1.5

Figure 6. Switching time resistive load Figure 7. Switching time resistive load

2.2 Test circuit

Figure 8. Resistive load switching test circuit



- 1. Fast electronic switch
- 2. Non-inductive resistor

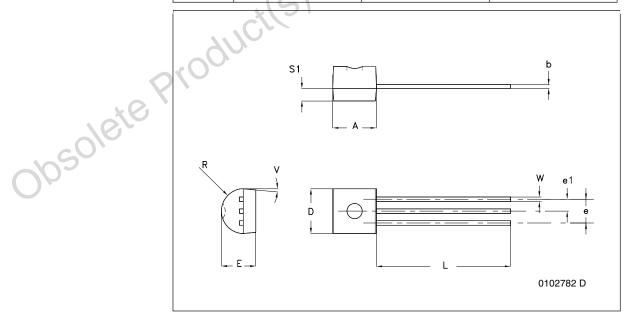
3 Package mechanical data

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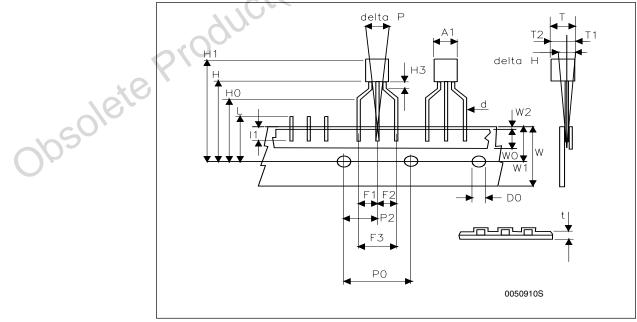
TO-92 bulk shipment mechanical data

DIM.	mm.				
Diw.	MIN.	ТҮР	MAX.		
А	4.32		4.95		
b	0.36		0.51		
D	4.45		4.95		
Е	3.30		3.94		
е	2.41		2.67		
e1	1.14	46	1.40		
L	12.70	7/6,	15.49		
R	2.16	-SO.	2.41		
S1	0.92	Q	1.52		
W	0.41		0.56		
V		5°			



TO-92 ammopack shipment (suffix"-AP") mechanical data

Dim.	mm				
Dilli.	Min	Тур	Max		
A1			4.80		
Т			3.80		
T1			1.60		
T2			2.30		
d			0.48		
P0	12.50	12.70	12.90		
P2	5.65	6.35	7.05		
F1,F2	2.44	2.54	2.94		
F3	4.98	5.08	5.48		
delta H	-2.00		2.00		
W	17.50	18.00	19.00		
W0	5.70	6.00	6.30		
W1	8.50	9.00	9.25		
W2			0.50		
Н	18.50		20.50		
H3	0.5	C 1	1.5		
H0	15.50	16.00	16.50		
H1			25.00		
D0	3.80	4.00	4.20		
t			0.90		
L			11.00		
l1	3.00				
delta P	-1.00		1.00		



STX790A Revision history

4 Revision history

Table 5. Document revision history

	Date	Revision	Changes
	24-Mar-2003	1	Initial release.
	29-Mar-2006	2	New template.
	25-Jun-2008	3	Updated TO-92 mechanical data.
	28-Apr-2009	4	Added new order code STX790AG-AP Table 1 on page 1.
Obsole	ie Pro	ductl	Added new order code STX790AG-AP Table 1 on page 1.

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