

# T1620T-8I, T1635T-8I

#### Snubberless™ 16 A Triac

Datasheet - production data

#### **Features**

- High static and dynamic commutation
- Three quadrants
- Snubberless device
- Package is RoHS (2002/95/EC) compliant
- Tab insulated, voltage = 2500 V rms
- UL certified (ref. file E81734)

#### **Applications**

- General purpose AC line load switching
- Home appliances:
  - Fan
  - Pump
  - Solenoid
- Lighting
- Heaters
- Inrush current limiting circuits
- Overvoltage crowbar protection circuits

## **Description**

Available in TO220AB-Ins. (ceramic insulated), the T1620T-8I, and T1635T-8I Triacs can be used as on/off or phase angle function controllers in general purpose AC switching.

These devices can be used without snubber (R + C networks) if the datasheet limits are respected.

Provides insulation rated at 2500 V rms (TO-220AB insulated package).

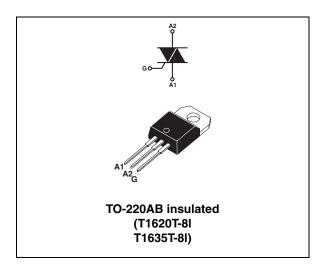


Table 1. Device summary

Order code	Quadrants	Value I <sub>GT</sub> (mA)
T1620T-8I	1 - 11 - 111	20
T1635T-8I	1 - 11 - 111	35

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Characteristics T1620T-8I, T1635T-8I

## 1 Characteristics

Table 2. Absolute maximum rating ( $T_j = 25$  °C, unless otherwise specified)

Symbol	Parameter				Unit	
	I <sub>T(RMS)</sub>   On-state rms current (full sine wave)		T <sub>c</sub> = 108 °C	16	Α	
IT(RMS)			T <sub>c</sub> = 119 °C	12	A	
	Non repetitive surge peak on-state current (full	F = 50 Hz	t <sub>p</sub> = 20 ms	120	۸	
I <sub>TSM</sub>	cycle, T <sub>j</sub> initial = 25 °C)	F = 60 Hz	$t_p = 16.7 \text{ ms}$	126	A	
l <sup>2</sup> t	I <sup>2</sup> t Value for fusing		t <sub>p</sub> = 10 ms	95	A <sup>2</sup> s	
V <sub>DRM</sub> /	$T_j = 150 ^{\circ}\text{C}$			600		
V <sub>RRM</sub>			T <sub>j</sub> = 125 °C	800	V	
V <sub>DSM</sub> , V <sub>RSM</sub>	Non repetitive surge peak off-state voltage	repetitive surge peak off-state voltage $t_p = 10 \text{ ms}$		900	V	
dl/dt	Critical rate of rise of on-state current $I_G = 2 \times I_{GT}$ $F = 100 \text{ H}$			100	A/µs	
I <sub>GM</sub>	Peak gate current $t_p = 20 \mu s$		T <sub>j</sub> = 150 °C	4	Α	
P <sub>G(AV)</sub>	Average gate power dissipation $T_j = 150 ^{\circ}\text{C}$			1	W	
T <sub>stg</sub>	Storage junction temperature range			-40 to +150 -40 to +150	°C	
T <sub>j</sub>	Operating junction temperature range			-40 10 +150		
T <sub>L</sub>	Lead temperature for soldering during 10 s (at 4 mm from case for TO220AB-ins.)			260	°C	
V <sub>ins</sub> (rms)	Insulation rms voltage, 1 minute, TO220AB ceramic insulated			2500	V	

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Table 3. Electrical characteristics ( $T_j = 25$  °C, unless otherwise specified)

Cymbal	Symbol Test conditions		Oughent	Quadrant		Value	
Symbol			Quadrant		T1620T	T1635T Unit	
I <sub>GT</sub> <sup>(1)</sup>	(1) V 40 V D 00 0		1 - 11 - 111	MIN.	1	1.75	mA
'GT ` ′	$V_D = 12 \text{ V}, R_L = 30 \Omega$		1 - 11 - 111	MAX.	20	35	mA
$V_{GT}$	$V_D = 12 \text{ V}, \text{ RL} = 30 \Omega$		All	MAX.	1.3		٧
$V_{GD}$	$V_D = 800 \text{ V}, R_L = 3.3 \text{ k}\Omega, T_j = 125 \text{ °C}$		All	MIN.	0.2		V
I <sub>H</sub> <sup>(1)</sup>	I <sub>T</sub> = 500 mA			MAX.	25	45	mA
	I <sub>L</sub> I <sub>G</sub> = 1.2 I <sub>GT</sub>		1 - 111	MAX.	35	55	mA
'L			II		40	65	
dV/dt (1)	$V_D = 67\% \times 800 \text{ V gate open}$ $T_j = 125 \text{ °C}$			MIN.	1000	2000	V/µs
u v/ut · /	$V_D = 67\% \text{ x } 600 \text{ V } \text{ gate open}$ $T_j = 150 \text{ °C}$			IVIIIN.	500	1000	ν/μδ
(dl/dt)c (1)	(dV/dt)c = snubberless (> 20 V/µs)	T <sub>j</sub> = 125 °C		MIN.	6	16	A/ms
	$T_j = 150 ^{\circ}\text{C}$			IVIII V.	4.5	12	7/113
t <sub>GT</sub>	gate controlled turn on time $I_{TM}$ = 13 A, $V_D$ = 400 V, $I_G$ = 100 mA, $dI_G/dt$ = 100 mA/ $\mu$ s, $R_L$ = 30 $\Omega$		1 - 11 - 111	TYP.	2	2	μs

<sup>1.</sup> For both polarities of A2 referenced to A1

Table 4. Static characteristics

Symbol	Test conditions			Value	Unit
V <sub>TM</sub> <sup>(1)</sup>	$I_{TM} = 22.6 \text{ A}, t_p = 380 \mu \text{s}$	$T_j = 25 ^{\circ}C$	MAX.	1.55	V
V <sub>to</sub> (1)	Threshold voltage	T <sub>j</sub> = 150 °C	MAX.	0.85	V
R <sub>d</sub> <sup>(1)</sup>	Dynamic resistance	T <sub>j</sub> = 150 °C	MAX.	30	mΩ
	V <sub>DRM</sub> = V <sub>RRM</sub> = 800 V	T <sub>j</sub> = 25 °C	MAX.	5	μΑ
I <sub>DRM</sub> I <sub>RRM</sub>		T <sub>j</sub> = 125 °C		1	mA
·HRIVI	V <sub>DRM</sub> = V <sub>RRM</sub> = 600 V	T <sub>j</sub> = 150 °C		3.6	IIIA

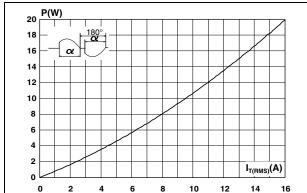
<sup>1.</sup> for both polarities of A2 referenced to A1

Table 5. Thermal resistance

Symbol	Parameter	Value	Unit
R <sub>th(j-c)</sub>	Junction to case (AC)	2.1	°C/W
R <sub>th(j-a)</sub>	Junction to ambient	60	°C/W

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Figure 1. Maximum power dissipation versus Figure 2. On-state rms current versus case on-state rms current (full cycle) temperature (full cycle)



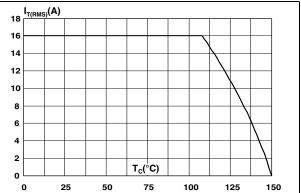
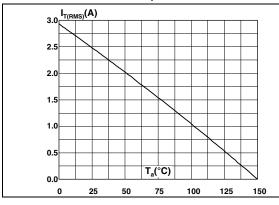


Figure 3. On-state rms current versus ambient temperature (free air convection)

Figure 4. Relative variation of thermal impedance versus pulse duration



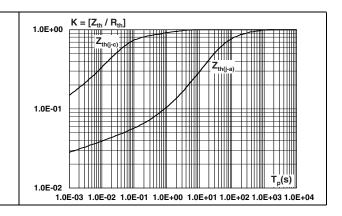
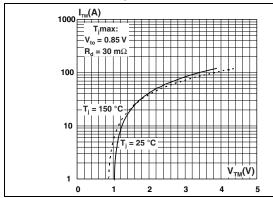
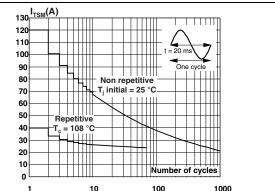


Figure 5. On-state characteristics (maximum Figure 6. Surge peak on-state current versus values) number of cycles



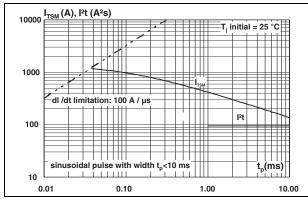


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Figure 7. Non repetitive surge peak on-state current and corresponding values of I<sup>2</sup>t

Figure 8. Relative variation of gate trigger current versus junction temperature



2.0 I<sub>GT</sub>[T<sub>j</sub>]/I<sub>GT</sub>[T<sub>j</sub> = 25 °C]

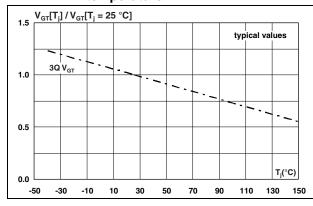
1.5 3Q I<sub>GT</sub> Q3 typical values

1.0 T<sub>j</sub>(°C)

-50 -30 -10 10 30 50 70 90 110 130 150

Figure 9. Relative variation of gate trigger voltage versus junction temperature

Figure 10. Relative variation of holding current and latching current versus junction temperature



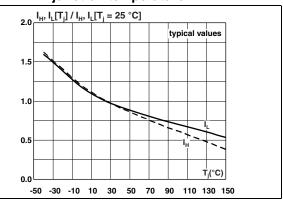
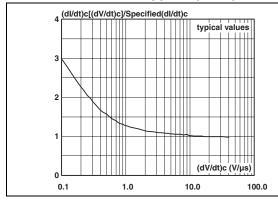
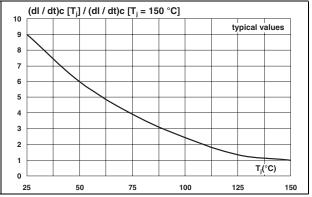


Figure 11. Relative variation of critical rate of Figure 12. decrease of main current (di/dt)c versus reapplied (dV/dt)c

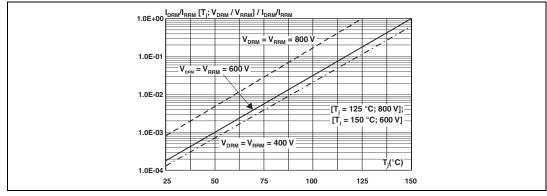
igure 12. Relative variation of critical rate of decrease of main current (di/dt)c versus junction temperature





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Figure 13. Relative variation of leakage current versus junction temperature for different values of blocking voltage

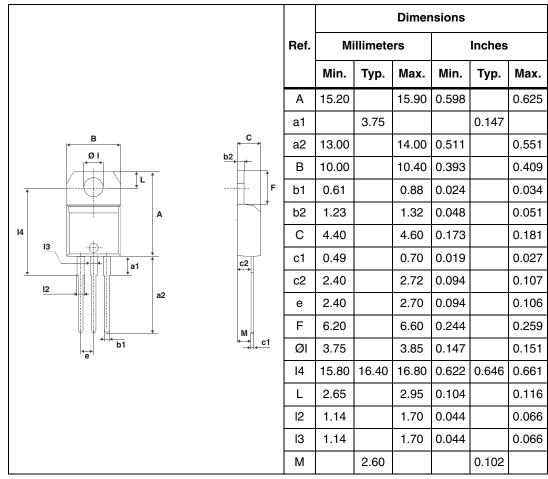


## 2 Package information

- Epoxy meets UL94, V0
- Recommended torque value: 0.4 to 0.6 N⋅m

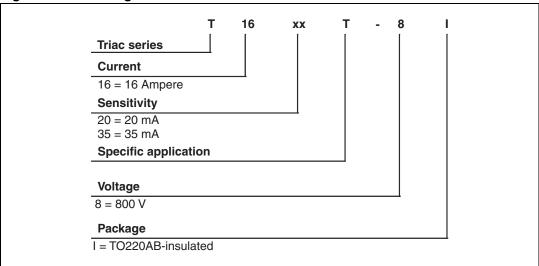
In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: <a href="https://www.st.com">www.st.com</a>. ECOPACK<sup>®</sup> is an ST trademark.

Table 6. TO-220AB insulated dimensions



# 3 Ordering information scheme

Figure 14. Ordering information scheme



# 4 Ordering information

Table 7. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
T1620T-8I	T1620T-8I	TO-220AB insulated	2.3	50	Tube
T1635T-8I	T1635T-8I	TO-220AB insulated	2.3	50	Tube

## 5 Revision history

Table 8. Document revision history

Date	Revision	Changes
20-Jan-2012	1	First issue.
25-Apr-2012	2	Updated UL certification.

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