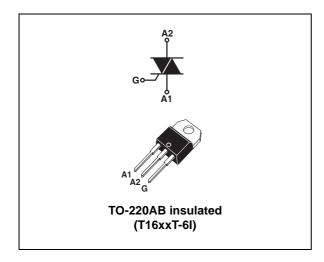


## Snubberless™, logic level and standard 16 A Triacs

**Datasheet - production data** 



#### **Features**

- Medium current Triac
- · High static and dynamic commutation
- · Low thermal resistance with clip bonding
- Packages is RoHS (2002/95/EC) compliant
- 600 V V<sub>RM</sub>
- UL certified (ref. file E81734)

### **Applications**

- · Value sensitive application
- General purpose ac line load switching
- Motor control circuits in power tools
- Small home appliances, lighting
- Inrush current limiting circuits
- Overvoltage crowbar protection

#### **Description**

Available in through-hole, the T16T series of Triacs can be used as on/off or phase angle control function in general purpose ac switching where high commutation capability is required.

This series can be designed-in in many value sensitive appliances thanks to the parameters guidance provided in the following pages.

Provides insulation rated at 2500 V rms (TO-220AB insulated package).

**Table 1. Device summary** 

Order code	Symbol	Value	
T1610T-6I	I <sub>GT</sub> 3Q logic level	10 mA	
T1620T-6I T1635T-6I	I <sub>GT</sub> 3Q Snubberless	20 / 35 mA	

TM: Snubberless is a trademark of STMicroelectronics

Characteristics T16T

## 1 Characteristics

Table 2. Absolute maximum ratings (limiting values;  $T_j$  = 25 °C, unless otherwise specified)

Symbol	Parameter				Unit
I <sub>T(RMS)</sub>	On-state rms current (full sine wave) $T_c = 86  ^{\circ}\text{C}$		16	Α	
I	Non repetitive surge peak on-state current (full	F = 50 Hz	t <sub>p</sub> = 20 ms	120	Α
I <sub>TSM</sub>	cycle, T <sub>j</sub> initial = 25 °C)	F = 60 Hz	$t_p = 16.7 \text{ ms}$	126	A
l <sup>2</sup> t	$I^2t$ Value for fusing $t_p = 10 \text{ ms}$			105	A <sup>2</sup> s
dI/dt	Critical rate of rise of on-state current $I_G = 2 \times I_{GT}$ $F = 60 \text{ Hz}$ $T_j = 125 \text{ °C}$		50	A/µs	
V <sub>DSM</sub> / V <sub>RSM</sub>	Non repetitive surge peak off-state voltage $t_p = 10 \text{ ms} \qquad T_j = 25 \text{ °C}$		V <sub>DRM</sub> /V <sub>RRM</sub> + 100	V	
I <sub>GM</sub>	Peak gate current $t_p = 20 \mu s$ $T_j = 125 °C$		4	Α	
P <sub>G(AV)</sub>	Average gate power dissipation $T_j = 125 ^{\circ}\text{C}$			1	W
T <sub>stg</sub>	Storage junction temperature range			- 40 to + 150	°C
Tj	Operating junction temperature range			- 40 to + 125	°C

T16T Characteristics

Table 3. Electrical characteristics ( $T_j = 25$  °C, unless otherwise specified)

Obl	Test conditions	0		T16xxT			l lm!4
Symbol		Quadrant		T1610T	T1620T	T1635T	Unit
I <sub>GT</sub> <sup>(1)</sup>	V 42 V D 20 W	1 - 11 - 111	MAX.	10	20	35	m 1
'GT`′	$V_D = 12 \text{ V}  R_L = 30 \text{ W}$	IV	WAX.				mA
V <sub>GT</sub>	$V_D = V_{DRM}, R_L = 3.3 \text{ kW},$ $T_j = 25 \text{ °C}$	ALL	MAX.		1.3		V
V <sub>GD</sub>	$V_D = V_{DRM}, R_L = 3.3 \text{ kW},$ $T_j = 125 \text{ °C}$				V		
I <sub>H</sub> <sup>(2)</sup>	I <sub>T</sub> = 500 mA		MAX.	12	25	40	mA
	I <sub>G</sub> = 1.2 I <sub>GT</sub>	1 - 111	MAX.	20	35	50	mA
I <sub>L</sub> I <sub>G</sub>		IV					
		II		30	40	80	
dV/dt <sup>(2)</sup>	V- = 67% V gate open	T <sub>j</sub> = 125 °C	MIN.	100	1000	2000	V/µs
u v/ui · ·	$V_D = 67\% V_{DRM,}$ gate open	$T_j = 150  ^{\circ}C^{(3)}$	IVIIIN.	20	500	1000	v/µS
	$(dV/dt)c = 0.1 V/\mu s$			8			
	$(dV/dt)c = 10 V/\mu s$	T <sub>j</sub> = 125 °C		4			
(di/dt)c (2)	Without snubber		MIN.		6	16	A/ms
(di/dt/c · /	(dV/dt)c = 0.1 V/μs			3			
	(dV/dt)c = 10 V/μs	$T_j = 150  ^{\circ}C^{(3)}$		1			
	Without snubber				3	12	

<sup>1.</sup> minimum  $I_{\mbox{\footnotesize GT}}$  is guaranted at 5% of  $I_{\mbox{\footnotesize GT}}$  max.

**Table 4. Static characteristics** 

Symbol	Test conditions			Value	Unit
V <sub>T</sub> <sup>(1)</sup>	$I_{TM} = 22.6 \text{ A}, t_p = 380 \ \mu\text{s}$	T <sub>j</sub> = 25 °C	MAX.	1.55	V
V <sub>TO</sub> (1)	Threshold voltage	T <sub>j</sub> = 125 °C	MAX.	0.85	V
R <sub>D</sub> <sup>(1)</sup>	Dynamic resistance	T <sub>j</sub> = 125 °C	MAX.	30	mΩ
	$V_{DRM} = V_{RRM}$	T <sub>j</sub> = 25 °C	MAX.	5	μA
I <sub>DRM</sub>		T <sub>j</sub> = 125 °C		1	^
IRRM	$V_D = 0.9 \times V_{DRM}$	$T_j = 150  ^{\circ}C^{(2)}$	TYP.	1.9	mA

<sup>1.</sup> for both polarities of A2 referenced to A1.

<sup>2.</sup> for both polarities of A2 referenced to A1.

<sup>3.</sup> derating information for excess temperature above  $T_i$  max.

<sup>2.</sup> derating information for excess temperature above  $T_j \, \text{max}$ .

Characteristics T16T

Table 5. Thermal resistance

Symbol	Parameter	Value	Unit
R <sub>th(j-c)</sub>	Junction to case (AC)	2.1	°C/W
R <sub>th(j-a)</sub>	Junction to ambient (DC)	60	°C/W

Figure 1. Maximum power dissipation versus rms on-state current (full cycle)

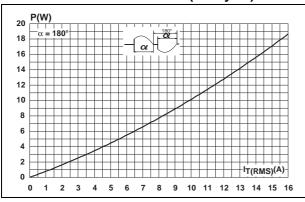


Figure 2. On-state rms current versus case temperature (full cycle)

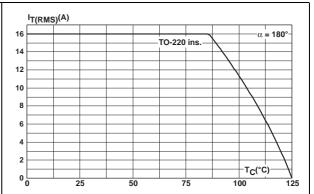


Figure 3. On-state rms current versus ambient temperature

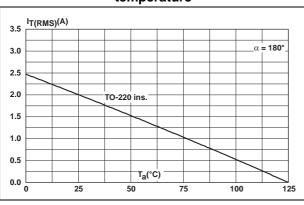


Figure 4. Relative variation of thermal impedance versus pulse duration

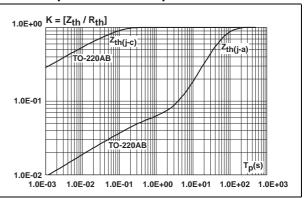


Figure 5. On state characteristics (maximum values)

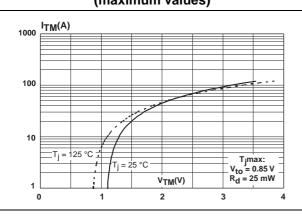
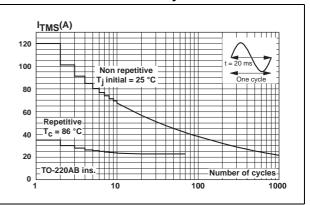


Figure 6. Surge peak on state current versus number of cycles



T16T Characteristics

Figure 7. Non repetitive surge peak on state current for a sinusoidal

Figure 8. Relative variation of gate trigger current and gate trigger voltage versus junction temperature

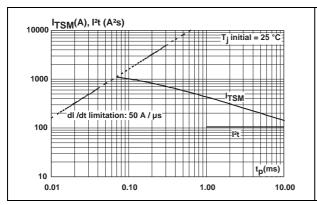
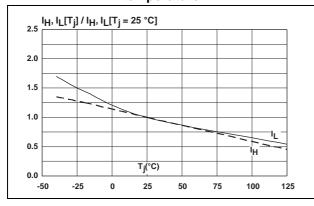


Figure 9. Relative variation of holding current and latching current versus junction temperature

Figure 10. Relative variation of critical rate of decrease of main current versus junction temperature



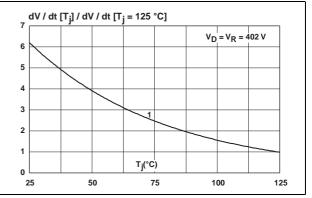
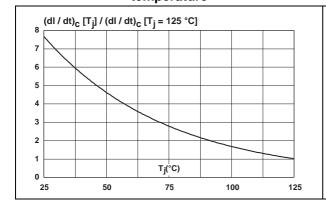
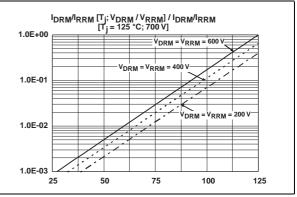


Figure 11. Relative variation of critical rate of decrease of main current versus junction temperature

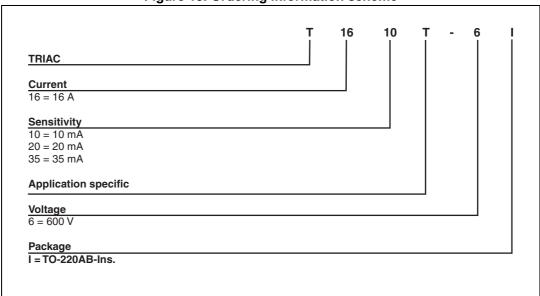
Figure 12. Leakage current versus junction temperature for different values of blocking voltage (typical values)





# 2 Ordering information scheme

Figure 13. Ordering information scheme



## 3 Package mechanical data

- Epoxy meets UL94, V0
- Lead-free packages

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: <a href="https://www.st.com">www.st.com</a>. ECOPACK<sup>®</sup> is an ST trademark.

**Dimensions** Ref. **Millimeters** Inches Min. Тур. Max. Min. Тур. Max. Α 15.20 15.90 0.598 0.625 a1 3.75 0.147 a2 13.00 14.00 0.511 0.551 Ø١ В 10.00 10.40 0.393 0.409 0.024 0.034 b1 0.61 0.88 1.23 1.32 0.048 0.051 b2 14 С 4.40 4.60 0.173 0.181 0.49 0.70 0.019 0.027 c1 c2 2.72 0.094 0.107 c2 2.40 2.40 2.70 0.094 0.106 е F 0.259 6.20 6.60 0.244 0.151 Ø١ 3.75 3.85 0.147 0.661 14 15.80 16.40 16.80 0.622 0.646 L 2.65 2.95 0.104 0.116 12 1.14 1.70 0.044 0.066 13 1.14 1.70 0.044 0.066 0.102 Μ 2.60

Table 6. TO-220AB insulated dimensions



Ordering information T16T

# 4 Ordering information

**Table 7. Ordering information** 

Order code	Marking	Package	Weight	Base qty	Delivery mode
T1610T-6I	T1610T-6I				
T1620T-6I	T1620T-6I	TO-220AB ins.	2.3 g	50	Tube
T1635T-6I	T1635T-6I				

# 5 Revision history

Table 8. Document revision history

Date	Revision	Changes
03-Dec-2009	1	Initial release.
18-Jan-2010	2	Updated pag.1.
19-Jun-2014	3	Updated features in cover page.

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