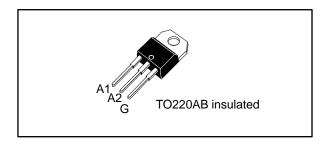


8 A Snubberless™ Triac

Datasheet - production data



Features

- High static dV/dt
- High dynamic commutation
- 150 °C maximum T_i
- Three quadrants
- Built-in ceramic for tab insulation
- Compliance to UL1557 standard (ref : E81734)
- ECOPACK®2 compliant component
- Complies with UL94,V0
- Surge capability V_{DSM}, V_{RSM} = 900 V

Benefits

- Device is less likely to have false turn-on thanks to high dV/dt
- Better turn-off in high temperature environments thanks to (dl/dt)c
- Increase of thermal margin due to extended working T_j up to 150 °C
- Better thermal resistance due to the ceramic inside the package

Applications

- General purpose AC line load switching
- Motor control circuits
- Home appliances
- Heating
- Lighting
- Inrush current limiting circuits
- Overvoltage crowbar protection

Description

Available in through-hole package, the T835T-8I Triac can be used for the on/off or phase angle control function in general purpose AC switching where high commutation capability is required. This device can be used without a snubber RC circuit when the limits defined are respected.

TO-220AB insulated provides tab insulation, UL1557 certified, rated at 2.5 kV RMS and UL-94, V0 resin compliance.

Package environmentally friendly ECOPACK[®]2 graded (RoHS and Halogen Free compliance).

Snubberless™ is a trademark of STMicroelectronics.

Figure 1: Functional diagram

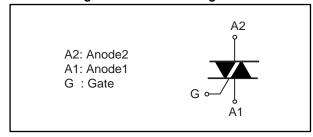


Table 1: Device summary

Symbol	Value	Unit
I _{T(RMS)}	8	Α
V _{DRM} /V _{RRM}	800	V
V _{DSM} /V _{RSM}	900	V
lgт	35	mA

Characteristics T835T-8I

1 Characteristics

Table 2: Absolute maximum ratings (limiting values)

Symbol	Paran	Value	Unit			
I _{T(RMS)}	RMS on-state current (full sine wave) T _c = 118 °C			8	Α	
l=a	Non repetitive surge peak on-state current,		$t_p = 16.7 \text{ ms}$	63	А	
I _{TSM}	T_j initial = 25 °C		t _p = 20 ms	60	A	
l ² t	I^2t value for fusing, $t_p = 10 \text{ ms}$		T _j initial = 25 °C	24	A ² s	
dl/dt	Critical rate of rise of on-state current, $I_G = 2 \times I_{GT}$, tr $\leq 100 \text{ ns}$	T _j = 150 °C	f = 100 Hz	100	A/µs	
\/\/\/	Repetitive peak off-state voltage		T _j = 150 °C	600	V	
V _{DRM} /V _{RRM}			T _j = 125 °C	800	V	
V _{DSM} /V _{RSM}	Non Repetitive peak off-state volta	t _p = 10 ms	900	V		
I _{GM}	Peak gate current	t _p = 20 μs	T _j = 150 °C	4	Α	
P _{G(AV)}	Average gate power dissipation	1	W			
T _{stg}	Storage junction temperature range			-40 to +150	°C	
Tj	Operating junction temperature range			-40 to +150	°C	
TL	Maximum lead temperature for soldering during 10 s			260	°C	
Vins	Insulation RMS voltage, 1 minute,	2.5	kV			

Table 3: Electrical characteristics (T_j = 25 °C, unless otherwise specified)

Symbol	Test Conditions				Unit
I _{GT} ⁽¹⁾	$V_D = 12 \text{ V}, \text{ R}_L = 30 \Omega$	1 - 11 - 111	Min.	1.75	mA
IGT ^(*)	$V_D = 12 \text{ V}, \text{ R}_L = 30 \Omega$	1 - 11 - 111	Max.	35	mA
V_{GT}	$V_D = 12 \text{ V}, R_L = 30 \Omega$	1 - 11 - 111	Max.	1.3	V
V _{GD}	$V_D = V_{DRM}, R_L = 3.3 \text{ k}\Omega, T_j = 150 \text{ °C}$	1 - 11 - 111	Min.	0.2	V
IL	lg = 1.2 x lgr	I - III	Max.	60	mA
	IG = 1.2 X IGT	II	Max.	70	mA
lн	I _T = 500 mA, gate open		Max.	40	mA
dV/dt	V _D = 536 V, gate open	T _j = 125 °C	Min.	2000	V/µs
uv/ut	V _D = 402 V, gate open	T _j = 150 °C	IVIII I.	1000	V/µs
(d1/dt) o	Without snubber, (dV/dt)c > 20 V/µs	T _j = 125 °C	Min.	8	A/ms
(dl/dt)c	vviitiout strubbet, (a v/at/c > 20 v/µs	T _j = 150 °C		4	A/ms

Notes:

 $^{^{(1)}}$ For both polarities of A2 referenced to A1.

T835T-8I Characteristics

Table 4: Static characteristics

Symbol	Test conditions	Tj		Value	Unit
V _{TM} ⁽¹⁾	I _T = 11.3 A, t _p = 380 μs	25 °C	Max.	1.60	٧
V _{TO}	Threshold on-state voltage	150 °C	Max.	0.87	٧
R _D	Dynamic resistance	150 °C	Max.	80	mΩ
	V _{DRM} = V _{RRM} = 800 V	25 °C	Max.	5	μΑ
IDRM/IRRM	VDRM = VRRM = 800 V	125°C	iviax.	1.0	mA
	$V_{DRM} = V_{RRM} = 600 \text{ V}$		Max.	2.5	mA

Notes:

Table 5: Thermal resistance

Symbol	Parameter Value			
R _{th(j-c)}	Junction to case (AC)	Max.	2.8	90044
R _{th(j-a)}	Junction to ambient	60	°C/W	

 $^{^{(1)}}$ For both polarities of A2 referenced to A1.

Characteristics T835T-8I

1.1 Characteristics (curves)

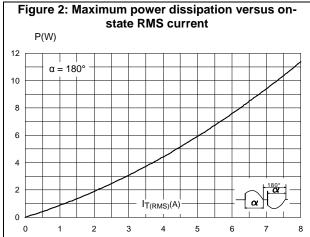
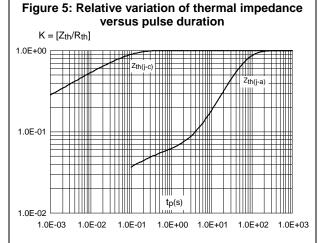


Figure 3: On-state RMS current versus case temperature

I_{T(RMS)}(A)

10
9
8
7
6
5
4
3
2
1
0
0
25
50
75
100
125
150



T835T-8I Characteristics

Figure 6: Relative variation of gate trigger voltage and current versus junction temperature (typical values)

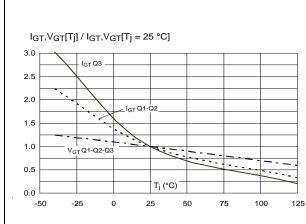


Figure 7: Relative variation of holding current and latching current versus junction temperature (typical values)

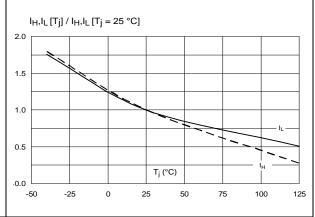


Figure 8: Surge peak on-state current versus number of cycles

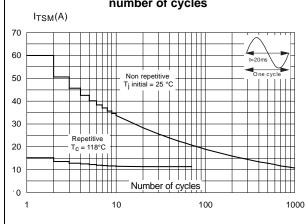


Figure 9: Non repetitive surge peak on-state current for a sinusoidal pulse with width tp < 10 ms

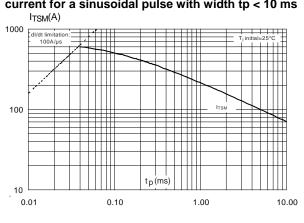


Figure 10: On-state characteristics (maximum values)

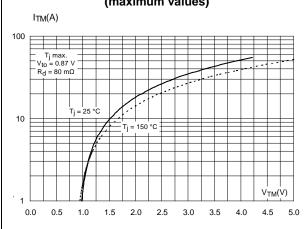
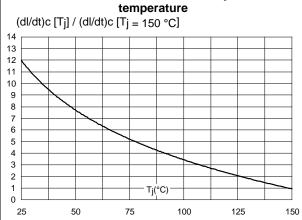
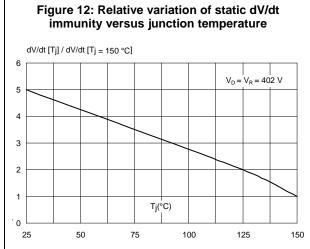
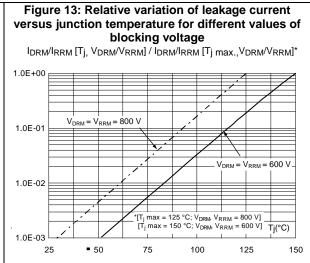


Figure 11: Relative variation of critical rate of decrease of main current versus junction



Characteristics T835T-8I





T835T-8I Package information

2 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: **www.st.com**. ECOPACK® is an ST trademark.

- ECOPACK®2 (Lead-free plating and Halogen free package compliance)
- Lead-free package leads finishing
- Halogen-free molding compound resin meets UL94 standard level V0.
- Recommended torque (for through-hole package): 0.4 to 0.6 N⋅m

2.1 TO-220AB Insulated package information

Figure 14: TO-220AB Insulated package outline $\overline{\mathsf{c}}$ В **b2** Resin gate 0.5 mm max. protusion(1) F Α 14 13 **c2** a1 12 a2 М с1 Resin gate 0.5 mm **b1** max. protusion(1) (1)Resin gate position accepted in one of the two positions or in the symmetrical opposites.

Table 6: TO-220AB Insulated package mechanical data

	Dimensions					
Ref.		Millimeters			Inches ⁽¹⁾	
	Min.	Тур.	Max.	Min.	Тур.	Max.
Α	15.20		15.90	0.5984		0.6260
a1		3.75			0.1476	
a2	13.00		14.00	0.5118		0.5512
В	10.00		10.40	0.3937		0.4094
b1	0.61		0.88	0.0240		0.0346
b2	1.23		1.32	0.0484		0.0520
С	4.40		4.60	0.1732		0.1811
c1	0.49		0.70	0.0193		0.0276
c2	2.40		2.72	0.0945		0.1071
е	2.40		2.70	0.0945		0.1063
F	6.20		6.60	0.2441		0.2598
1	3.73		3.88	0.1469		0.1528
L	2.65		2.95	0.1043		0.1161
12	1.14		1.70	0.0449		0.0669
13	1.14		1.70	0.0449		0.0669
14	15.80	16.40	16.80	0.6220	0.6457	0.6614
М		2.6			0.1024	

Notes:

⁽¹⁾Inch dimensions are for reference only.

T835T-8I Ordering information

3 Ordering information

Figure 15: Ordering information scheme

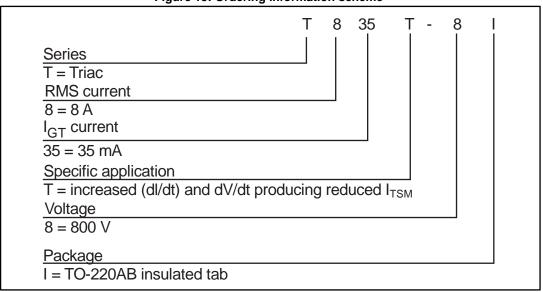


Table 7: Ordering information

Order code	Marking	Package	Weight	Base qty.	Delivery mode
T835T-8I	T835T-8I	TO-220AB insulated	2.3 g	50	Tube

4 Revision history

Table 8: Document revision history

Date	Revision	Changes
17-Oct-2017	1	Initial release.
06-Nov-2017	2	Updated Table 4: "Static characteristics".

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