

## TN1205T-600

### 12 A SCR

### Features

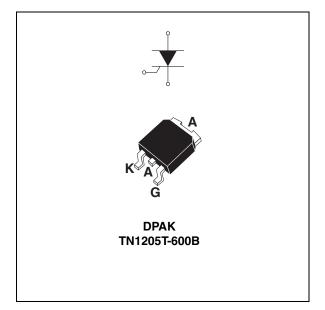
High current density per square mm

### **Applications**

- Overvoltage crowbar protection
- Motor control circuits in power tools and kitchen aids
- Inrush current limiting circuits

### Description

This device is mounted in DPAK and intended for use in applications such as voltage regulators circuits for motorbikes, overvoltage crowbar protection, motor control circuits in power tools and capacitive discharge ignition.



### Table 1.Device summary

I <sub>T(rms)</sub>	12 A	
V <sub>DRM</sub> /V <sub>RRM</sub>	600 V	
I <sub>GT</sub>	2 to 5 mA	

## 1 Characteristics

### Table 2.Absolute ratings<sup>(1)</sup>

Symbol	Parameter	Value	Unit		
I <sub>T(RMS)</sub>	On-state rms current (180 °C conduction angle)		T <sub>c</sub> = 103 °C	12	А
I <sub>T(AV)</sub>	Average on-state current(180 °C conduction ang	le)	T <sub>c</sub> = 103 °C	8	А
I <sub>TSM</sub>	Non repetitive surge peak on-state current		$\begin{array}{l}t_{p}=8.3 \text{ ms}\\t_{p}=10 \text{ ms}\end{array}$	120 115	А
I <sup>2</sup> T	$I^2$ T value for fusing $t_p = 10 \text{ ms}$				A <sup>2</sup> s
di/dt	Critical rate of rise of on-state current $I_G = 2 \times I_{GT}$ , tr $\leq 100 \text{ ns}$	F = 60 Hz	T <sub>j</sub> = 125 °C	50	A/µs
I <sub>GM</sub>	Peak gate current $t_p = 20 \ \mu s$ $T_c = 125 \ ^{\circ}C$				А
P <sub>G(AV)</sub>	Average gate power dissipation $T_j = 125 \text{ °C}$				W
T <sub>stg</sub>	Storage junction temperature range				о°С
Тj	Operating junction temperature range			-40 to + 125	U

1.  $T_j = 25 \ ^{\circ}C$ , unless otherwise specified

### Table 3. Electrical characteristics<sup>(1)</sup>

Symbol	Test conditions			Тур.	Max.	Unit
I <sub>GT</sub>	$V_D$ = 12 V, R <sub>L</sub> = 33 $\Omega$		2		5	mA
$V_{GT}$	$V_D = 12 \text{ V}, \text{ R}_L = 33 \Omega$				1.3	V
$V_{GD}$	$V_D = V_{DRM,} R_L = 3.3 \text{ k}\Omega$	T <sub>j</sub> = 125 °C	0.2			V
Ι <sub>Η</sub>	I <sub>T</sub> = 500 mA gate open	•			15	mA
١L	$I_{G} = 1.2 I_{GT}$				30	mA
dV/dt	V <sub>D</sub> = 67% V <sub>DRM</sub> gate open	T <sub>j</sub> = 125 °C	100			V/µs
t <sub>GT</sub>	Gate controlled turn on time $I_{TM} = 40 \text{ A}, V_D = V_{DRM(MAX)}, I_{GT} = 100 \text{ mA}$ $dI_G/dt = 5 \text{ A}/\mu\text{s}, R_G = 68 \Omega$			1.2		μs
t <sub>q</sub>	Circuit commutated turn off time $V_D = 67\% V_{DRM(MAX)}$ , $T_j = 125$ °C, $I_{TM} = 20$ A, $V_R = 25$ V $dI_T/dt = 30 \text{ A/µS}$ , $dV_D/dt = 50 \text{ V/µs}$ , $R_{GK} = 100 \Omega$			55		μs
$V_{TM}$	I <sub>TM</sub> = 24 A, T <sub>p</sub> = 380 μs				1.6	V
$V_{T0}$	Threshold voltage $T_j = 125 \text{ °C}$				0.85	V
R <sub>d</sub>	Dynamyc restistance	T <sub>j</sub> = 125 °C			30	mΩ
I <sub>DRM</sub>	V – V	T <sub>j</sub> = 25 °C			5	μA
I <sub>RRM</sub>	V <sub>DRM</sub> = V <sub>RRM</sub>	T <sub>j</sub> = 125 °C			2	mA

1.  $T_j = 25$  °C, unless otherwise specified

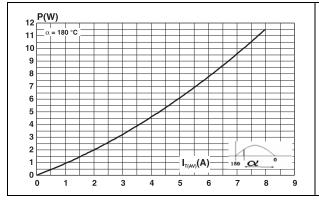


#### Table 4.Thermal resistance

Symbol	Parameter		Value	Unit
R <sub>th(j-c)</sub>	Junction to case (DC)		1.8	°C/W
R <sub>th(j-a)</sub>	Junction to ambient (DC)	$S^{(1)} = 0.5 \text{ cm}^2$	70	°C/W

1. S = Copper surface under tab.

#### Figure 1. Maximum average power dissipation versus average on-state current



## Figure 3. Average DC on-state current versus ambient temperature

## Figure 2. Average and DC on-state current versus case temperature

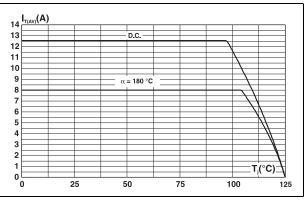
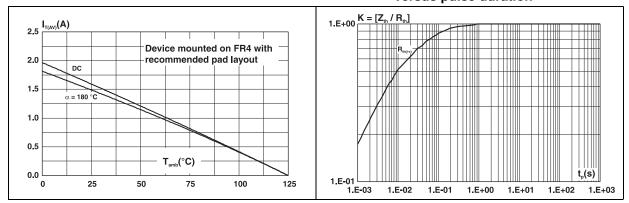


Figure 4. Relative variation of thermal impedance junction to case versus pulse duration





# Figure 5. Relative variation of thermal impedance junction to ambient versus pulse duration

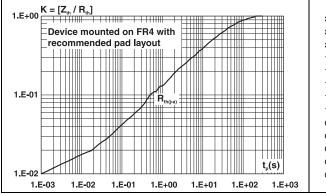
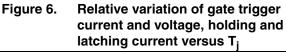


Figure 7. Surge peak on-state current versus number of cycles



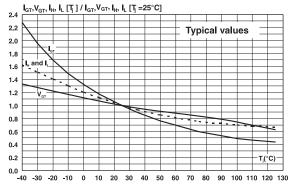


Figure 8. Non-repetitive surge peak on-state current for a sinusoidal pulse, and corresponding values of l<sup>2</sup>t

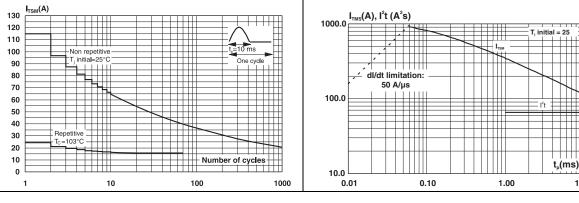
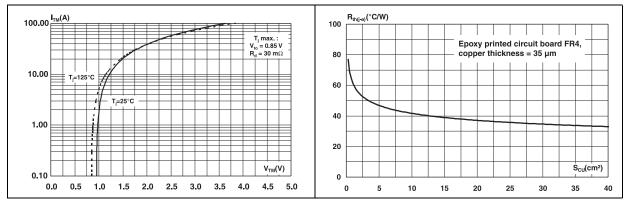


Figure 9. On-state characteristics (maximum values)

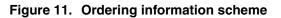
Figure 10. Thermal resistance junction to ambient versus copper surface under tab





10.00

## 2 Ordering information scheme



600 = 600 V Package		Standard SCR seriesCurrent12 = 12 ASensitivity05 = 5 mAApplication specificVoltage	TN 12 05 T - 600 B (-TR)
	B = DPAK	600 = 600 V	

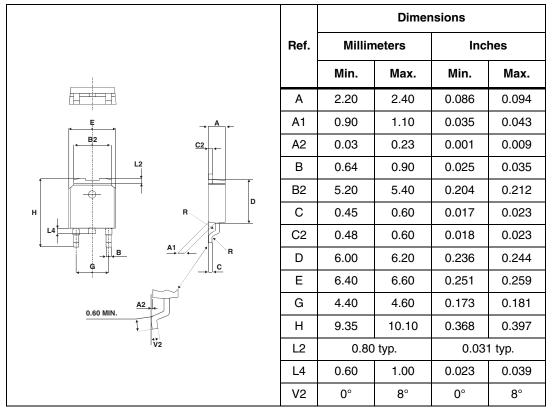


### 3 Package information

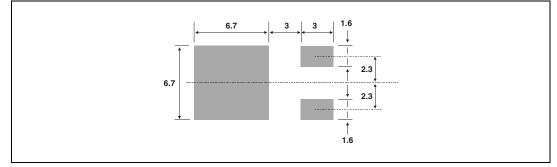
- Epoxy meets UL94, V0
- Lead-free package

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Table 5. DPAK dimensions



### Figure 12. Footprint (dimensions in mm)





## 4 Ordering information

### Table 6. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
TN1205T-600B	TN12 05T6	DPAK	0.3g	75	Tube
TN1205T-600B-TR	TN12 05T6	DFAR	0.5 <u>y</u>	2500	Tape and reel

## 5 Revision history

### Table 7.Document revision history

Date	Revision	Changes
01-Oct-2009	1	Initial release.



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 057219R
 T1190N16TOF VT
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 T201N70TOH
 T700N22TOF
 TT250N12KOF-K
 VS-110RKI40
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