

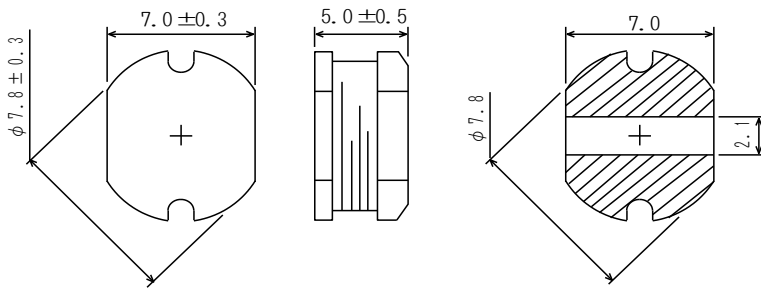
SMD Power Inductor CD75/T125



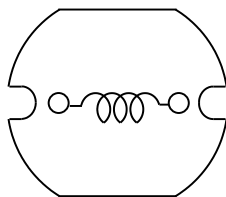
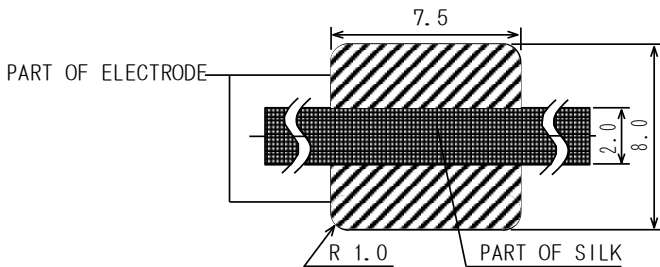
Description

- Ferrite drum core construction.
- Magnetically unshielded.
- L × W × H: 8.1 × 7.3 × 5.5mm Max.
- Product weight: 0.76g(Ref.)
- Moisture Sensitivity Level: 1
- RoHS compliance.
- Qualification to AEC-Q200.

Dimension - [mm]



Land pattern and Schematics - [mm]



Environmental Data

- Operating temperature range: -40°C ~ +125°C (including coil's self temperature rise)
- Storage temperature range: -40°C ~ +125°C
- Solder reflow temperature: 260 °C peak.

Packaging

- Carrier tape and reel packaging.
- 13" diameter reel
- 500pcs per reel

Applications

- Automotive and other high temperature, high reliability application.



Electrical Characteristics

Part No.	Stamp	Inductance [Within] ※1	D.C.R. (Ω) [Max.] (at 20°C)	Saturation current (A) (at 20°C) ※2	Temperature rise current (A) ※3
CD75T125NP-100KC	100K	10 μ H \pm 10%	0.07	2.80	3.18
CD75T125NP-120KC	120K	12 μ H \pm 10%	0.08	2.45	3.15
CD75T125NP-150KC	150K	15 μ H \pm 10%	0.09	2.16	3.02
CD75T125NP-180KC	180K	18 μ H \pm 10%	0.10	2.00	2.80
CD75T125NP-220KC	220K	22 μ H \pm 10%	0.11	1.80	2.63
CD75T125NP-270KC	270K	27 μ H \pm 10%	0.12	1.66	2.46
CD75T125NP-330KC	330K	33 μ H \pm 10%	0.13	1.51	2.35
CD75T125NP-390KC	390K	39 μ H \pm 10%	0.16	1.39	2.05
CD75T125NP-470KC	470K	47 μ H \pm 10%	0.18	1.26	1.95
CD75T125NP-560KC	560K	56 μ H \pm 10%	0.24	1.22	1.72
CD75T125NP-680KC	680K	68 μ H \pm 10%	0.28	1.06	1.48
CD75T125NP-820KC	820K	82 μ H \pm 10%	0.37	0.95	1.34
CD75T125NP-101KC	101K	100 μ H \pm 10%	0.43	0.90	1.29
CD75T125NP-121KC	121K	120 μ H \pm 10%	0.47	0.87	1.17
CD75T125NP-151KC	151K	150 μ H \pm 10%	0.64	0.73	0.96
CD75T125NP-181KC	181K	180 μ H \pm 10%	0.71	0.63	0.93
CD75T125NP-221KC	221K	220 μ H \pm 10%	0.96	0.61	0.80
CD75T125NP-271KC	271K	270 μ H \pm 10%	1.11	0.54	0.74
CD75T125NP-331KC	331K	330 μ H \pm 10%	1.26	0.51	0.72
CD75T125NP-391KC	391K	390 μ H \pm 10%	1.77	0.47	0.64
CD75T125NP-471KC	471K	470 μ H \pm 10%	1.96	0.43	0.60

※1 Measuring frequency 10 μ H ~ 82 μ H ; at 2.52 MHz
100 μ H ~ 820 μ H ; at 1 kHz

※2 Saturation current: This indicates the actual value of D.C. current when the inductance becomes 10% lower than its initial value.(Ta=20°C)

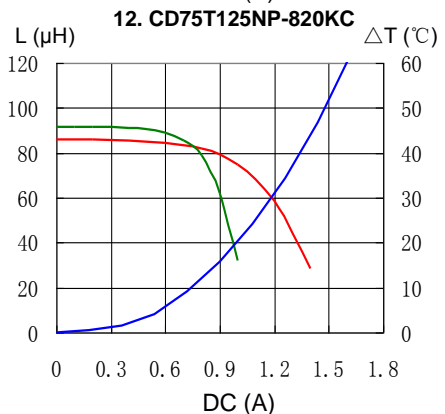
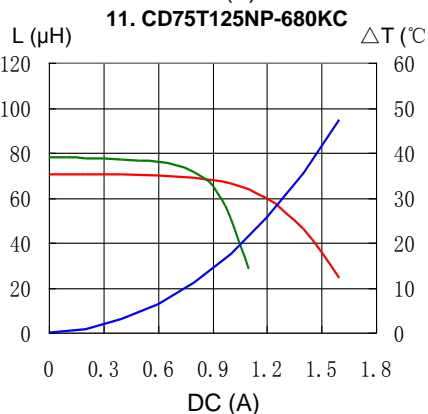
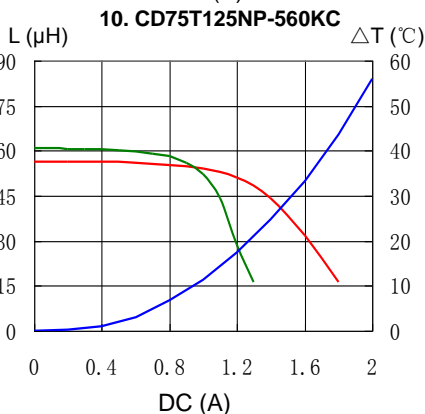
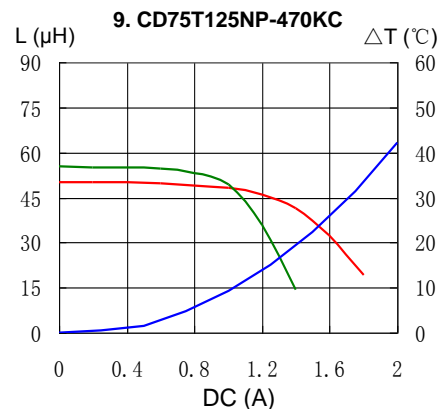
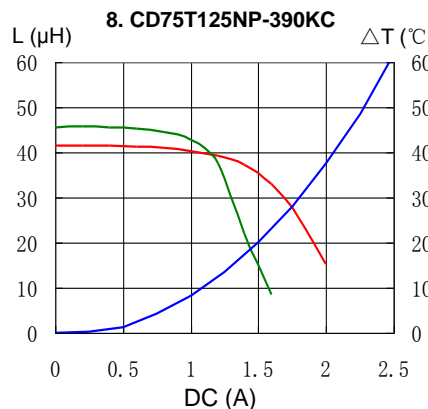
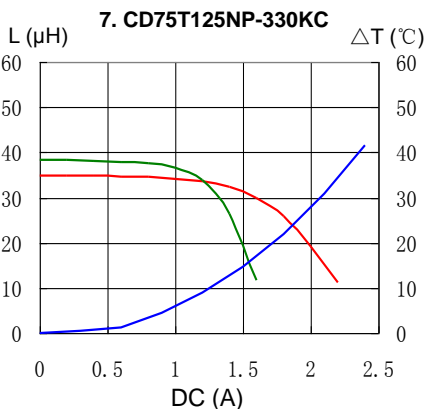
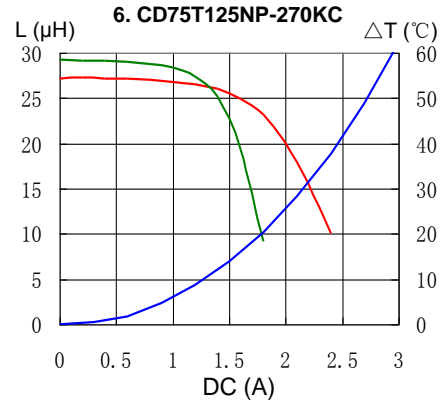
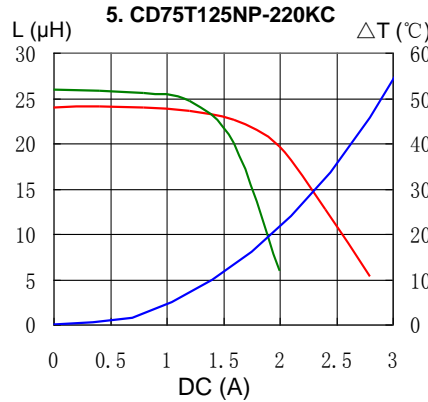
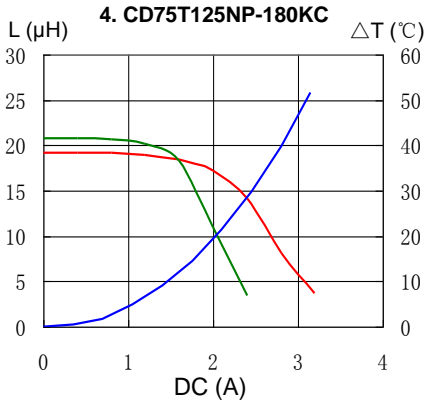
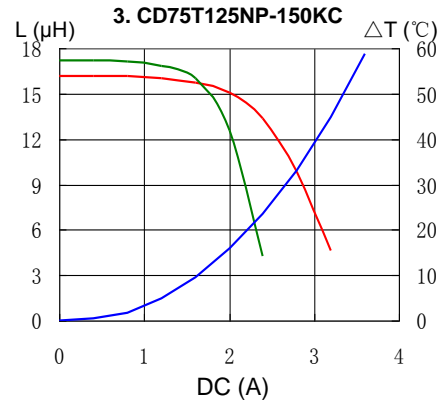
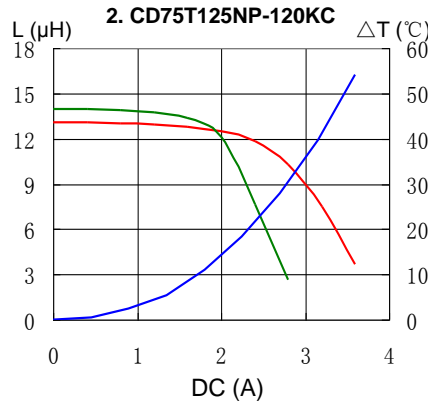
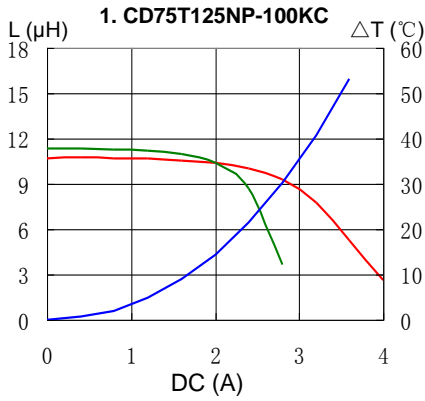
※3 Temperature rise current: The actual value of D.C. current when the temperature of coil becomes $\Delta T=40^\circ\text{C}$ (Ta=20°C).

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Saturation Current & Temperature Rise Graph

— L (20°C) — L (105°C) — ΔT

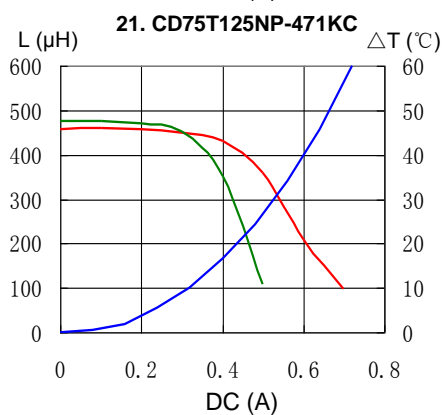
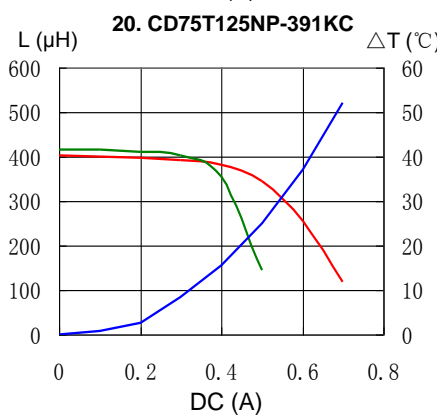
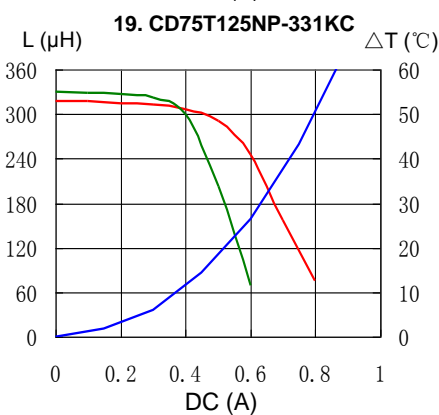
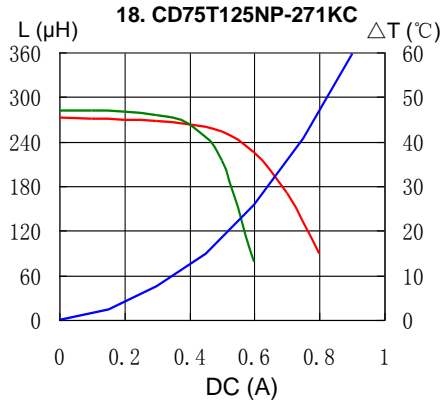
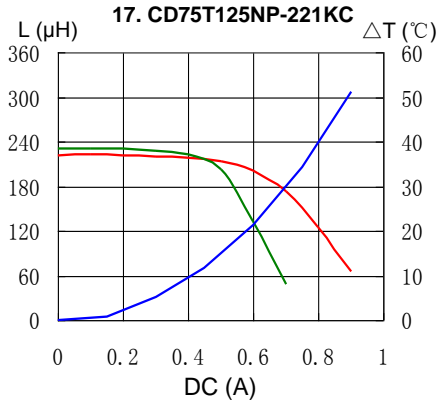
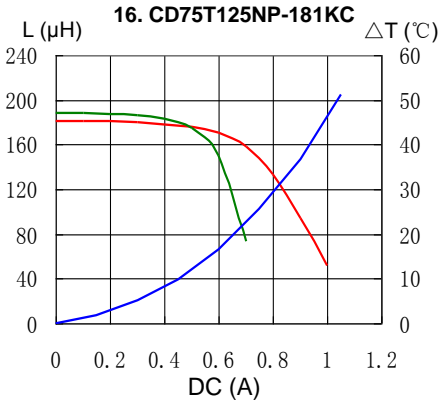
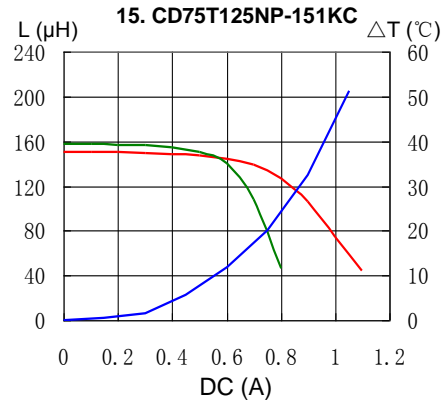
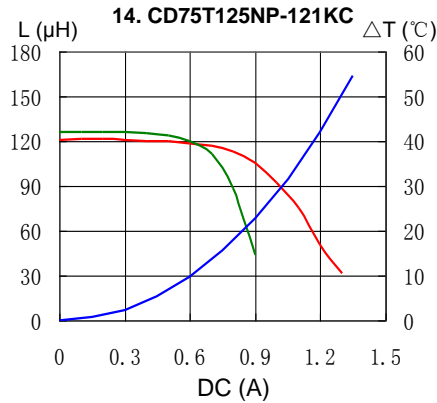
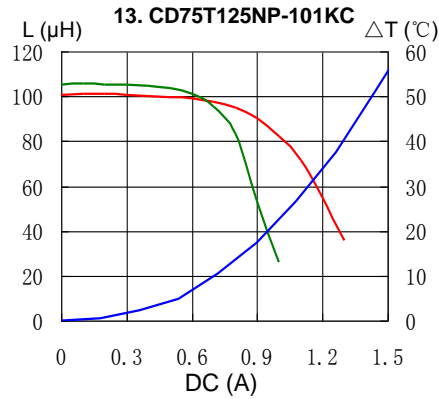


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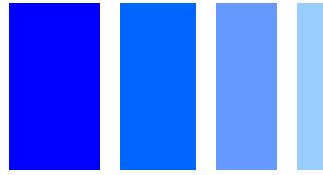


Saturation Current & Temperature Rise Graph

— L (20°C) — L (105°C) — ΔT

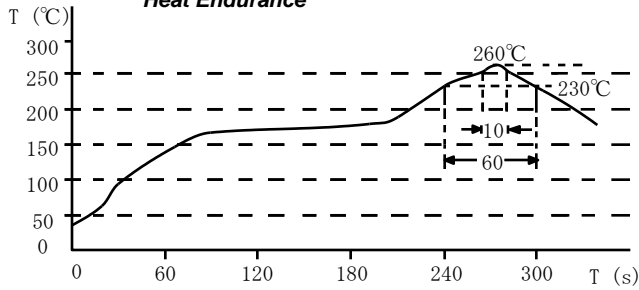


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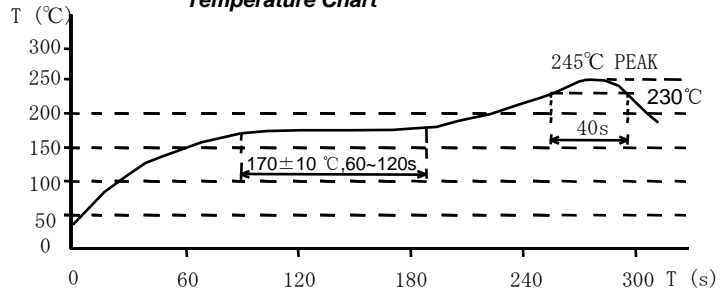


Solder Reflow Condition

Heat Endurance



Temperature Chart



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infocomp@eu.sumida.com

Taipei

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