# SPECIFICATIONS

Customer	
Product Name	Multi-layer Chip Ferrite Beads
Sunlord Part Number	PZ2012D600-3R0TF
Customer Part Number	

### [New Released, Revised]

SPEC No.: PZ201260

Rev.	Effective Date	Changed Contents	Change reasons	Approved By
01	1	New release	1	Hai Guo

#### 【 This SPEC is total 10 pages.】

[RoHS Compliant Parts]

Approved By	Checked By	Issued By
	部	軍影

## Shenzhen Sunlord Electronics Co., Ltd.

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[For Customer approval Only] Date:					
Qualification Status:	📙 Full 📙 R	estricted 🗌 Rejec	ted		
Approved By	Verified By	Re-checked By	Checked By		
Comments:					

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1.	Scop	е										
	This s	pecifica	tion appli	ies to P	Z2012D	600-3R(	0TF m	nulti	layer	ferrite chip beads.		
2.	Appli	ed Docu	uments									
	EIA/IS	6-759	Multi	-layer C	hip Bea	id Qualif	ficatio	n Sp	ecifio	cation.		
	MIL-S	TD-202	Test N	vethods	s for Ele	ctronic a	and El	ectr	ical C	hip Parts.		
	IEC-6	8	Test I	Method	s for Env	vironme	ntal Te	estir	ıg.			
3.	Prod	uct Desc	cription						•			
		Descript	-			·			,			
	,	•	Bead,201	2,600h	m@_10	0MHz.0	.02Oł	חm I	RDC.	3000mA		
	2)		Identifica		-				,			
		<u>PZ</u>	<u>2012</u>	•	600	- <u>3R0</u>	т	F				
		(1)	2	<u>D</u> ③	(4)	(5)	<u>T</u> ⑥	<u>F</u>	)			
		-	-		-		7					
	1	)	Ту	уре					2	External Din	nensions (LXW)	[mm]
		ΡZ	F	<sup>:</sup> or Larg	je currer	nt				2012 [0805]	2.0 X 1.2	25
	3	)	Materia	al Code			٦		4	Nomina	al Impedance	
	Q	/	matche	D			-			Example	Nominal Va	alue
				<u> </u>			_]			600	60 Ω	
	(5	)	Rate C	urrent			7					
		3R0		3.(	AC			Г	(6)	Pag	kina	
									\(J)	Pac	NILIU	

6 Packing				
В	Bulk Package			
Т	Tape Carrier Package			

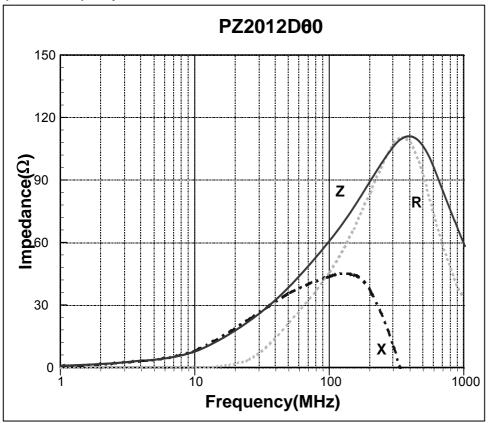
#### 4. **Electrical Characteristics**

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Part Number	Impedance (Ω)	Z Test Freq.(MHz)	DCR (Ω) Max.	Ir (mA) Max.
PZ2012D600	60±25%	100	0.02	3000

#### Impedance Frequency Characteristics

**HSF** Products Hazardous Substance Free Products

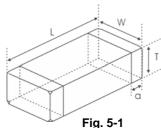


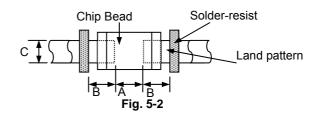
<sup>1)</sup> Operating and storage temperature range (individual chip without packing):: -40  $^\circ \! \mathbb{C}$  to +85  $^\circ \! \mathbb{C}$ 2)

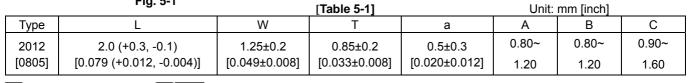
Storage temperature range (packaging conditions):-10  $^\circ\!C$  ~+40  $^\circ\!C$  and RH 70% (Max.)

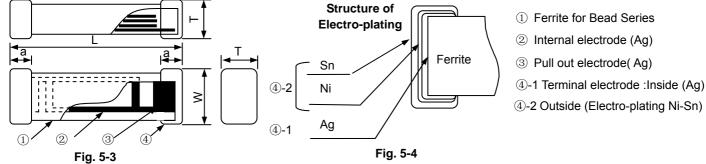
#### 5. Shape and Dimensions

- 1) Dimensions and recommended PCB pattern for reflow soldering: See Fig.5-1, Fig.5-2 and Table 5-1
- 2) Structure: See Fig. 5-3 and Fig. 5-4









#### **Material Information**

Code	Part Name	Material Name
1	Ferrite Body	Ferrite Powder
2	Inner Coils	Silver Paste
3	Pull-out Electrode (Ag)	Silver Paste
<b>④-1</b>	Terminal Electrode: Inside Ag	Termination Silver Composition
<b>④-2</b>	Electro-Plating: Ni/Sn plating	Plating Chemicals

#### 6. Test and Measurement Procedures 6.1 Test Conditions

6.1 Test Conditions

Unless otherwise specified, the standard atmospheric conditions for measurement/test as:

- a. Ambient Temperature: 20±15°C
- b. Relative Humidity: 65±20%
- c. Air Pressure: 86 KPa to 106 KPa
- If any doubt on the results, measurements/tests should be made within the following limits:
  - a. Ambient Temperature: 20±2°C
  - b. Relative Humidity: 65±5%
  - c. Air Pressure: 86KPa to 106 KPa

#### 6.2 Visual Examination

a. Inspection Equipment: 20 X magnifier

#### 6.3 Electrical Test

6.3.1 DC Resistance (DCR)

- a. Refer to Item 4.
- b. Test equipment (Analyzer): High Accuracy Milliohmmeter-HP4338B

6.3.2 Impedance (Z)

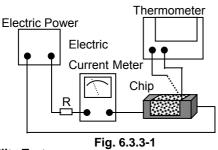
- a. Refer to Item 4.
- b. Test equipment: High Accuracy RF Impedance /Material Analyzer-HP4291B+16192A
- c. Test signal: -20dBm or 50mV
- d. Test frequency refers to Item 4.

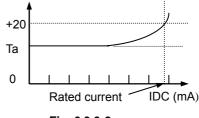
#### **Specifications for Multi-layer Chip Ferrite Beads**

#### 6.3.3 Rated Current

- a. Refer to Item 4.
- b. Test equipment (see Fig.6.3.3-1): Electric Power, Electric current meter, Thermometer.
- c. Measurement method (see Fig. 6.3.3-1):
  - 1. Set test current to be 0 mA.
  - 2. Measure initial temperature of chip surface.
  - 3. Gradually increase voltage and measure chip temperature for corresponding current.

d. Definition of Rated Current(IDC): IDC is direct electric current as chip surface temperature rose just 20 °C against chip initial surface temperature(Ta). (see Fig. 6.3.3-2):







#### 6.4 Reliability Test

Items	Requirements	Test Methods and Remarks
6.4.1 Terminal Strength	No removal or split of the termination or other defects shall occur. Chip Chip F Mounting Pad Glass Epoxy Board Fig.6.4.1-1	<ol> <li>Solder the bead to the testing jig (glass epoxy board shown in Fig.6.4.1-1) using eutectic solder. Then apply a force in the direction of the arrow.</li> <li>10N force for 2012 series.</li> <li>Keep time: 10±1s</li> <li>Speed:1.0mm/s.</li> </ol>
6.4.2 Resistance to Flexure	No visible mechanical damage. Unit: mm [inch] Type a b c 2012[0805] 1.2 4.0 1.65	<ol> <li>Solder the bead to the test jig (glass epoxy board shown in Fig.6.4.2-1) Using a eutectic solder. Then apply a force in the direction shown Fig. 6.4.2-2.</li> <li>Flexure: 2mm</li> <li>Pressurizing Speed: 0.5mm/sec.</li> <li>Keep time: 30 sec.</li> </ol>
	b ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓	Flexure
6.4.3 Vibration	<ol> <li>No visible mechanical damage.</li> <li>Impedance change: within ±20%</li> <li>Cu pad Solder mask</li> <li>Cu pad Solder mask</li> <li>Glass Epoxy Board</li> <li>Fig. 6.4.3-1</li> </ol>	<ol> <li>Solder the bead to the testing jig (glass epoxy board shown in Fig.6.4.3-1) using eutectic solder.</li> <li>The bead shall be subjected to a simple harmonic motion having total amplitude of 1.5mm, the frequency being varied uniformly between the approximate limits of 10 and 55 Hz.</li> <li>The frequency range from 10 to 55 Hz and return to 10 Hz shall be traversed in approximately 1 minute. This motion shall be applied for a period of 2 hours in each 3mutually perpendicular directions (total of 6 hours)</li> </ol>
6.4.4 Dropping	<ol> <li>No visible mechanical damage.</li> <li>Impedance change: within ±20%</li> </ol>	Drop chip bead 10 times on a concrete floor from a height of 100 cm.
6.4.5 Temperature 6.4.6 Solderability	Impedance change should be within ±20% of initial value measuring at 20°C.① No visible mechanical damage.② Wetting shall exceed 95% coverage.	Temperature range:-40°C to +85°C         Reference temperature: +20°C         1       Solder temperture:240±2°C         2       Duration: 3 sec.         3       Solder: Sn/3.0Ag/0.5Cu         4       Flux: 25% Resin and 75% ethanol in weight.

6.4.7	① No visible mechanical damage.	① Solder temperature :260±3℃
Resistance to	2 Wetting shall exceed 95% coverage.	② Duration: 5 sec.
Soldering	③ Impedance change: within ±20%	③ The chip shall be stabilized at normal condition for
Heat		1~2hours before measuring.
		④ Solder: Sn/3.0Ag/0.5Cu
		5 Flux: 25% Resin and 75% ethanol in weight
6.4.8 Thermal	1 No mechanical damage.	1 Temperature, Time: (See Fig.6.4.8-1).
Shock	<ol> <li>Impedance change: Within ±20%</li> </ol>	$\rightarrow$ +0°°C for 30±3 min $\rightarrow$ +85°°C for 30±3min
Chicola		<ol> <li>Transforming interval: Max. 20 sec.</li> </ol>
		<ul> <li>3 Tested cycle: 100 cycles</li> </ul>
	30 min. 30 min	<ul> <li>4 The chip shall be stabilized at normal condition for 1~2</li> </ul>
	+85℃ 30 min. 30 min.	hours before measuring.
	Ambient	nouis belore measuring.
	l	
	Temperature _40℃ 30 min.	
	Fig. 6.4.8-1 <sup>20sec.</sup> (max.	1
6.4.9	① No mechanical damage.	① Temperature: -40±2°C
Resistance to	2 Impedance change: within ±20%	② Duration: 1000 <sup>+24</sup> hours
Low		③ The chip shall be stabilized at normal condition for 1~2
Temperature		hours before measuring.
_		
6.4.10	① No mechanical damage.	1) Temperature: 85±2°C
Resistance to	2 Impedance change: within ±20%	② Duration: 1000 <sup>+24</sup> hours
High		③ The chip shall be stabilized at normal condition for 1~2
Temperature		hours before measuring.
6.4.11	① No visible mechanical damage.	① Temperature: 55±2℃
Damp Heat	② Impedance change: within ±20%	② Humidity: 90% to 95% RH
(Steady		③ Duration: 1000 <sup>+24</sup> hours
States)		④ The chip shall be stabilized at normal condition for 1~2
		hours before measuring.
6.4.12	① No visible mechanical damage.	① Temperature: 55±2°C
Loading	2 Impedance change: within ±20%	<ul> <li>Humidity: 90% to 95% RH</li> </ul>
Under Damp		3 Duration: 1000 <sup>+24</sup> hours
Heat		
		(5) The chip shall be stabilized at normal condition for 1~2
		hours before measuring.
6.4.13	1 No visible mechanical damage.	① Temperature:+85±2℃
Loading at	2 Impedance change: within ±20%	② Duration: 1000 <sup>+24</sup> hours
High		③ Applied current: Rated current.
Temperature		4 The chip shall be stabilized at normal condition for
(Life Test)		1~2hours before measuring.
· ,		~

#### 7. Packaging, Storage and Transportation

#### 7.1 Packaging

There are two types of packaging for the chip beads. Please specify the packing code when ordering.

- 7.1.1 Bulk Packaging:
  - Packaging code: B, The quantity of each bag is integral multiple of 1000.

Remark: The actual quantity in a package may change sometimes.

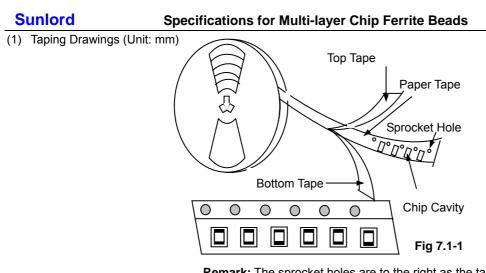
7.1.2 Tape Carrier Packaging:

Packaging code: T

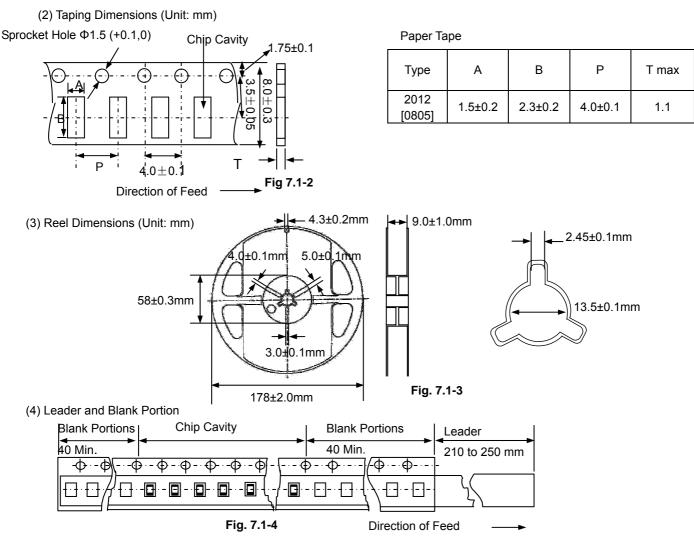
- a. Tape carrier packaging are specified in attached figure Fig.7.1-1~4
- b. Tape carrier packaging quantity please see the following table:

Туре	2012[0805]
T(mm)	0.85±0.2
Таре	Paper Tape
Quantity	4К

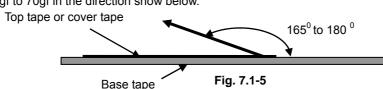
- c. Reel shall be packaged in vinyl bag.
- d. Maximum of 5 or 10 reels bags shall be packaged in an inner box.
- e. Maximum of 6 or 10 inner boxes shall be packaged in an outer case.



Remark: The sprocket holes are to the right as the tape is pulled toward the user.



- (5) Missing beads number within 0.1% of the number per reel or 1 pcs, whichever is greater, and are not continuous.
- (6) The top tape and bottom tape shall not protrude beyond the edges of the tape and shall not cover sprocket hole.
- (7) Cumulative tolerance of sprocket holes, 10 pitches: ±0.3mm
- (8) Peeling off force: 10gf to 70gf in the direction show below.



#### 7.2 Storage

a. The solderability of the external electrode may be deteriorated if packages are stored where they are exposed to high humidity. Package must be stored at 40°C or less and 70% RH or less.

#### **Specifications for Multi-layer Chip Ferrite Beads**

- b. The solderability of the external electrode may be deteriorated if packages are stored where they are exposed to dust of harmful gas (e.g. HCl, sulfurous gas of H<sub>2</sub>S)
- c. Packaging material may be deformed if package are stored where they are exposed to heat of direct sunlight.
- d. Minimum packages, such as polyvinyl heat-seal packages shall not be opened until they are used. If opened, use the reels as soon as possible.
- e. Solderability specified in **Clause 6.4.6** shall be guaranteed for 3 months from the date of delivery on condition that they are stored at the environment specified in **Clause 4**. For those parts, which passed more than 3 months shall be checked solder-ability before use.

#### 7.3 Transportation

The cases shall not be damaged, destroyed and rained on.

#### 7.4 General Handling Precautions

- a. Always wear static control bands to protect against ESD.
- b. Any devices used with the beads (soldering iron, measuring instruments) should be properly grounded.
- c. Keep bare hands and metal conductors (i.e., metal desk) away from chip electrodes or conductive areas that lead to chip electrodes.

#### 7.5 Precautions on Use

- a. Pre-heating when soldering, see detail in Clause 9 in this specification.
- b. When splitting the PC board after mounting beads and other components, care is required so as not to give any stress of deflection or twisting to the board.
- c. Don't apply current in excess of the rated current value. It may reduce the impedance due to the temperature increase effect.
- d. Always wear static control bands to protect against ESD.
- e. Keep clear of anything that may generate magnetic fields such as speakers, coils.
- f. When Soldering, the impedance may be varied due to hot energy and mechanical stress.
- g. Mounting ships with adhesive in preliminary assembly, before the soldering stage, may lead appropriately checked; the size of land pattern, type of adhesive, amount applied, hardening of the adhesive on proper usage and amounts of adhesive to use.
- h. Mounting Density: Add special attention to radiating heat of products when mounting the beads near the products with heating. The excessive heat by other products may cause deterioration at joint of this product with substrate.

#### 8. Packing Documents and Marking

#### 8.1 Packing documents

Packing documents include the following:

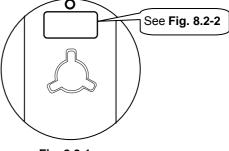
- a. Packaging list
- b. Certificate of compliance (COC)

#### 8.2 Marking

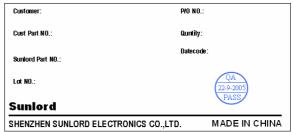
- 1.Marking label information on reels includes (see Fig.8.2-1~2):
  - a. Customer
  - b. Customer part No.
  - c. Customer P/O#
  - d. Sunlord part No.
  - e. Date code
  - f. Lot number
  - g. Quantity per reel
  - h. Inspection stamp
  - i. MFG address as 'MADE IN CHINA'

2.Marking label on inner box (see Fig.8.2-3~4):

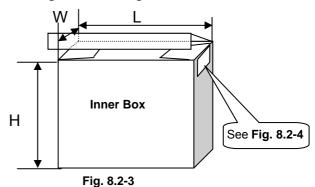
- a. Customer
- b. Customer part No.
- c. Customer P/O#
- d. Sunlord part No.
- e. Date code
- f. Quantity per reel
- g. Manufacturer







#### Fig. 8.2-2: Marking label on reels



#### **Specifications for Multi-layer Chip Ferrite Beads**

Customer:	PYO NO.:
Cust Part NO.:	Quntily:
Sunlord Part NO.:	Datecode:
Sunlord	QA 22-9-2005 PASS
SHENZHEN SUNLORD ELECTRONICS CO.,LT	). MADE IN CHINA

#### Fig. 8.2-4: Marking Label on inner box

- 3.Marking on outer case (see Fig.8.2-5~6):
  - a. Manufacturer: Sunlord ID:
  - "Shenzhen Sunlord Electronics Co., Ltd." b. Packing label include the following:
    - i) Customer
    - ii) Manufacture
    - iii) Date code
    - iv) C/No. Example; "1/10" means this case is the 1st one of total 10 cases
    - v) P/O No.
    - vi) Customer P/N
    - vii) Sunlord P/N
    - viii) QT'Y(K)

	Packaging type	L(cm)	W(cm)	H(cm)
1	Inner Box- 1	18	7.5	18
2	Inner Box- 2	18	12	18

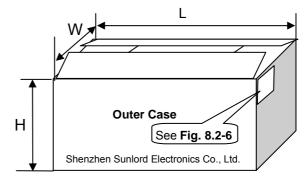
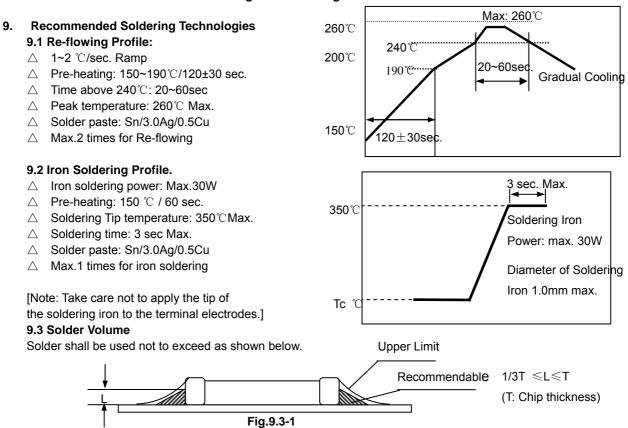


Fig. 8.2-5

one		Packaging type	L(cm)	W(cm)	H(cm)
	1	Outer Case- 1	40	20	20
	2	Outer Case- 2	40	20	40

Sunlord	Customer			DATE CODE
	Manufacturer	SHEN ZHEN SUNLORD ELECTRONICS CO., LTD		
C/No.	P/O No.	Customer P/N	Sunlord P/N	QT'Y(K)

#### Fig. 8.2-6: Marking label on outer case

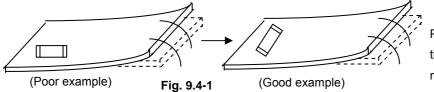


Accordingly increasing the solder volume, the mechanical stress to product is also increased. Exceeding solder volume may cause the failure of mechanical or electrical performance.

#### **Specifications for Multi-layer Chip Ferrite Beads**

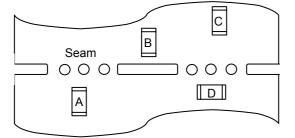
#### 9.4 Attention Regarding PCB Bending

- The following shall be considered when designing and laying out PCB's.
- (1) PCB shall be designed so that products are not subjected to the mechanical stress from board warp or deflection.



Products shall be located in the sideways direction to the mechanical stress

(2) Products location on PCB separation.



Product shall be located carefully because they may be subjected to the mechanical stress in order of A>C=B>D.

Fig. 9.4-2

#### 9.5 Cleaning

Products shall be cleaned on the following conditions:

- (1) Cleaning temperature shall be limited to  $60^{\circ}$ C Max. ( $40^{\circ}$ C Max. for fluoride and alcohol type cleaner.)
- (2) Ultrasonic cleaning shall comply with the following conditions, avoiding the resonance phenomenon at the mounted products and PCB.

Power: 20W/I Max.

Frequency: 28KHz to 40 KHz

- Time: 5 minutes Max
- (3) Cleaner
  - ① Alternative cleaner
    - i. Isopropyl alcohol (IPA)
    - ii. HCFC-225
  - ② Aqueous agent
    - Surface Active Agent Type (Clean through-750H)
    - Hydrocarbon Type (Techno Cleaner-335)
    - Higher Alcohol Type (Pine Alpha ST-100S)
    - Alkali saponifier Type (% Aqua Cleaner 240)
    - % Alkali saponification shall be diluted to 20% volume with de-ionized water.
- (4) There shall be no residual flux and residual cleaner after cleaning. In the case of using aqueous agent, product shall be dried completely after rinse with de-ionized water in order to remove the cleaner.
- (5) Some products may become slightly whitened. However, product performance or usage is not affected.
- 10. Supplier Information
  - a) Supplier:
    - Shenzhen Sunlord Electronics Co., Ltd
    - b) Manufacturer

Shenzhen Sunlord Electronics Co., Ltd

c) Manufacturing Address:

Sunlord Industrial Park, Dafuyuan Industrial Zone, Guanlan, Shenzhen, China Zip: 518110

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