In Development

SPEC No.: SWPA01150000

SPECIFICATIONS

Customer	
Product Name	Wire Wound SMD Power Inductor
Sunlord Part Number	SWPA8065S Series
Customer Part Number	

[New Released, Revised] [This SPEC is total 12 pages.]

[ROHS Compliant Parts]

Approved By	Checked By	Issued By

Shenzhen Sunlord Electronics Co., Ltd.

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[For Customer approval Only] Date:					
Qualification Status: 🗌 Full 🗌 Restricted 🗌 Rejected					
Approved By	Verified By	Re-checked By	Checked By		
Comments:					
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[Version change history]

Rev.	Effective Date	Effective Date Changed Contents Change Reasons		Approved By
01	1	New released	1	Qintian Hou

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1 Scope

This specification applies to the SWPA8065S Series of wire wound SMD power inductor.

2 Product Description and Identification (Part Number)

1) Description:

SWPA8065S series of Wire wound SMD power inductor.

2) Product Identification (Part Number)

③ Feature type		
S	Standard Type	

5 Ir	D Inductance Tolerance			
Ν	±30%			
М	±20%			

6	Packing		
Т	Tape Carrier Package		

	8.0X8.0X 6.5	
Nominal	Inductance	
	Example	
	1.0uH	
	3.3uH	
	100uH	
	Nominal	

External Dimensions(L×W×H) [mm]

(7)	ç	Special Process code			
		Special Process code			
	* Standard product is blank				

3 Electrical Characteristics

Please refer to Item 12.

- 1) Operating and storage temperature range (individual chip without packing): -40°C ~ +125°C (Including Self-heating)
- 2) Storage temperature range (packaging conditions): -10 $^\circ\!C$ ~+40 $^\circ\!C$ and RH 70% (Max.)

4 Test and Measurement Procedures

4.1 Test Conditions

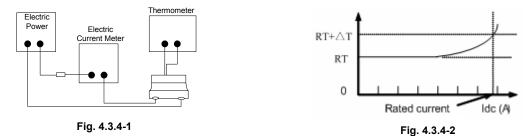
- 4.1.1 Unless otherwise specified, the standard atmospheric conditions for measurement/test as:
 - a. Ambient Temperature: 20±15°C
 - b. Relative Humidity: 65±20%
 - c. Air Pressure: 86kPa to 106kPa
- 4.1.2 If any doubt on the results, measurements/tests should be made within the following limits:
 - a. Ambient Temperature: 20±2°C
 - b. Relative Humidity: 65±5%
 - c. Air Pressure: 86kPa to 106kPa

4.2 Visual Examination

Inspection Equipment: Visual.

4.3 Electrical Test

- 4.3.1 Inductance (L)
 - a. Refer to Item 12. Test equipment: WK3260BLCR meter or equivalent.
 - b. Test Frequency and Voltage: refers to Item 12.
- 4.3.2 Direct Current Resistance (DCR)
 - a. Refer to Item 12.
 - b. Test equipment: HIOKI 3540 or equivalent.
- 4.3.3 Saturation Current (Isat)
 - a. Refer to Item 12.
 - b. Test equipment: WK3260B LCR meter or equivalent.
- 4.3.4Temperature rise current (Irms)
 - a. Refer to Item 12.
 - b. Test equipment (see Fig. 4.3.4-1, Fig. 4.3.4-2): Electric Power, Electric current meter, Thermometer.
 - c. Measurement method
 - 1. Set test current to be 0 mA.
 - 2. Measure initial temperature of choke surface.
 - 3. Gradually increase current and measure choke temperature for corresponding current.
 - 4. Definition of Temperature rise current: DC current that causes the temperature rise (\triangle T) from ambient temperature.

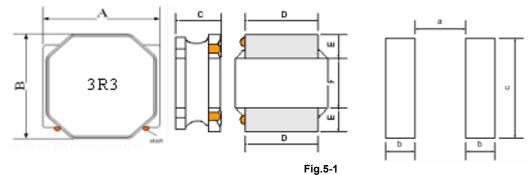


4.3.5 Self-resonant frequency(SRF)

- a. Refer to Item 12.
- b. Test equipment: Agilent E4991A+16197or equivalent.

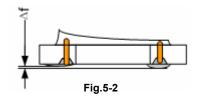
Shape and Dimensions 5

Dimensions and recommended PCB pattern for reflow soldering, please see Fig.5-1, Fig. 5-2 and Table 5-1.



[Table 5-1] (Unit: mm)

Series	А	В	C.	D	E	F	a.	b	с
SWPA8065S	8.0±0.3	8.0±0.3	6.5Max.	6.3±0.3	2.00±0.3	4.0±0.3	3.8Тур.	2.2Тур.	7.5Тур.



Components

Ferrite Core

Magnetic Glue

Electrodes

Marking

Wire

[Table 6-1]

Ferrite

Material

Polyurethane system enameled copper wire

Epoxy resin and magnetic powder

substrate+ Top Electrodes

Nitrocellulose

Δf: Clearance between terminal and the surface of plate must be 0.2mm max when coil is placed on a flat plate.

No.

1

2

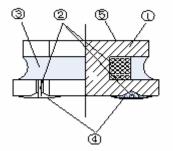
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6 Structure

The structure of SWPA8065S product, please refer to Fig.6-1 and Table 6-1.



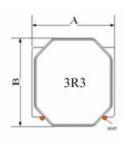
a	.6-	1	

Fig.6-1	

7 **Product Marking**

Please refer to Fig. 7-1.

The content of marking please refers to Item 12.



8 Reliability	' Test	
Items	Requirements	Test Methods and Remarks
8.1 Terminal Strength	No removal or split of the termination or other defects shall occur. X direct	 Solder the inductor to the testing jig (glass epoxy board shown in Fig.8.1-1) using eutectic solder. Then apply a force in the direction of the arrow. 10N force. Keep time: 5s
	Fig.8.1-1	
8.2 Resistance to Flexure	No visible mechanical damage	 Solder the chip to the test jig (glass epoxy board) using eutectic solder. Then apply a force in the direction shown as Fig.8.2-1. Flexure: 2mm Pressurizing Speed: 0.5mm/sec Keep time: 30±1s Test board size: 100X40X1.0 Land dimension: Please see Fig.5-1
	Fig.8.2-1	
8.3 Vibration	 No visible mechanical damage. Inductance change: Within ±10% 	 Solder the chip to the testing jig (glass epoxy board shown as the following figure) using eutectic solder. The chip shall be subjected to a simple harmonic motion having total amplitude of 1.5mm, the frequency being varied uniformly between the approximate limits of 10 and 55 Hz. The frequency range from 10 to 55 Hz and return to 10 Hz shall be traversed in approximately 1 minute. This motion shall be applied for a period of 2 hours in each 3 mutually perpendicular directions (total of 6 hours).
8.4 Temperature coefficient	Inductance change: Within ±20%	 Temperature: -40 °C ~+125 °C With a reference value of +20 °C, change rate shall be calculated
8.5 Solderability	90% or more of electrode area shall be coated by new solder.	 The test samples shall be dipped in flux, and then immersed in molten solder. Solder temperature: 245±5°C Duration: 5±1 sec. Solder: Sn/3.0Ag/0.5Cu Flux: 25% resin and 75% ethanol in weight Immersion depth: all sides of mounting terminal shall be immersed
8.6 Resistance to Soldering Heat	 No visible mechanical damage. Inductance change: Within ±10% 	 Re-flowing Profile: Please refer to Fig. 8.6-1. Test board thickness: 1.0mm Test board material: glass epoxy resin The chip shall be stabilized at normal condition for 1~2 hours before measuring 260 °C 260 °C Max Ramp Up Rate=3°C/sec. Max Ramp Down Rate=6°C/sec 60~90sec. 150 °C 60~120sec 25 °C Time 25°C to Peak =8 min max Fig. 8.6-1

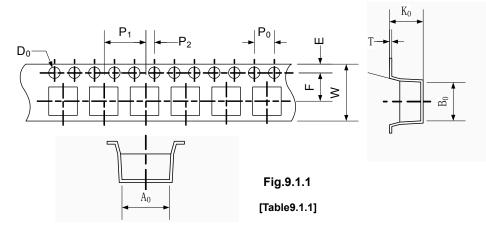
8.7 Thermal Shock	 No visible mechanical damage. Inductance change: Within ±10% 	① Temperature and time: -40±3℃ for 30±3 min→125℃ for 30±3min, please refer to Fig. 8.7-1 .
	125℃ 30 min. 30 min. Ambient	 2 Transforming interval: Max. 20 sec 3 Tested cycle: 100 cycles 4 The chip shall be stabilized at normal condition for 1~2 hours before measuring
8.8 Resistance to Low	Fig.8.7-1 1 No visible mechanical damage 2 Inductance change: Within ±10%	 Temperature: -40±3°C Duration: 1000^{±24} hours
Temperature	inductance change. Within ±10%	 3 The chip shall be stabilized at normal condition for 1~2 hours before measuring
8.9 Resistance to High Temperature	 No mechanical damage. Inductance change: Within ±10% 	 Temperature: 125±2℃ Duration: 1000^{±24} hours The chip shall be stabilized at normal condition for 1~2 hours before measuring.
8.10 Damp Heat	 No mechanical damage. Inductance change: Within ±10% 	 Temperature: 60±2℃ Humidity: 90% to 95%RH Duration: 1000^{±24} hours The chip shall be stabilized at normal condition for 1~2 hours before measuring
8.11 Loading Under Damp Heat	 No mechanical damage. Inductance change: Within ±10% 	 Temperature: 60±2°C Humidity: 90% to 95% RH Applied current: Rated current Duration:1000^{±24} hours The chip shall be stabilized at normal condition for 1~2 hours before measuring
8.12 Loading at High Temperature	 No mechanical damage. Inductance change: Within ±10% 	 Temperature: 85±2°C Applied current: Rated current Duration: 1000^{±24} hours The chip shall be stabilized at normal condition for 1~2 hours before measuring

9 Packaging and Storage

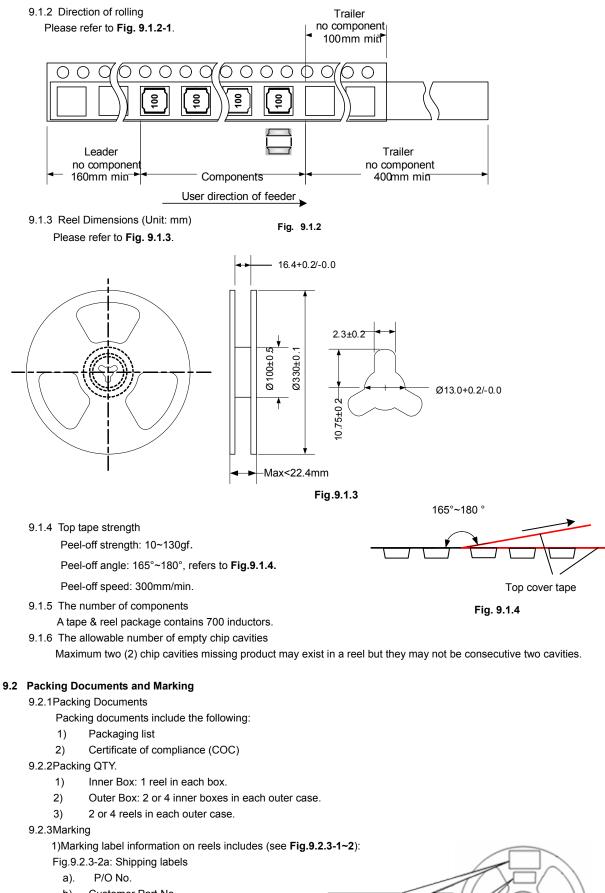
9.1 Tape and Reel Packaging Dimensions

9.1.1Taping Dimensions (Unit: mm)

Please refer to Fig. 9.1.1 and Table 9.1.1.



Series	A ₀	B ₀	W	E	F	P ₀	P ₁	P ₂	D ₀	Т	K ₀
SWPA8065S	8.30±0.1	8.30±0.1	16.0±0.3	1.75±0.1	7.5±0.1	4.0±0.1	12.0±0.1	2.0±0.1	1.5+0.1/-0.0	0.5±0.03	6.7±0.1



- b). Customer Part No.
- c). Sunlord Part No.
- d). Quantity..
- e). Lot No.
- f). Date code
- g). Inspection stamp
- h). MFG address as 'Made In China'.

Fig.9.2.3-1

Fig.9.2.3-2a

Fig.9.2.3-2b

Specifications for Wire Wound SMD Power Inductor

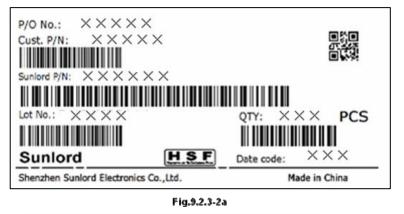
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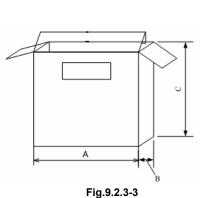
Packaging

Н

Made in China

- Fig.9.2.3-2b: Production labels
- a). P/O No.
- b). Quantity ..
- c). Lot No.
- d). Inspe No
- e). Inspection stamp
- f). MFG address as 'Made in China'.
- g). sequence number





[Table 9.2.3-1]								
Packaging type	A(mm)	B(mm)	C(mm)					
Inner box	340	30	340					

[Table 9.2.3-2]

W(mm)

H(mm)

Fig.9.2.3-2b

2)Marking label information on inner box

a). Inner box please refers to Fig.9.2.3-3 and Table 9.2.3-1.

HSF

b). Marking Label on inner box N/A.

P/N: XXXXXXXXXXXXXX

Qty: XXXXXXXX

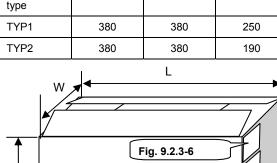
Sunlord

3)Marking on outer case (see Fig.9.2.3-4~6):

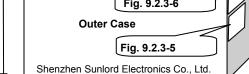
- Out case size pleases reefers to Table 9.2.3-2.
- a). Manufacturer: Sunlord ID:
 - "Shenzhen Sunlord Electronics Co., Ltd."
- b). Packing label include the following:
 - i) Customer
 - ii) Manufacturer
 - iii) Date code
 - iv) C/No.

Example; "1/10" means that this case is the 1st one

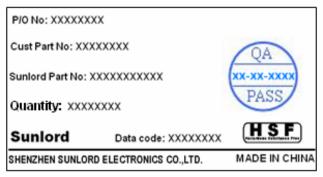
- Of total 10 cases
 - v) P/O No.
 - vi) Customer Part No.
 - vii) Sunlord Part No.



L(mm)







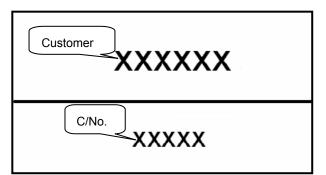


Fig.9.2.3-5

Sumoru Specifications for Wire Wound SMD Power Inductor Page 9 of 12 10 Visual inspection standard of product Page 9 of 12								
File No:								
Effective date:		Applied to	REV:01					
No.	Defect Item	Graphic Rejection identification						
1	Core defect		The defect length/width (I or <i>w</i>) more than L/6 or W/6, NG.	AQL=0.65				
2	Core crack		Visual cracks, NG.	AQL=0.65				
3	Starvation		 Resin starved length, <i>I</i>, more than L/2, NG. ① IF <i>W</i> > 2mm, resin starved width, <i>w</i>, more than W/2, NG. ② IF <i>W</i>≤2mm, resin starved width, <i>w</i>, don't control. 	AQL=0.65				
4	Excessive glue		The length, width or height of product beyond specified value, NG.	AQL=0.65				
5	Cold solder		① Cold solders <i>I</i> more than1mm, NG.	AQL=0.65				
6	Solder icicle		 The height <i>H</i> of product beyond specified value, NG; The clearance <i>Δf</i> beyond specified value listed in Item 5, NG; 	AQL=0.65				
7	Electrode uneven	Δf	The clearance Δf beyond specified value listed in Item 5 , NG;	AQL=0.65				
8	Marking defect		 The content of marking 1) is indistinct, 2) disagrees with current product P/N requirements, NG; Intersection angle by L1 and L2 more than 45°, NG. 	AQL=0.65				

Specifications for Wire Wound SMD Power Inductor

11 Recommended Soldering Technologies 11.1Re-flowing Profile:

- \triangle Preheat condition: 150 ~200 °C/60~120sec.
- \triangle Allowed time above 217°C: 60~90sec.
- △ Max temp: 260°C
- △ Max time at max temp: 5sec. Solder paste: Sn/3.0Ag/0.5Cu
- \triangle Allowed Reflow time: 2x max
 - Please refer to Fig. 11.1-1.

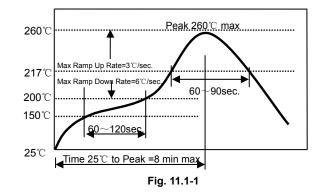
[Note: The reflow profile in the above table is only for qualification and is not meant to specify board assembly profiles. Actual board assembly profiles must be based on the customer's specific board design, solder paste and process, and should not exceed the parameters as the Reflow profile shows.]

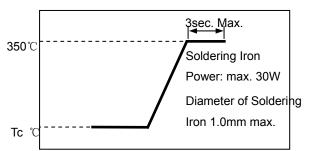
11.2 Iron Soldering Profile:

- \triangle Iron soldering power: Max. 30W
- \triangle Pre-heating: 150°C/60sec.
- \triangle Soldering Tip temperature: 350 °C Max.
- \triangle Soldering time: 3sec. Max.
- △ Solder paste: Sn/3.0Ag/0.5Cu
- △ Max.1 times for iron soldering Please refer to **Fig. 11.2-1**.

[Note: Take care not to apply the tip of the soldering iron to the terminal electrodes.]

12 Electrical Characteristics







Custo mer	Part Number	Inductance	Min. Self-reso	DC Resistance		Saturation Current		Heat Rating Current		Marki
P/N		0.1MHz/1V	nant frequency	Max.	Тур.	Max.	Тур.	Max.	Тур.	ng
	Units	μH	MHz	Ω	Ω	А	А	А	А	
	Symbol	L	SRF	D	CR	ls	at	Irr	ns	-
	SWPA8065S3R3MT	3.3±20%	27	0.018	0.015	9.50	10.00	5.10	5.90	3R3
	SWPA8065S4R7MT	4.7±20%	18	0.022	0.018	8.50	9.50	4.70	5.40	4R7
	SWPA8065S5R6MT	5.6±20%	17	0.026	0.022	8.00	9.00	4.50	5.20	5R6
	SWPA8065S6R8MT	6.8±20%	16	0.026	0.022	7.50	8.00	4.50	5.20	6R8
	SWPA8065S8R2MT	8.2±20%	15	0.031	0.026	7.00	7.70	4.20	4.80	8R2
	SWPA8065S220MT	22±20%	8	0.065	0.054	4.30	4.80	2.85	3.30	220
	SWPA8065S431MT	430±20%	1.5	1.20	1.00	0.95	1.05	0.61	0.69	431

Note:

(1) Rated current: Saturation Current and Heat Rating Current which is smaller.

(2) Test conditions: Ambient temperature: 20±15°C; Relative Humidity: 65±20%; Air Pressure: 86kPa to 106kPa If any doubt on the results, measurements/tests should be made within the following limits: Ambient temperature: 20±2°C; Relative Humidity: 65±5%; Air Pressure: 86kPa to 106kPa

(3) Inductance using WK3260B or equivalent tested on 100KHz/1V.

- (4) **SRF** using Agilent E4991A+16197A or equivalent.
- (5) DCR using HIOKI3540.

(6) **Saturation Current** using WK3260B. Max. Value, DC current at which the inductance drops less than 30% from its value without current; Typ. Value, DC current at which the inductance drops approximate 30% from its value without current;

Heat Rating Current using thermocouple, electric Power and electric current meter. DC current that causes the temperature rise (Δ T) from 25°C ambient. Max. Value: Δ T <40°C; *Typ. Value*, Δ T is approximate 40°C.

The part temperature (ambient + temp. rise) should not exceed 125 °C under worst case operating conditions. Circuit design, component placement, PCB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application

13 Precautions

13.1 Surface mounting

- Mounting and soldering condition should be checked beforehand.
- Applicable soldering process to this product is reflow soldering only.
- Recommended conditions for repair by soldering iron:
 - Preheat the circuit board with product to repair at 150 $^\circ\!\mathrm{C}$ for about 1 minute.
 - Put soldering iron on the land-pattern.
 - Soldering iron's temperature: 350 °C maximum/Duration: 3 seconds maximum/1 time for each terminal.
 - The soldering iron should not directly touch the inductor.
 - Product once removes from the circuit board may not be used again.

13.2 Handing

- Keep the products away from all magnets and magnetic objects.
- Be careful not to subject the products to excessive mechanical shocks.
- Please avoid applying impact to the products after mounted on pc board.
- Avoid ultrasonic cleaning.

13.3 Storage

• To maintain the solderability of terminal electrodes and to keep the packing material in good condition, temperature and humidity in the storage area should be controlled.

- Recommended conditions: -10℃~40℃, 70%RH (Max.)
- Even under ideal storage conditions, solderability of products electrodes may decrease as time passes. For this reason, product should be used with one year from the time of delivery.
- In case of storage over 6 months, solderability shall be checked before actual usage.

13.4 Regarding Regulations

- Any Class- I or Class- II ozone-depleting substance (ODS) listed in the Clean Air Act in US for regulation is not included in the products or applied to the products at any stage of whose manufacturing processes.
- Certain brominated flame retardants (PBBs, PBDEs) are not used at all.
- The products of this specification are not subject to the Export Trade Control Order in China or the Export Administration Regulations in US.

13.5 Guarantee

- The guaranteed operating conditions of the products are in accordance with the conditions specified in this specification.
- Please note that Sunlord takes no responsibility for any failure and/or abnormality which is caused by use under other than the aforesaid operating conditions.

14 Supplier Information

14.1 Supplier:

Shenzhen Sunlord Electronics Co., Ltd.

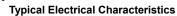
14.2 Manufacturer:

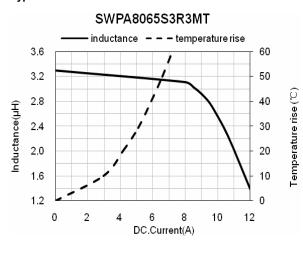
Shenzhen Sunlord Electronics Co., Ltd.

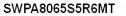
14.3 Manufacturing Address:

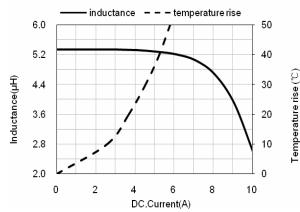
Sunlord Industrial Park, Dafuyuan Industrial Zone, Guanlan, Shenzhen, China Zip: 518110

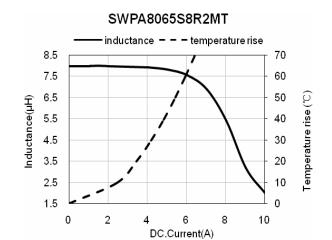
Appendix



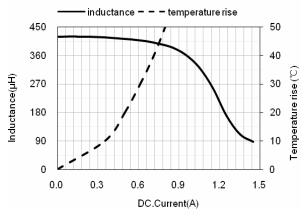


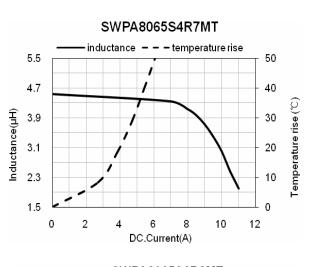


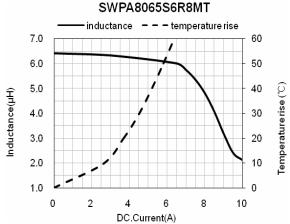




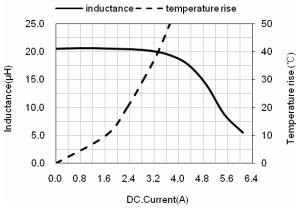












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