

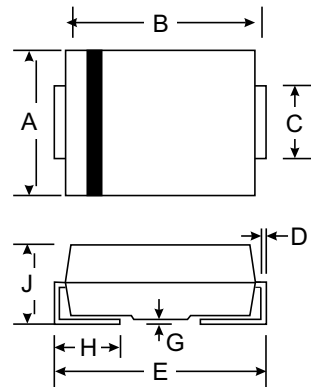
**VOLTAGE RANGE: 20 - 100V**  
**CURRENT: 2.0 A**

### Features

- Schottky Barrier Chip
- Ideally Suited for Automatic Assembly
- Low Power Loss, High Efficiency
- For Use in Low Voltage Application
- Guard Ring Die Construction
- Plastic Case Material has UL Flammability Classification Rating 94V-O

### Mechanical Data

- Case: SMA/DO-214AC, Molded Plastic
- Terminals: Solder Plated, Solderable per MIL-STD-750, Method 2026
- Polarity: Cathode Band or Cathode Notch
- Marking: Type Number
- Weight: 0.064 grams (approx.)



| SMA(DO-214AC)        |      |      |
|----------------------|------|------|
| Dim                  | Min  | Max  |
| A                    | 2.29 | 2.92 |
| B                    | 4.00 | 4.60 |
| C                    | 1.27 | 1.63 |
| D                    | 0.15 | 0.31 |
| E                    | 4.80 | 5.59 |
| G                    | 0.10 | 0.20 |
| H                    | 0.76 | 1.52 |
| J                    | 2.01 | 2.62 |
| All Dimensions in mm |      |      |

### Maximum Ratings and Electrical Characteristics T<sub>A</sub> = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

| Characteristic  | Symbol            | SS22A       | SS23A | SS24A | SS25A       | SS26A | SS28A | SS210A | Unit |
|---|-------------------|-------------|-------|-------|-------------|-------|-------|--------|------|
| Maximum repetitive peak reverse voltage   | V <sub>RRM</sub>  | 20          | 30    | 40    | 50          | 60    | 80    | 100    | V    |
| Maximum RMS voltage   | V <sub>RMS</sub>  | 14          | 21    | 28    | 35          | 42    | 56    | 70     | V    |
| Maximum DC blocking voltage   | V <sub>DC</sub>   | 20          | 30    | 40    | 50          | 60    | 80    | 100    | V    |
| Maximum average forward rectified current at T <sub>L</sub> (see fig.1)                             | I <sub>(AV)</sub> | 2.0         |       |       |             |       |       |        | A    |
| Peak forward surge current<br>8.3ms single half sine-wave superimposed on rated load (JEDEC Method) | I <sub>FSM</sub>  | 50.0        |       |       |             |       |       |        | A    |
| Maximum instantaneous forward voltage at 2.0A   | V <sub>F</sub>    | 0.55        |       | 0.70  |             | 0.85  |       | V      |      |
| Maximum DC reverse current<br>at rated DC blocking voltage  | I <sub>R</sub>    | 0.5         |       |       |             |       |       |        | mA   |
| <small>T<sub>A</sub>=25°C</small><br><small>T<sub>A</sub>=100°C</small>                             |                   | 20          |       | 10    |             |       |       |        |      |
| Typical junction capacitance (NOTE 1)   | C <sub>J</sub>    | 220         |       | 180   |             | pF    |       |        |      |
| Typical thermal resistance (NOTE 2)   | R <sub>θJA</sub>  | 75.0        |       |       |             |       |       |        | °C/W |
| Operating junction temperature range  | T <sub>J</sub>    | -65 to +125 |       |       | -65 to +150 |       |       | °C     |      |
| Storage temperature range   | T <sub>STG</sub>  | -65 to +150 |       |       |             |       |       |        | °C   |

- Note:**
1. Measured at 1MHz and applied reverse voltage of 4.0V D.C.
  2. P.C.B. mounted with 0.2x0.2" (5.0x5.0mm) copper pad areas

## RATINGS AND CHARACTERISTIC CURVES SS22A THRU SS210A

AVERAGE FORWARD RECTIFIED CURRENT, AMPERES

FIG. 1- FORWARD CURRENT DERATING CURVE

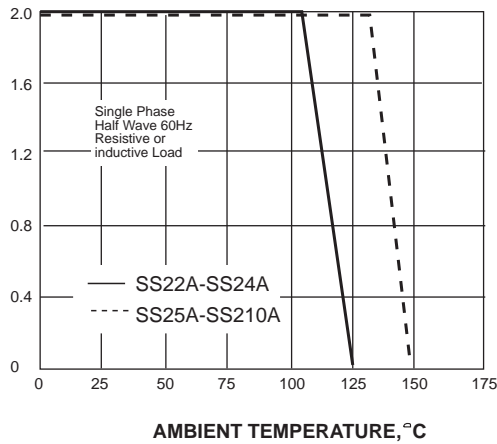


FIG. 2-MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT

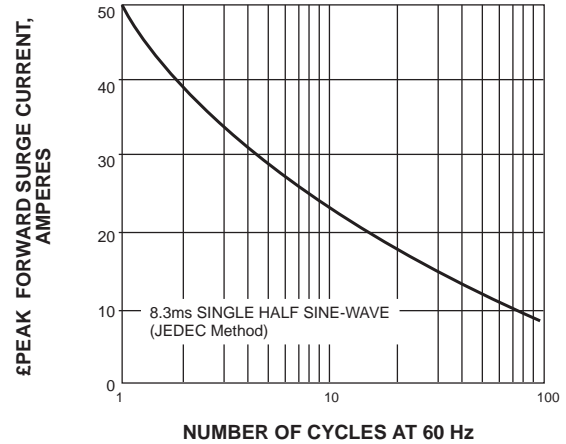


FIG. 3-TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

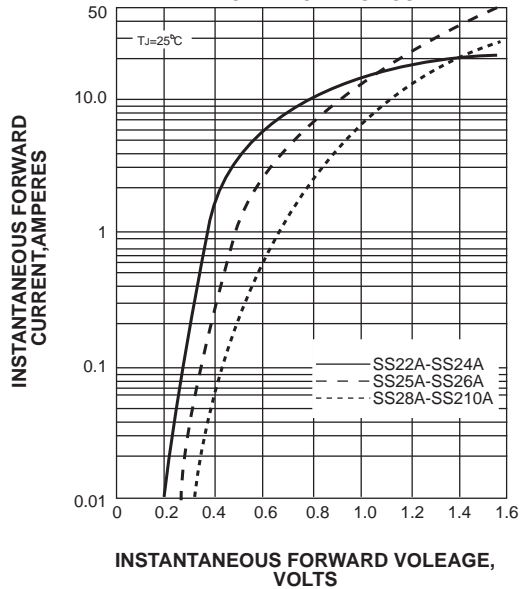


FIG. 4-TYPICAL REVERSE CHARACTERISTICS

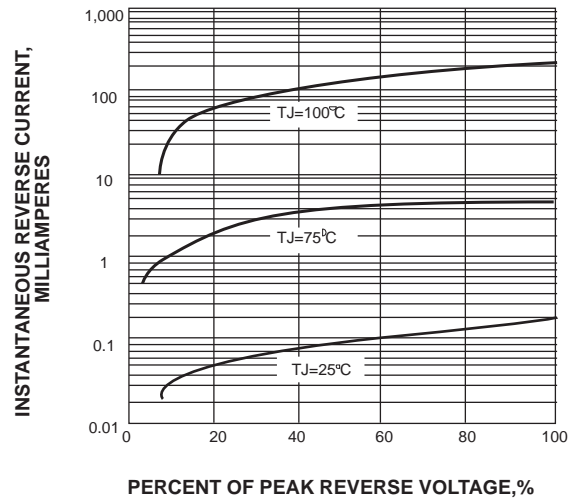


FIG. 5-TYPICAL JUNCTION CAPACITANCE

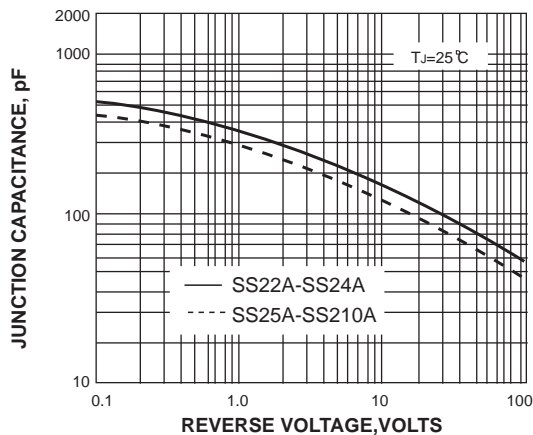
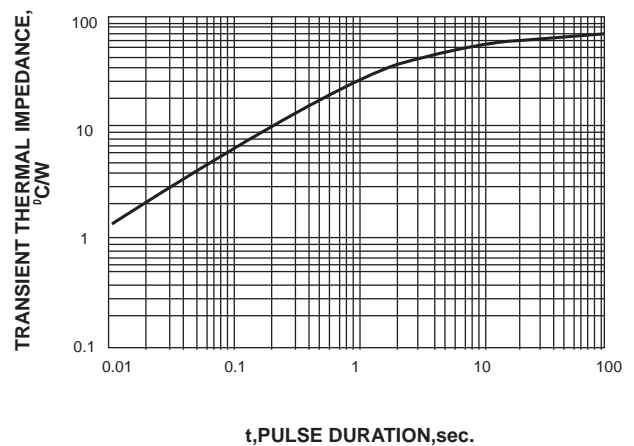


FIG. 6-TYPICAL TRANSIENT THERMAL IMPEDANCE



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