



SUPER-SEMI



SUPER-MOSFET

Super Junction Metal Oxide Semiconductor Field Effect Transistor

800V Super Junction Power MOSFET
SS*80R240S

Rev. 1.3
Aug. 2019

www.supersemi.com.cn

SSF80R240S/SSP80R240S/SSB80R240S 800V N-Channel MOSFET

Description

SJ-FET is new generation of high voltage MOSFET family that is utilizing an advanced charge balance mechanism for outstanding low on-resistance and lower gate charge performance. This advanced technology has been tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate and higher avalanche energy. SJ-FET is suitable for various AC/DC power conversion in switching mode operation for higher efficiency.

Features

- Multi-Epi process SJ-FET
- 850V @T_J = 150 °C
- Typ. R_{DS(on)} = 0.22Ω
- Ultra Low Gate Charge (typ. Q_g = 27.5nC)
- 100% avalanche tested

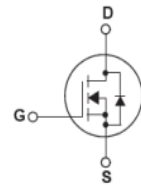
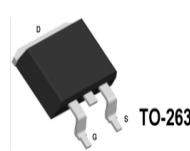
SSF80R240S



SSP80R240S



SSB80R240S



Absolute Maximum Ratings

Symbol	Parameter	SSP_B80R240S	SSF80R240S	Unit
V _{DSS}	Drain-Source Voltage	800		V
I _D	Drain Current			A
	-Continuous (TC = 25°C)	18.4*		
	-Continuous (TC = 100°C)	11.6*		
I _{DM}	Drain Current – Pulsed (Note 1)	51*		A
V _{GSS}	Gate-Source voltage	±30		V
E _{AS}	Single Pulsed Avalanche Energy (Note 2)	485		mJ
I _{AR}	Avalanche Current (Note 1)	3.5		A
E _{AR}	Repetitive Avalanche Energy (Note 1)	1		mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)	15		V/ns
dVds/dt	Drain Source voltage slope (Vds=640V)	50		V/ns
P _D	Power Dissipation (TC = 25°C)	151	35	W
T _J , T _{STG}	Operating and Storage Temperature Range	-55 to +150		°C
T _L	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds	300		°C

* Drain current limited by maximum junction temperature. Maximum duty cycle D=0.75.

Thermal Characteristics

Symbol	Parameter	SSP_B80R240S	SSF80R240S	Unit
R _{θJC}	Thermal Resistance, Junction-to-Case	0.83	3.7	°C/W
R _{θCS}	Thermal Resistance, Case-to-Sink Typ.	0.5	-	°C/W
R _{θJA}	Thermal Resistance, Junction-to-Ambient	62	80	°C/W



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Electrical Characteristics TC = 25°C unless otherwise noted

SSF80R240S/SSP80R240S/SSB80R240S 800V N-Channel MOSFET

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250μA, T _J = 25°C	800	-	-	V
		V _{GS} = 0V, I _D = 250μA, T _J = 150°C	-	850	-	V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I _D = 250μA, Referenced to 25°C	-	0.6	-	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 800V, V _{GS} = 0V -T _J = 150°C	-	- 10	1 -	μA μA
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 30V, V _{DS} = 0V	-	-	100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -30V, V _{DS} = 0V	-	-	-100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250μA	2.5	3.5	4.5	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10V, I _D = 9A	-	0.22	0.26	Ω
g _{FS}	Forward Trans conductance	V _{DS} = 40V, I _D = 18A	-	19	-	S
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} = 25V, V _{GS} = 0V, f = 1MHz	-	1290	-	pF
C _{oss}	Output Capacitance		-	380	-	pF
C _{rss}	Reverse Transfer Capacitance		-	22	-	pF
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{DD} = 400V, I _D = 10A R _G = 25Ω(Note 4)	-	40	-	ns
t _r	Turn-On Rise Time		-	21	-	ns
t _{d(off)}	Turn-Off Delay Time		-	139	-	ns
t _f	Turn-Off Fall Time		-	21	-	ns
Q _g	Total Gate Charge	V _{DS} = 450V, I _D = 10A V _{GS} = 10V (Note 4)	-	27.5	-	nC
Q _{gs}	Gate-Source Charge		-	6.3	-	nC
Q _{gd}	Gate-Drain Charge		-	11.2	-	nC
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain-Source Diode Forward Current		-	-	18	A
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current		-	-	51	A
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0V, I _F = 20A	-	1	1.5	V
t _{rr}	Reverse Recovery Time	V _R = 400V, V _{GS} = 0V, I _F = 20A, di _F /dt = 100A/μs	-	710	-	ns
Q _{rr}	Reverse Recovery Charge		-	13	-	μC
I _{rrm}	Peak reverse recovery Current		-	33	-	A

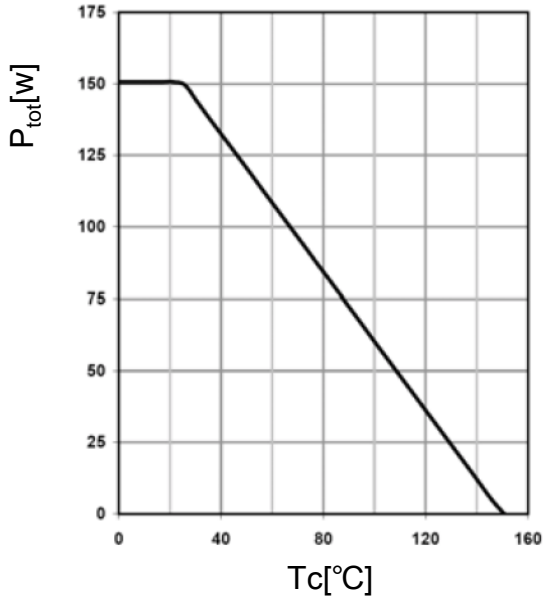
NOTES:

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. I_{AS}=3.5A, V_{GD}=50V, Starting T_J=25 °C
3. I_{SD}≤I_D, di/dt ≤ 200A/us, V_{DD} ≤ BV_{DSS}, Starting T_J = 25 °C
4. Essentially independent of Operating Temperature Typical Characteristics

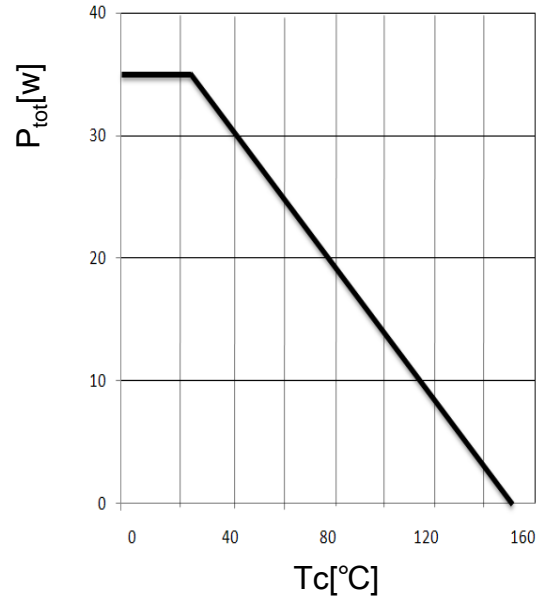


Typical Performance Characteristics

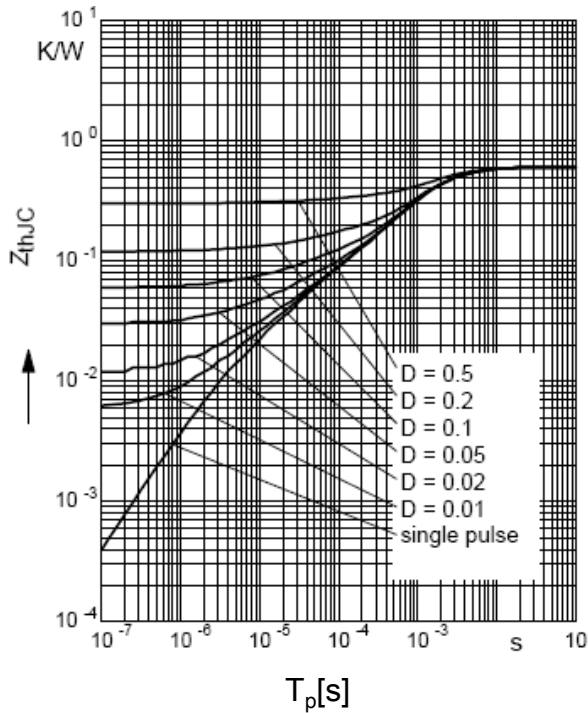
Power dissipation
TO-220, TO-263



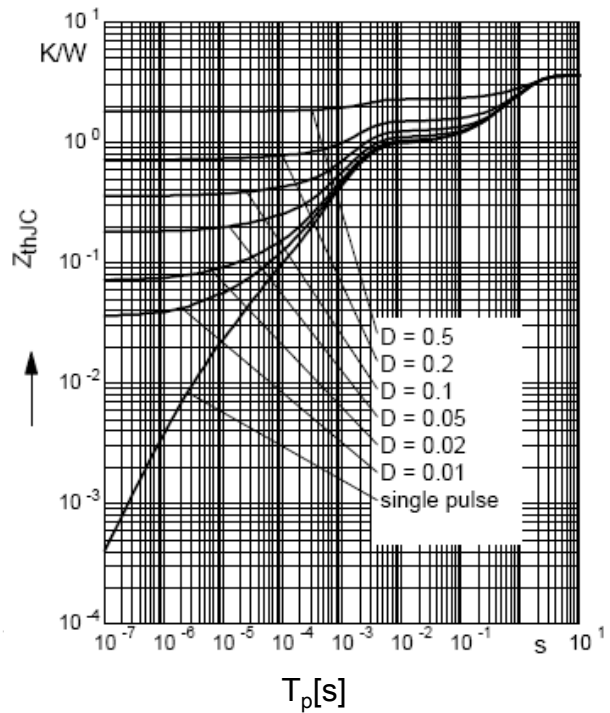
Power dissipation
TO-220F



Max. transient thermal impedance
TO-220, TO-263



Max. transient thermal impedance
TO-220F



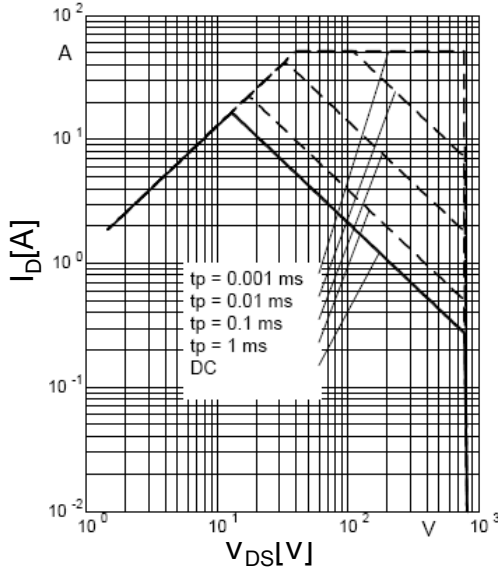
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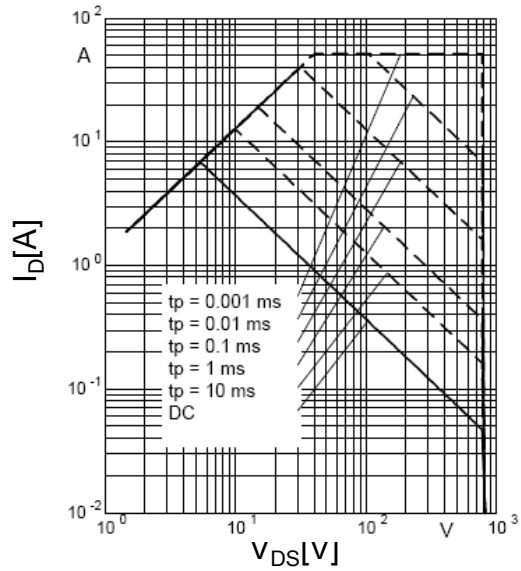
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Typical Performance Characteristics

Safe operating area $T_C=25\text{ }^\circ\text{C}$
TO-220, TO-263

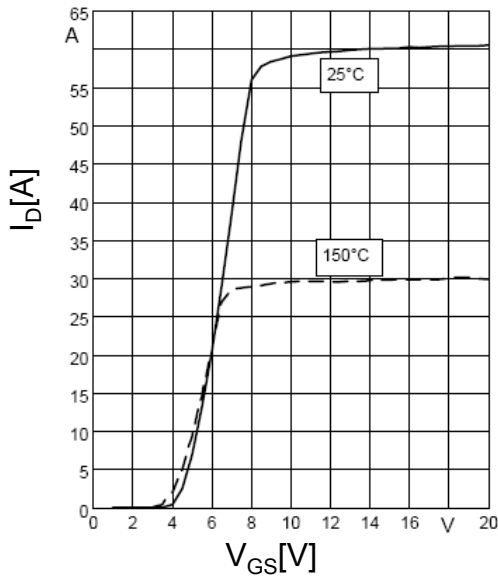


Safe operating area $T_C=25\text{ }^\circ\text{C}$
TO-220F



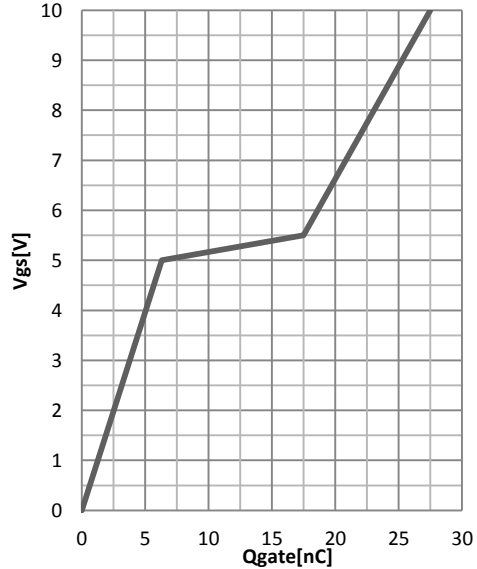
$I_D=f(V_{DS}); T_C=25\text{ }^\circ\text{C}; V_{GS} > 7\text{V}; D=0; \text{parameter } t_p$

Typ. transfer characteristics



$I_D=f(V_{GS}); V_{DS}=40\text{V}$

Typ. gate charge



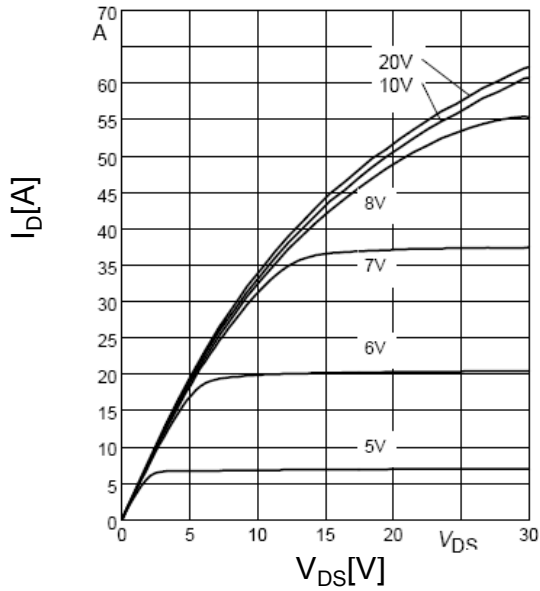
$V_{GS}=f(Q_g), I_D=18\text{ A pulsed}$



Typical Performance Characteristics

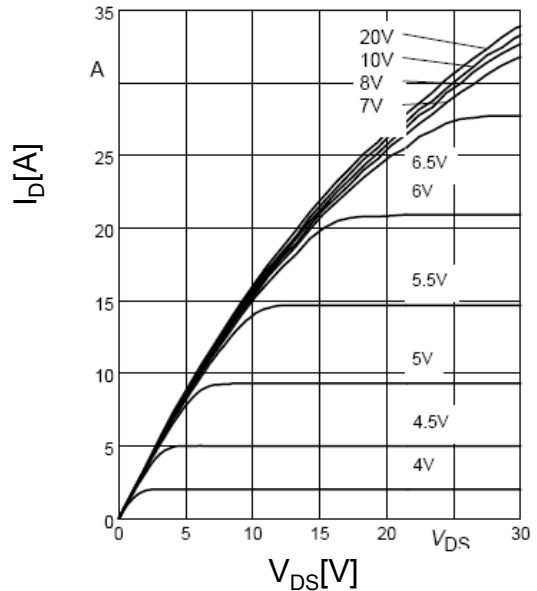
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Typ. output characteristics
 $T_j=25\text{ }^\circ\text{C}$



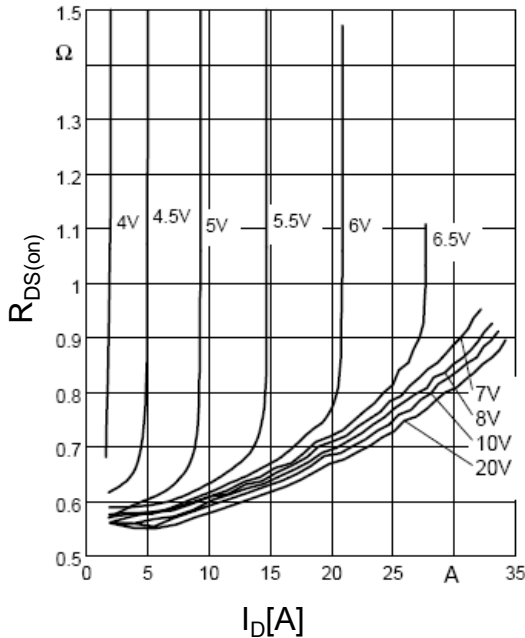
$I_D=f(V_{DS}); T_j=25\text{ }^\circ\text{C}$; parameter: V_{GS}

Typ. output characteristics
 $T_j=150\text{ }^\circ\text{C}$



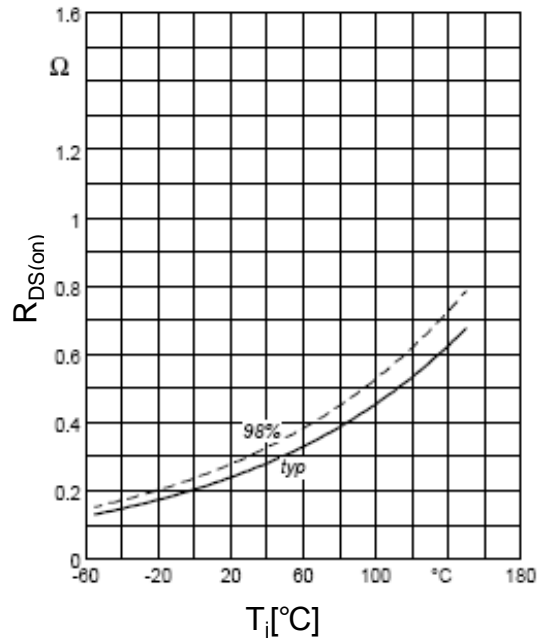
$I_D=f(V_{DS}); T_j=150\text{ }^\circ\text{C}$; parameter: V_{GS}

Typ. drain-source on-state resistance



$R_{DS(on)}=f(I_D); T_j=150\text{ }^\circ\text{C}$;
parameter: V_{GS}

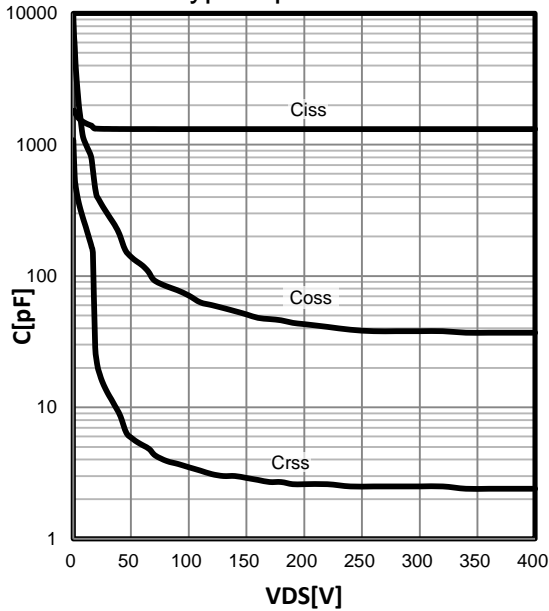
Typ. drain-source on-state resistance



$R_{DS(on)}=f(T_j); I_D=11\text{ A}; V_{GS}=10\text{ V}$

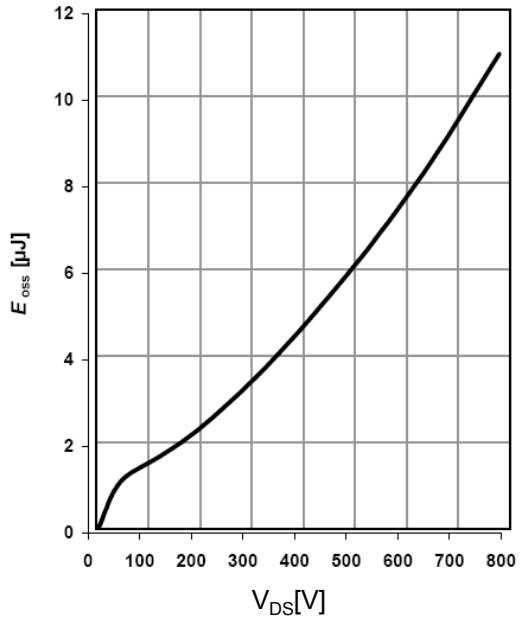
Typical Performance Characteristics

Typ. capacitances



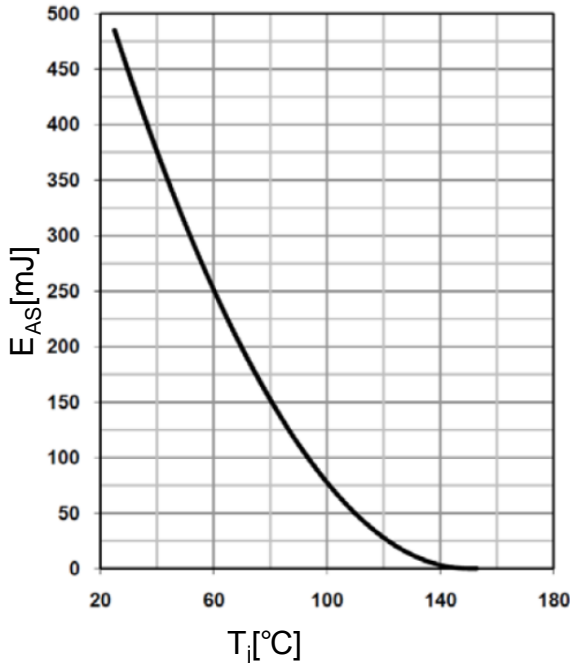
$$C=f(V_{DS}); V_{GS}=0\text{ V}; f=1\text{ MHz}$$

Typ. Coss stored energy



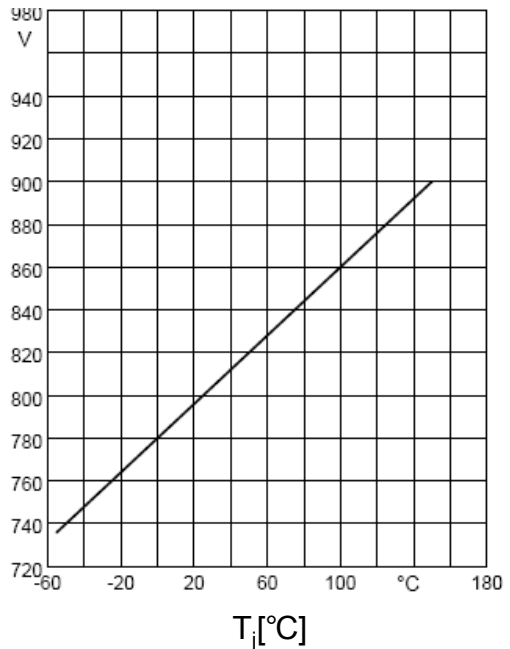
$$E_{OSS}=f(V_{DS})$$

Avalanche energy



$$E_{AS}=f(T_j); I_D=3.5\text{ A}; V_{DD}=50\text{ V}$$

Drain-source breakdown voltage

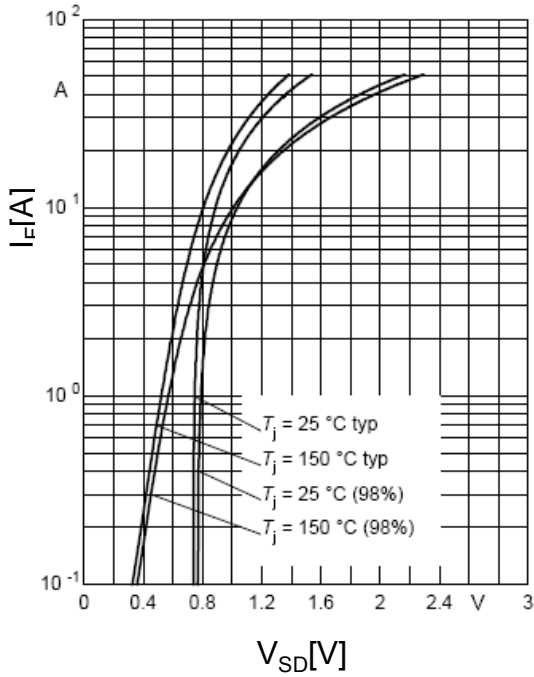


$$V_{BR(DSS)}=f(T_j); I_D=1.0\text{ mA}$$



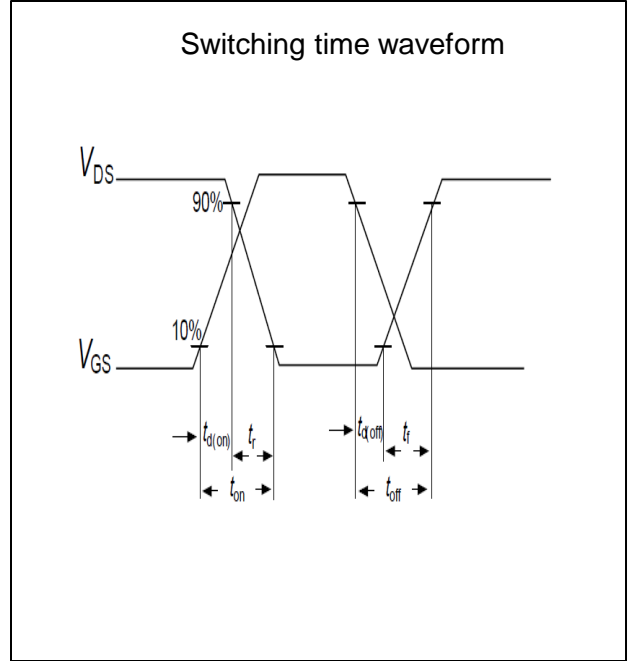
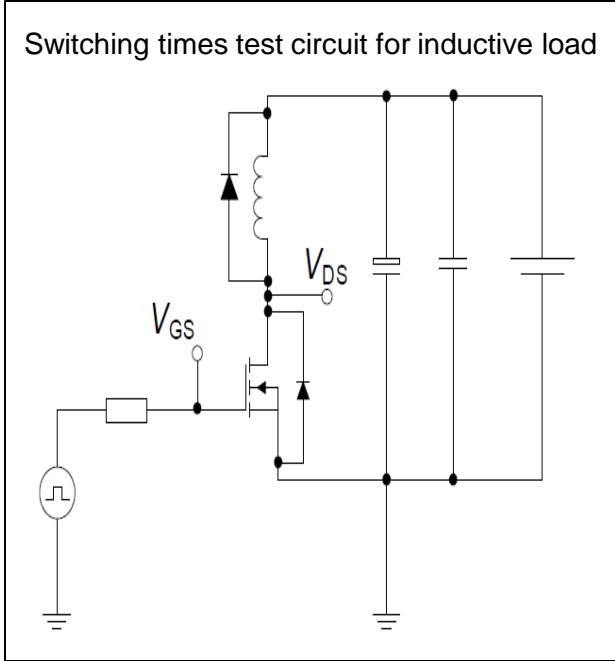
Typical Performance Characteristics

Forward characteristics of reverse diode

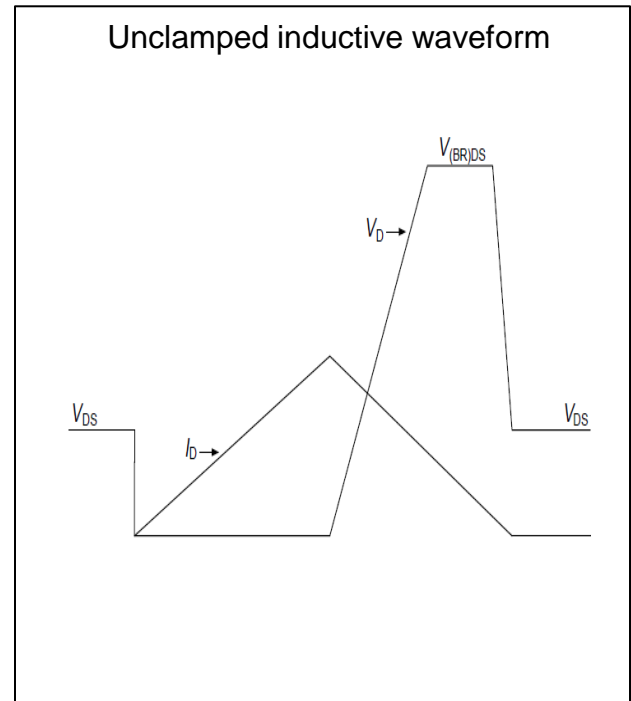
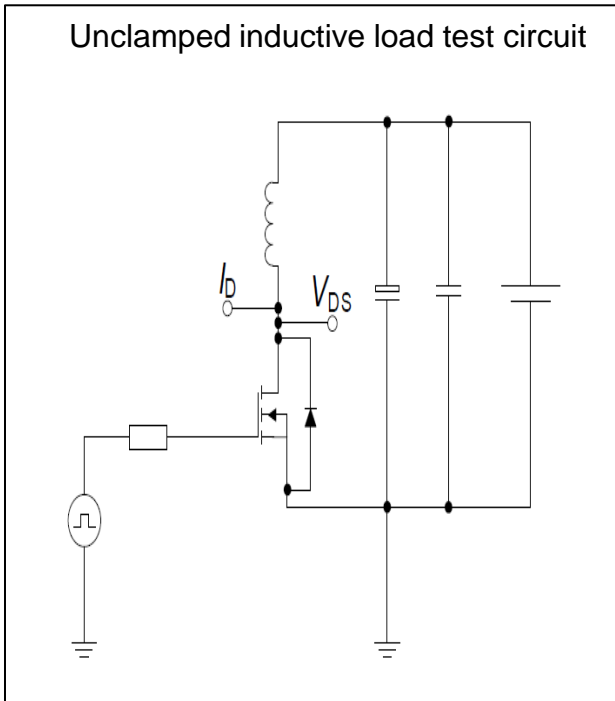


$I_F = f(V_{SD}); \text{ parameter: } T_j$

Switching times test circuit and waveform for inductive load

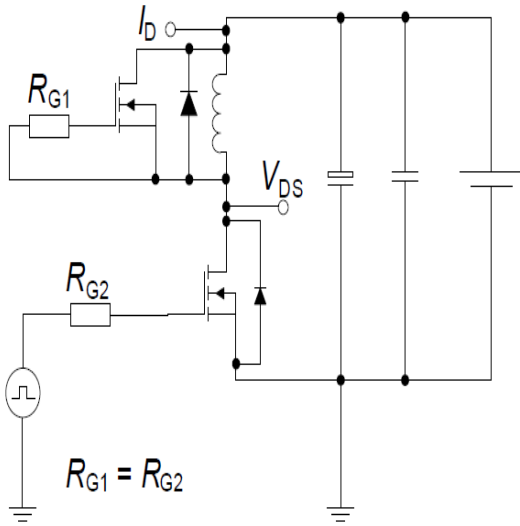


Unclamped inductive load test circuit and waveform

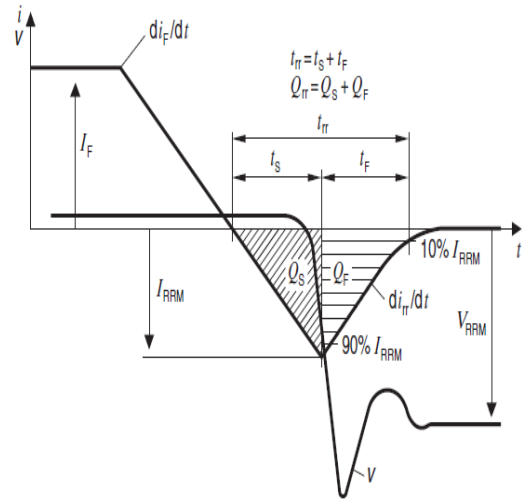


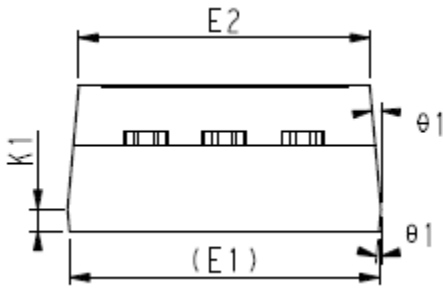
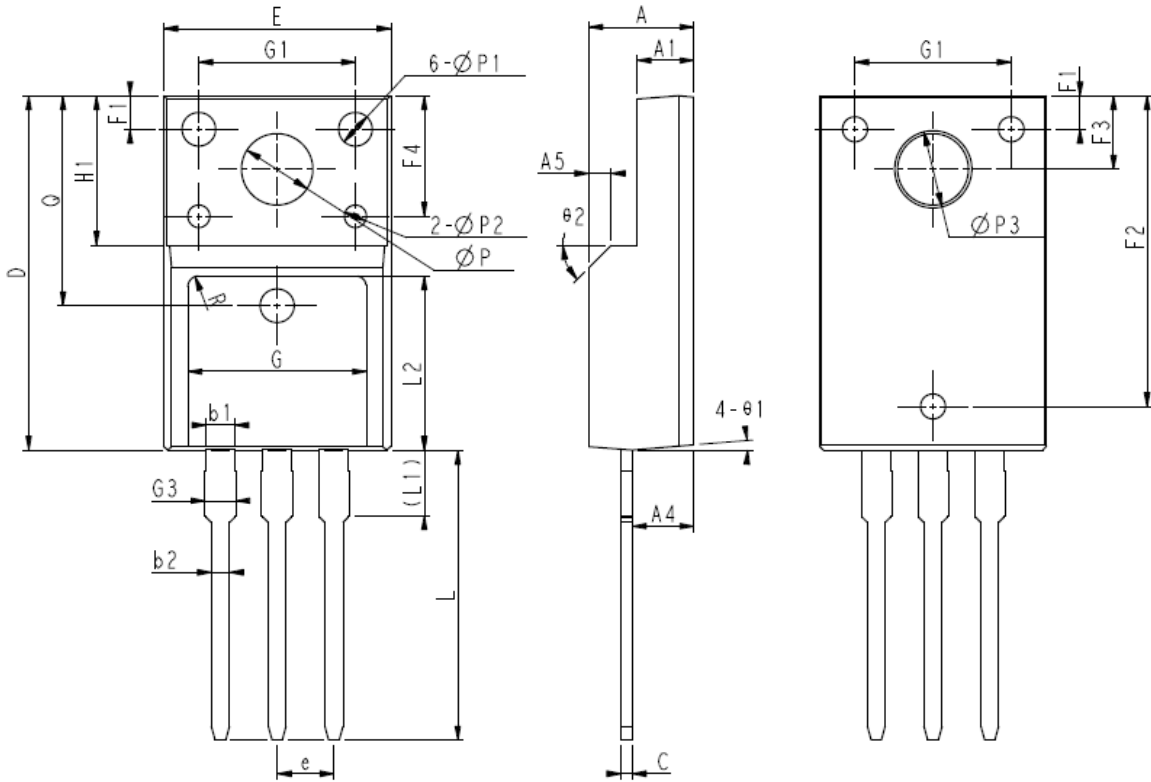
Test circuit and waveform for diode characteristics

Test circuit for diode characteristics



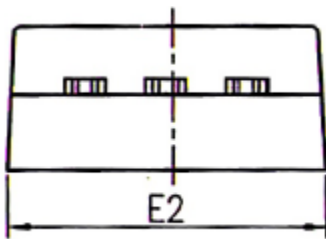
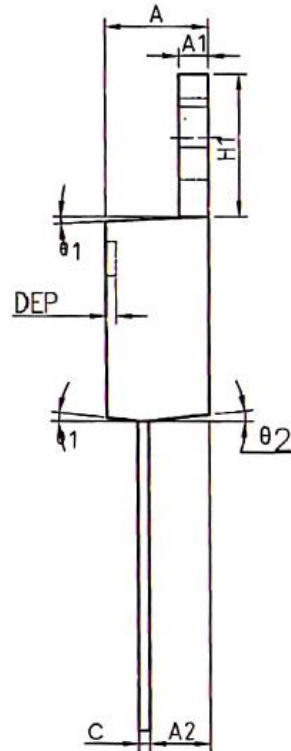
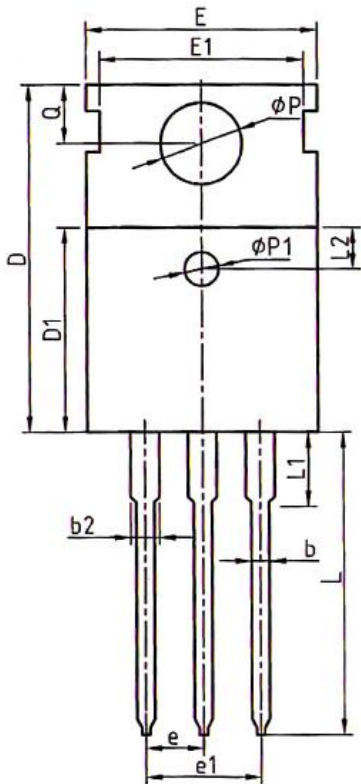
Diode recovery waveform





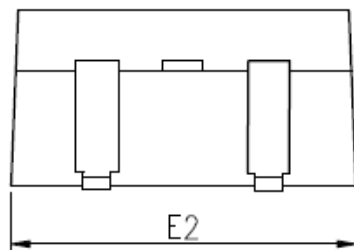
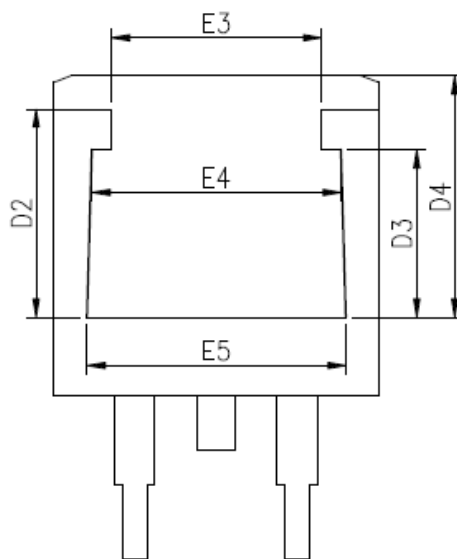
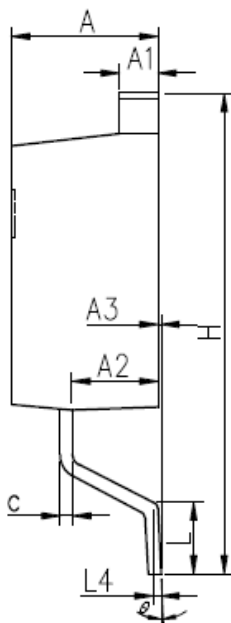
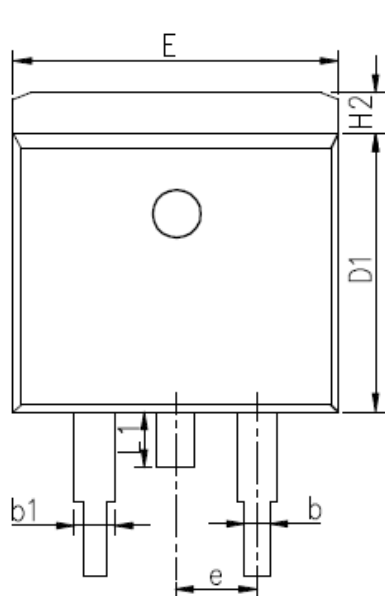
COMMON DIMENSIONS

SYMBOL	MM		
	MIN	NOM	MAX
E	10.00	10.16	10.32
E1	9.94	10.04	10.14
E2	9.36	9.46	9.56
A	4.50	4.70	4.90
A1	2.34	2.54	2.74
A4	2.66	2.76	2.86
A5	1.00REF		
c	0.45	0.50	0.60
D	15.67	15.87	16.07
Q	9.40REF		
H1	6.70REF		
e	2.54BSC		
Φ P	3.18REF		
L	12.78	12.98	13.18
L1	2.83	2.93	3.03
L2	7.70	7.80	7.90
Φ P1	1.40	1.50	1.60
Φ P2	0.95	1.00	1.05
Φ P3	3.45REF		
θ 1	3°	5°	7°
θ 2	-	45°	-
F1	1.00	1.50	2.00
F2	13.80	13.90	14.00
F3	3.20	3.30	3.40
F4	5.30	5.40	5.50
G	7.80	8.00	8.20
G1	6.90	7.00	7.10
G3	1.25	1.35	1.45
b1	1.23	1.28	1.38
b2	0.75	0.80	0.90
K1	0.65	0.70	0.75
R	0.50REF		



COMMON DIMENSIONS

SYMBOL	MM		
	MIN	NDM	MAX
A	4.40	4.57	4.70
A1	1.27	1.30	1.37
A2	2.35	2.40	2.50
b	0.77	0.80	0.90
b2	1.17	1.27	1.36
c	0.48	0.50	0.56
D	15.40	15.60	15.80
D1	9.00	9.10	9.20
DEP	0.05	0.10	0.20
E	9.80	10.00	10.20
E1	-	8.70	-
E2	9.80	10.00	10.20
$\phi P1$	1.40	1.50	1.60
e	2.54BSC		
e1	5.08BSC		
H1	6.40	6.50	6.60
L	12.75	13.50	13.65
L1	-	3.10	3.30
L2	2.50REF		
ϕP	3.50	3.60	3.63
Q	2.73	2.80	2.87
$\theta 1$	5°	7°	9°
$\theta 2$	1°	3°	5°
$\theta 3$	1°	3°	5°



COMMON DIMENSIONS

SYMBOL	MM		
	MIN	NOM	MAX
A	4.27	4.57	4.87
A1	1.22	1.27	1.42
A2	2.39	2.69	2.99
A3	0.00	0.13	0.20
b	0.70	0.81	1.01
b1	1.17	1.27	1.50
c	0.30	0.38	0.53
D1	8.40	8.70	9.00
D2	5.33	6.33	6.63
D3	4.54	5.54	5.84
D4	6.60	7.60	8.00
E	9.88	10.16	10.50
E2	9.80	10.10	10.40
E3	4.94	5.94	6.24
E4	6.67	7.67	7.97
E5	7.06	8.06	8.36
e	2.54 BSC		
H	14.70	15.10	15.50
H2	1.00	1.27	1.50
L	2.00	2.30	2.60
L1	1.35	1.55	1.75
L4	0.25 BSC		
θ	0°	5°	9°



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