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SUPER-MOSFET

Super Junction Metal Oxide Semiconductor Field Effect Transistor

800V Super Junction Power MOSFET SSB80R380S

Rev. 1.0 Jan. 2020

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September, 2013 SJ-FET

SSB80R380S 800V N-Channel MOSFET

Description

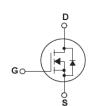
SSMOS-FET is new generation of high voltage MOSFET family that is utilizing an advanced charge balance mechanism for outstanding low on-resistance and lower gate charge performance. This advanced technology has been tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate and higher avalanche energy. SJ-FET is suitable for various AC/DC power conversion in switching mode operation for higher efficiency.

Features

- Multi-Epi process SJ-FET 850V @TJ = 150 °C
- Typ. RDS(on) = 0.36Ω • Ultra Low Gate Charge (typ. Qg = 17.5nC)
- 100% avalanche tested

SSB80R380S





Absolute Maximum Ratings

Symbol	Parameter		SSB80R380S	Unit
V _{DSS}	Drain-Source Voltage		800	V
I _D	Drain Current -Continuous (TC = 25°C) -Continuous (TC = 100°C	<u>.</u>)	13.6* 8.6*	А
I _{DM}	Drain Current - Pulsed	(Note 1)	40*	А
V_{GSS}	Gate-Source voltage		±30	V
E _{AS}	Single Pulsed Avalanche Energy	(Note 2)	284	mJ
I _{AR}	Repetitive Avalanche Current	(Note 1)	2.4	А
E _{AR}	Repetitive Avalanche Energy	(Note 1)	0.43	mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	15	V/ns
dVds/dt	Drain Source voltage slope (Vds=640V)		50	V/ns
P_D	Power Dissipation (TC = 25°C)		104	W
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +150	°C
T _L	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds		300	℃

^{*} Drain current limited by maximum junction temperature. Maximum duty cycle D=0.75.

Thermal Characteristics

Symbol	Parameter	SSB80R380S	Unit
R _{eJC}	Thermal Resistance, Junction-to-Case	1.2	°C/W
R _{ecs}	Thermal Resistance, Case-to-Sink Typ.	0.5	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	62	°C/W



Electrical Characteristics TC = 25°C unless otherwise noted

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Off Characterist	tics					
BVDSS	Drain-Source Breakdown Voltage	VGS = 0V, ID = 250µA, TJ = 25°C	800	-	-	V
		VGS = 0V, ID = 250μA, TJ = 150°C	-	850	-	V
ΔBVDSS/ΔTJ	Breakdown Voltage Temperature Coefficient	ID = 250µA, Referenced to 25°C	-	0.6	-	V/°C
IDSS	Zero Gate Voltage Drain Current	VDS = 800V, VGS = 0V -TJ = 150°C	-	- 10	1 -	μA μA
IGSSF	Gate-Body Leakage Current, Forward	VGS = 30V, VDS = 0V	-	-	100	nA
IGSSR	Gate-Body Leakage Current, Reverse	VGS = -30V, VDS = 0V	-	-	-100	nA
On Characterist	tics					
VGS(th)	Gate Threshold Voltage	VDS = VGS, ID = 250µA	2.5	3.5	4.5	V
RDS(on)	Static Drain-Source On-Resistance	VGS = 10V, ID = 7.5A	-	0.36	0.41	Ω
gFS	Forward Transconductance	VDS = 40V, ID = 15A	-	12	-	S
Dynamic Charac	cteristics					
Ciss	Input Capacitance	VDS = 25V, VGS = 0V,	-	800	-	pF
Coss	Output Capacitance	f = 1MHz	-	230	-	pF
Crss	Reverse Transfer Capacitance		-	15	-	pF
Switching Chara	acteristics					
td(on)	Turn-On Delay Time	VDD = 400V, ID = 7.5A, RG =	-	31	-	ns
tr	Turn-On Rise Time	25Ω(Note 4)	-	19	-	ns
td(off)	Turn-Off Delay Time		-	91	-	ns
tf	Turn-Off Fall Time		-	20	-	ns
Qg	Total Gate Charge	VDS = 450V, ID = 7.5A, VGS =	-	17.5	-	nC
Qgs	Gate-Source Charge	10V (Note 4)	-	4.1	-	nC
Qgd	Gate-Drain Charge		-	7.1	-	nC
Drain-Source Di	iode Characteristics and Maximum Rating	S				
Is	Maximum Continuous Drain-Source Di	Maximum Continuous Drain-Source Diode Forward Current		-	14	Α
Ism	Maximum Pulsed Drain-Source Diode	-Source Diode Forward Current		-	40	Α
VsD	Drain-Source Diode Forward Voltage	VGS = 0V, IS = 15A	-	0.9	1.5	V
trr	Reverse Recovery Time	$V_R = 400V, VGS = 0V,$	-	660	-	ns
Qrr	Reverse Recovery Charge	IF = 15A, dlr/dt =100A/µs	-	9.7	-	μC
I _{rrm}	Peak reverse recovery Current		-	25	-	A

NOTES:

- Repetitive Rating: Pulse width limited by maximum junction temperature 2. I_{AS} =2.4A, VDD=50V, Starting TJ=25 °C 3. I_{SD} =ID, di/dt ≤ 200A/us, V_{DD} ≤ BV_{DSS}, Starting TJ = 25 °C 4. Essentially Independent of Operating Temperature Typical Characteristics



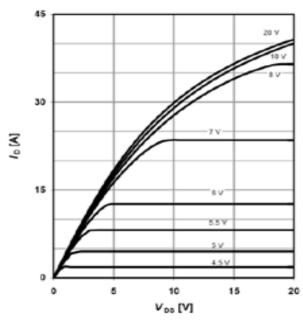


Figure 1: On-Region Characteristics@25°C

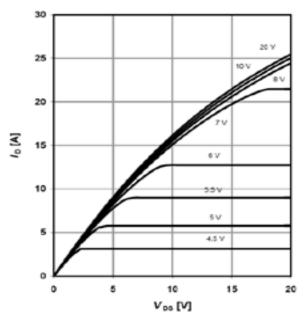


Figure 2: On-Region Characteristics@125°C

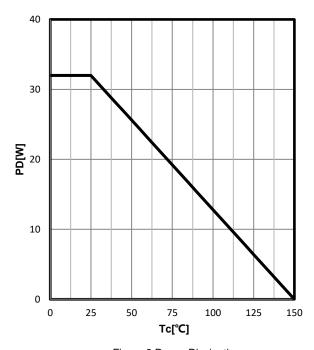


Figure 3:Power Dissipation

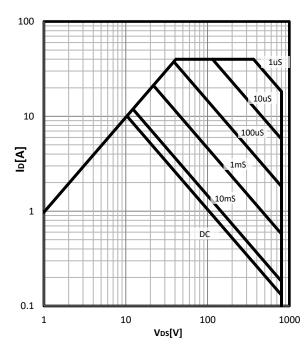


Figure 4: Maximum Forward Biased Safe Operating Area Tc=25°C



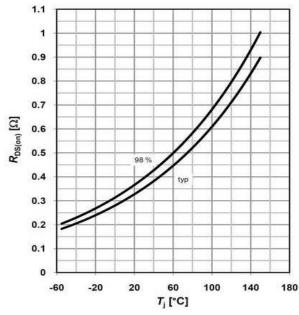


Figure 5: On-Resistance vs. Junction Temperature

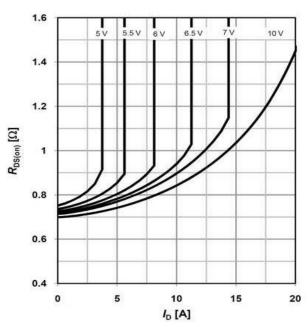


Figure 6: On-Resistance vs. Drain Current, Tj=125°C

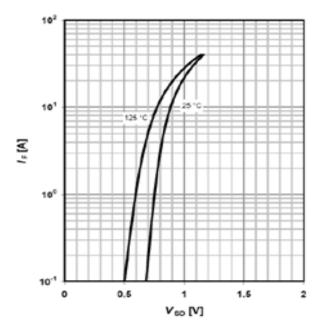


Figure 7: Body-Diode Characteristics

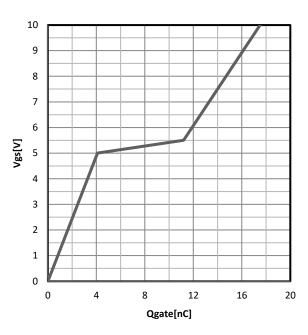


Figure 8: Gate-Charge Characteristics



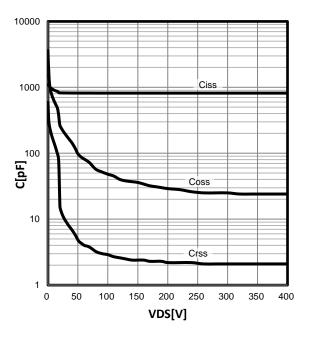


Figure 9: Capacitance Characteristics

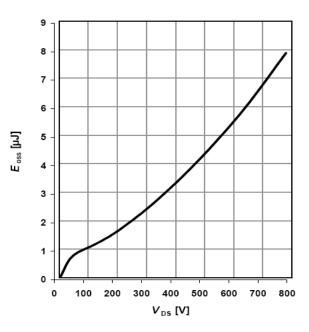


Figure 10: C_{oss} stored Energy

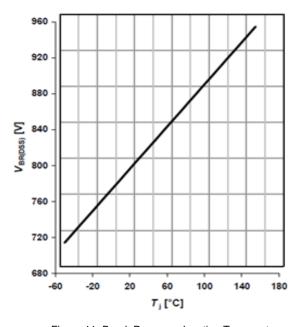


Figure 11: Break Down vs. Junction Temperature

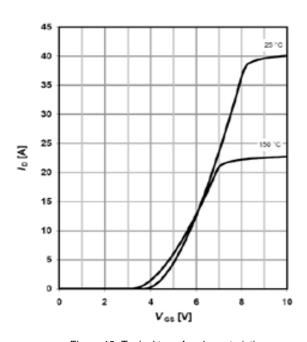


Figure 12: Typical transfer characteristics



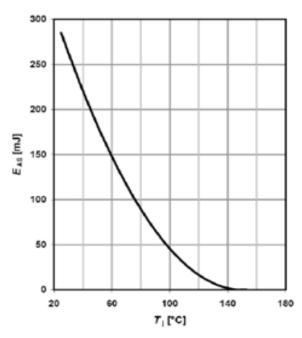


Figure 13: Avalanche energy

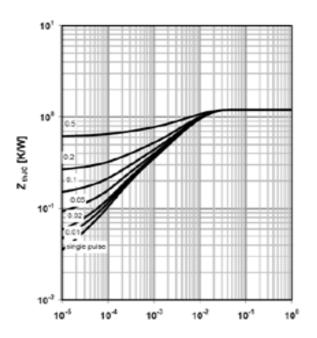
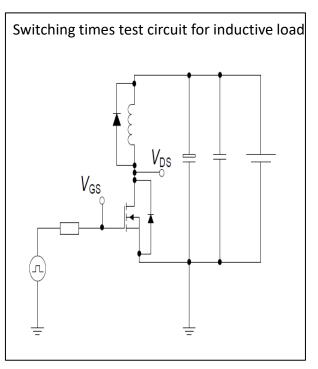


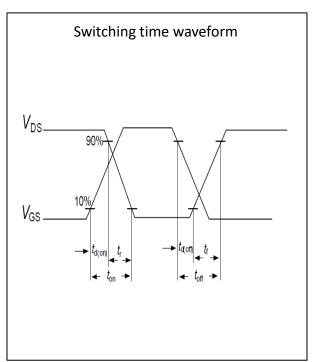
Figure 14: Maximum Transient Thermal Impedance



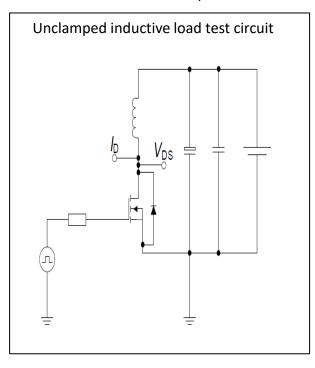
Test circuits

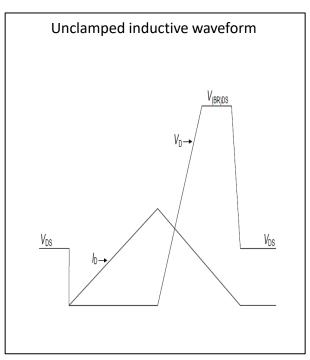
Switching times test circuit and waveform for inductive load





Unclamped inductive load test circuit and waveform

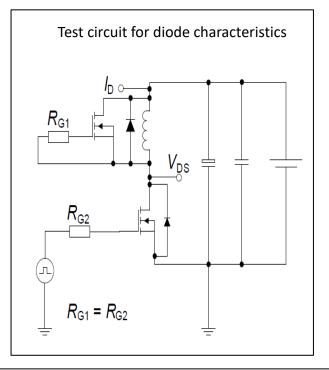


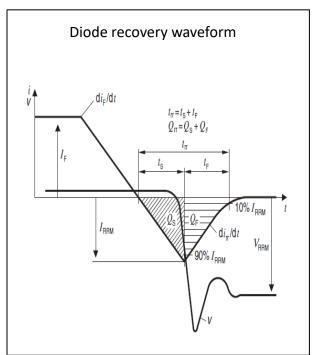


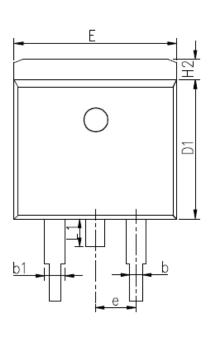


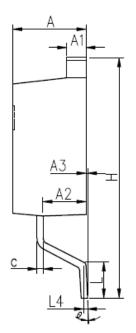
Test circuits

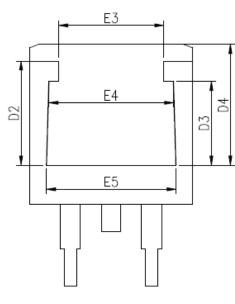
Test circuit and waveform for diode characteristics

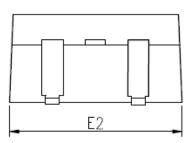












COMMON DIMENSIONS

SYMBOL	MM			
STIVIDOL	MIN	NOM	MAX	
Α	4.27	4.57	4.87	
A1	1.22	1.27	1.42	
A2	2.39	2.69	2.99	
A3	0.00	0.13	0.20	
b	0.70	0.81	1.01	
b1	1.17	1.27	1.50	
С	0.30	0.38	0.53	
D1	8.40	8.70	9.00	
D2	5.33	6.33	6.63	
D3	4.54	5.54	5.84	
D4	6.60	7.60	8.00	
Е	9.88	10.16	10.50	
E2	9.80	10.10	10.40	
E3	4.94	5.94	6.24	
E4	6.67	7.67	7.97	
E5	7.06	8.06	8.36	
е	2.54 BSC			
Н	14.70	15.10	15.50	
H2	1.00	1.27	1.50	
L	2.00	2.30	2.60	
L1	1.35	1.55	1.75	
L4	0.25 BSC			
θ	0°	5°	9°	



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