



SUPER-SEMI



## SUPER-MOSFET

Super Junction Metal Oxide Semiconductor Field Effect Transistor

700V Super Junction Power MOSFET Gen- II  
SSF70R450S

Rev. 1.0  
Dec. 2018

[www.supersemi.com.cn](http://www.supersemi.com.cn)



# SSF70R450S

## 700V N-Channel Super-Junction MOSFET Gen-II

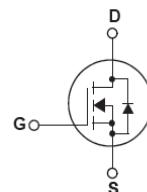
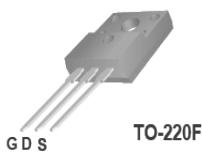
### Description

SJ-FET is new generation of high voltage MOSFET family that is utilizing an advanced charge balance mechanism for outstanding low on-resistance and lower gate charge performance. This advanced technology has been tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate and higher avalanche energy. SJ-FET is suitable for various AC/DC power conversion in switching mode operation for higher efficiency.

### Features

- Multi-Epi process SJ-FET
- 750V @ $T_J = 150^\circ\text{C}$
- Typ.  $R_{DS(on)} = 0.38\Omega$
- Ultra Low Gate Charge (typ.  $Q_g = 19.5\text{nC}$ )
- 100% avalanche tested

SSF70R450S



### Absolute Maximum Ratings

Symbol	Parameter	SSF70R450S	Unit
$V_{DSS}$	Drain-Source Voltage	700	V
$I_D$	Drain Current - Continuous ( $TC = 25^\circ\text{C}$ )	10.5*	A
	- Continuous ( $TC = 100^\circ\text{C}$ )	6.7*	
$I_{DM}$	Drain Current - Pulsed (Note 1)	42	A
$V_{GSS}$	Gate-Source voltage	$\pm 30$	V
$E_{AS}$	Single Pulsed Avalanche Energy (Note 2)	142	mJ
$I_{AS}$	Avalanche current, repetitive or not-repetitive (pulse width limited by $T_j$ max)	3.1	A
$dv/dt$	Peak Diode Recovery $dv/dt$ (Note 3)	15	V/ns
$dV_{ds}/dt$	Drain Source voltage slope ( $V_{ds}=480\text{V}$ )	50	V/ns
$P_D$	Power Dissipation ( $TC = 25^\circ\text{C}$ )	31	W
$T_J, T_{STG}$	Operating and Storage Temperature Range	-55 to +150	$^\circ\text{C}$
$T_L$	Maximum Lead Temperature for Soldering Purpose, 1/16" from Case for 10 Seconds	260	$^\circ\text{C}$

\* Drain current limited by maximum junction temperature. Maximum duty cycle D=0.75.

### Thermal Characteristics

Symbol	Parameter	SSF70R450S	Unit
$R_{eJC}$	Thermal Resistance, Junction-to-Case	4.0	$^\circ\text{C/W}$
$R_{eCS}$	Thermal Resistance, Case-to-Sink Typ.	-	$^\circ\text{C/W}$
$R_{eJA}$	Thermal Resistance, Junction-to-Ambient	80	$^\circ\text{C/W}$



# Electrical Characteristics TC = 25°C unless otherwise noted

SSF70R450S 700V N-Channel Super-Junction MOSFET Gen-II

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
BVdss	Drain-Source Breakdown Voltage	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA, T <sub>J</sub> = 25°C	700	-	-	V
		V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA, T <sub>J</sub> = 150°C	-	750	-	V
ΔBV <sub>DSS</sub> /ΔT <sub>J</sub>	Breakdown Voltage Temperature Coefficient	I <sub>D</sub> = 250μA, Referenced to 25°C	-	0.6	-	V/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>D</sub> S = 700V, V <sub>G</sub> S = 0V -T <sub>C</sub> = 125°C	-	-	100	μA
I <sub>GSSF</sub>	Gate-Body Leakage Current, Forward	V <sub>G</sub> S = 30V, V <sub>D</sub> S = 0V	-	-	100	nA
I <sub>GSSR</sub>	Gate-Body Leakage Current, Reverse	V <sub>G</sub> S = -30V, V <sub>D</sub> S = 0V	-	-	-100	nA
<b>On Characteristics</b>						
V <sub>G</sub> (th)	Gate Threshold Voltage	V <sub>D</sub> S = V <sub>G</sub> S, I <sub>D</sub> = 250μA	2.0	3.0	4.0	V
R <sub>D</sub> S(on)	Static Drain-Source On-Resistance	V <sub>G</sub> S = 10V, I <sub>D</sub> = 5.5A	-	0.38	0.45	Ω
<b>Dynamic Characteristics</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>D</sub> S = 100V, V <sub>G</sub> S = 0V, f = 1.0MHz	-	700	-	pF
C <sub>oss</sub>	Output Capacitance		-	29	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		-	0.4	-	pF
Q <sub>g</sub>	Total Gate Charge	V <sub>D</sub> S = 400V, I <sub>D</sub> = 11A, V <sub>G</sub> S = 10V (Note 4)	-	19.5	-	nC
Q <sub>gs</sub>	Gate-Source Charge		-	4.5	-	nC
Q <sub>gd</sub>	Gate-Drain Charge		-	8.5	-	nC
R <sub>g</sub>	Gate resistance	f=1 MHz, open drain	-	8.2	-	Ω
<b>Switching Characteristics</b>						
t <sub>d</sub> (on)	Turn-On Delay Time	V <sub>D</sub> S = 400V, I <sub>D</sub> = 5.5A R <sub>G</sub> = 10Ω, V <sub>G</sub> S = 10V (Note 4)	-	12.2	-	ns
t <sub>r</sub>	Turn-On Rise Time		-	22.5	-	ns
t <sub>d</sub> (off)	Turn-Off Delay Time		-	40	-	ns
t <sub>f</sub>	Turn-Off Fall Time		-	19.5	-	ns
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
I <sub>S</sub>	Maximum Continuous Drain-Source Diode Forward Current	V <sub>G</sub> S = 0V, I <sub>S</sub> = 11A	-	-	10.5	A
I <sub>SM</sub>	Maximum Pulsed Drain-Source Diode Forward Current		-	-	42	A
V <sub>SD</sub>	Drain-Source Diode Forward Voltage	V <sub>G</sub> S = 0V, I <sub>S</sub> = 11A	-	0.9	1.4	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>G</sub> S = 0V, V <sub>D</sub> S = 400V, I <sub>S</sub> = 5.5A, dI/dt = 100A/μs	-	240	-	ns
Q <sub>rr</sub>	Reverse Recovery Charge		-	1.74	-	μC
I <sub>rrm</sub>	Peak Reverse Recovery Current		-	14.5	-	A

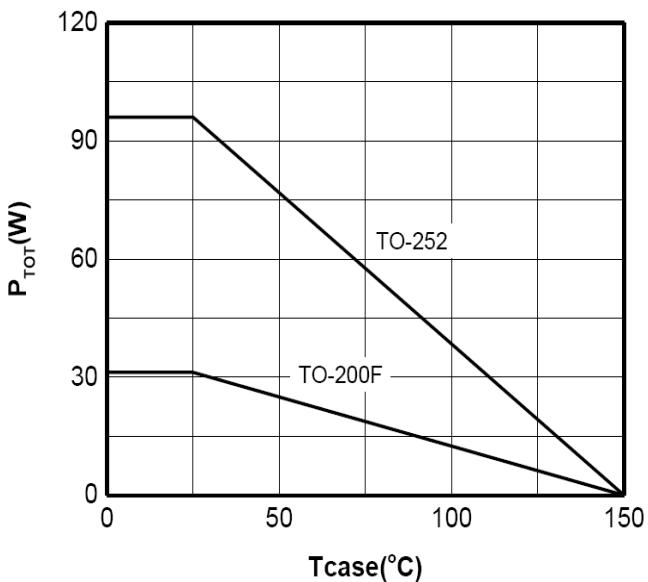
## NOTES:

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. I<sub>D</sub>=I<sub>AS</sub>, V<sub>D</sub>D=50V, Starting T<sub>J</sub>=25 °C
3. I<sub>SD</sub>≤I<sub>D</sub>, di/dt ≤ 200A/μs, V<sub>DD</sub>≤ BV<sub>DSS</sub>. Starting T<sub>J</sub> = 25 °C
4. Essentially Independent of Operating Temperature Typical Characteristics

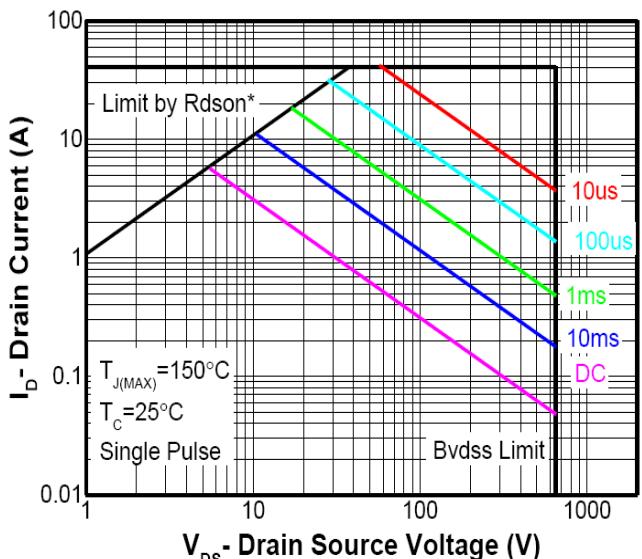


## Typical Performance Characteristics

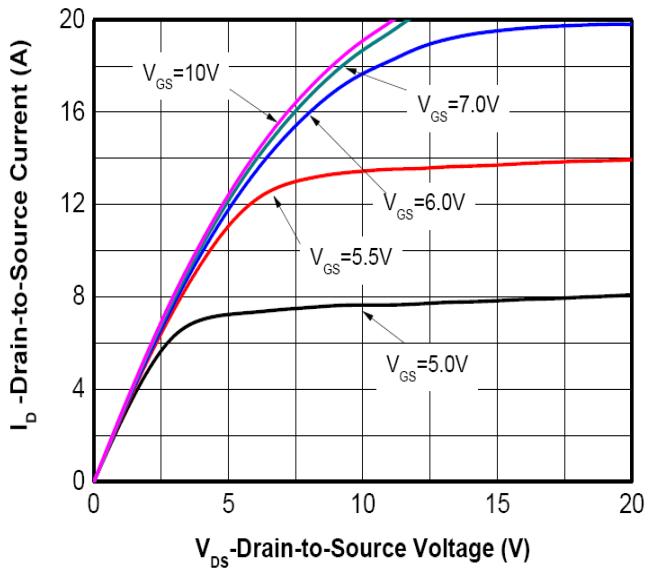
Power dissipation



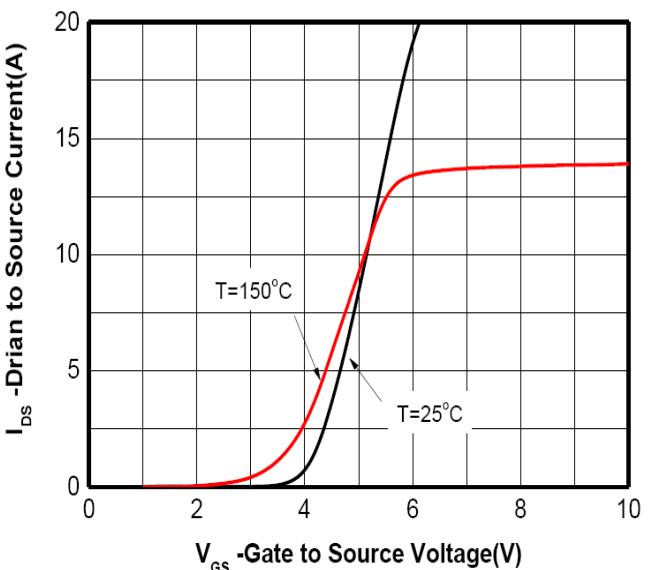
Safe operating area  $TC=25\text{ }^{\circ}\text{C}$   
TO-220FullPAK



Typ. output characteristics  $T_f=25\text{ }^{\circ}\text{C}$



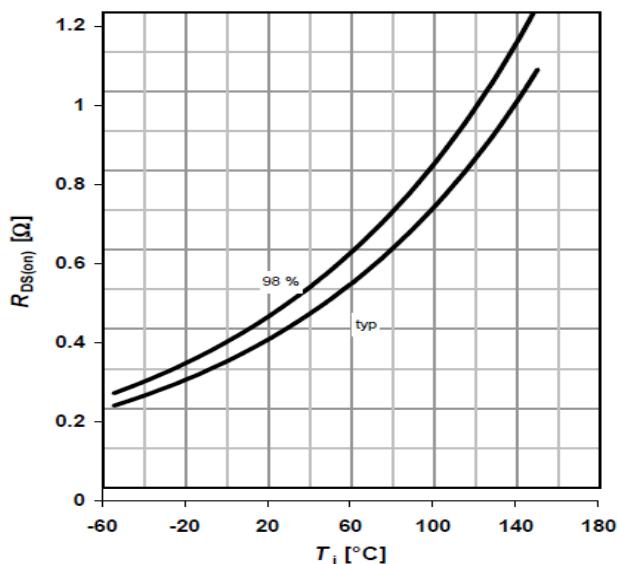
Typ. transfer characteristics



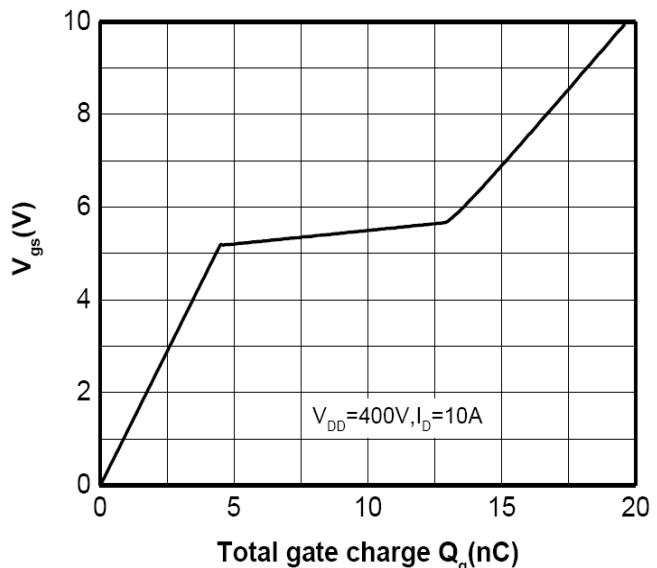


## Typical Performance Characteristics

Typ. drain-source on-state resistance

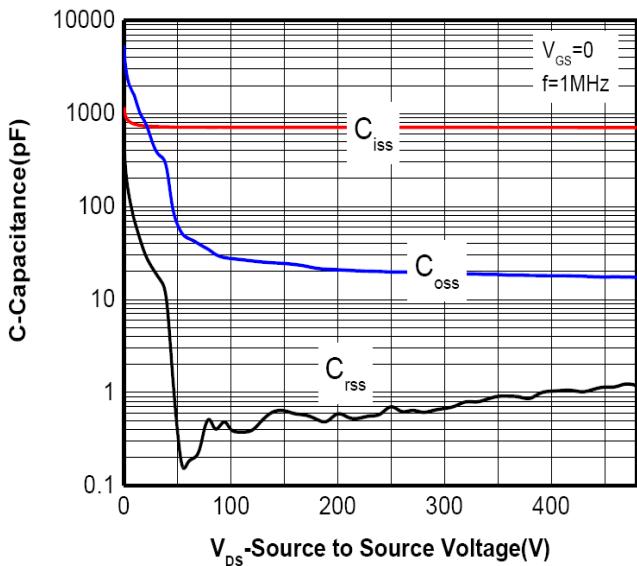


Typ. gate charge characteristics

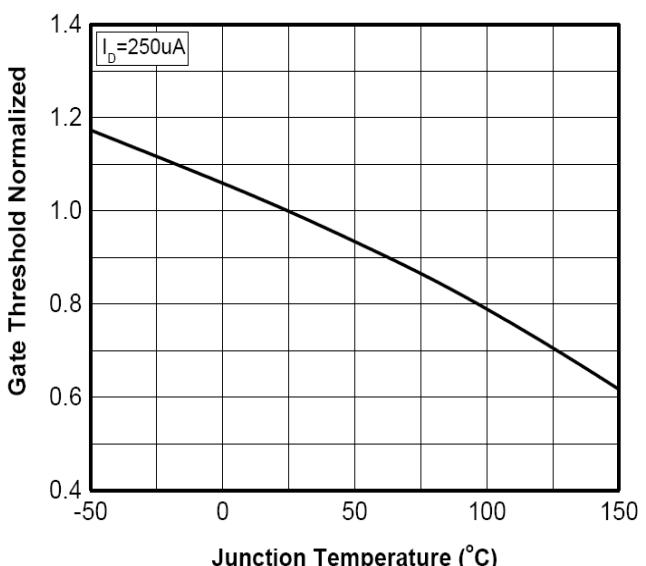


SSF70R450S 700V N-Channel Super-Junction MOSFET Gen-II

Typ. capacitances



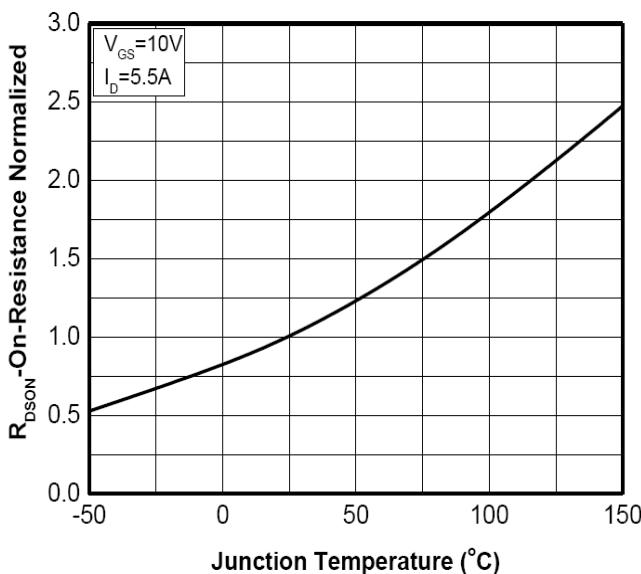
Normalized  $V_{GS(th)}$  characteristics



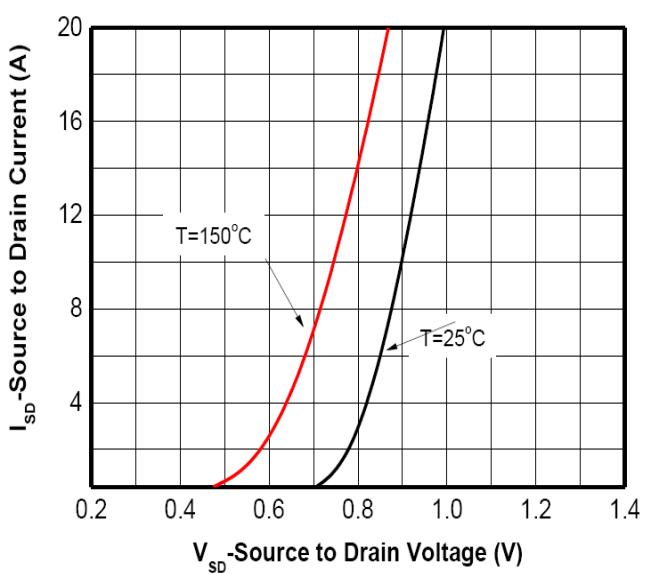


## Typical Performance Characteristics

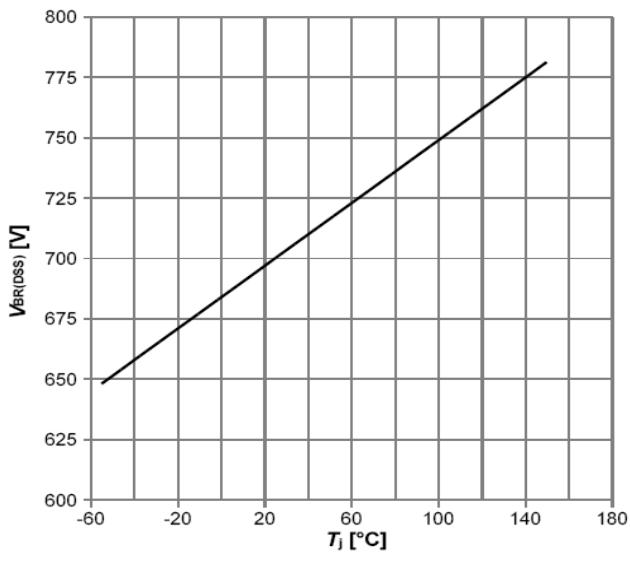
Normalized on resistance vs temperature



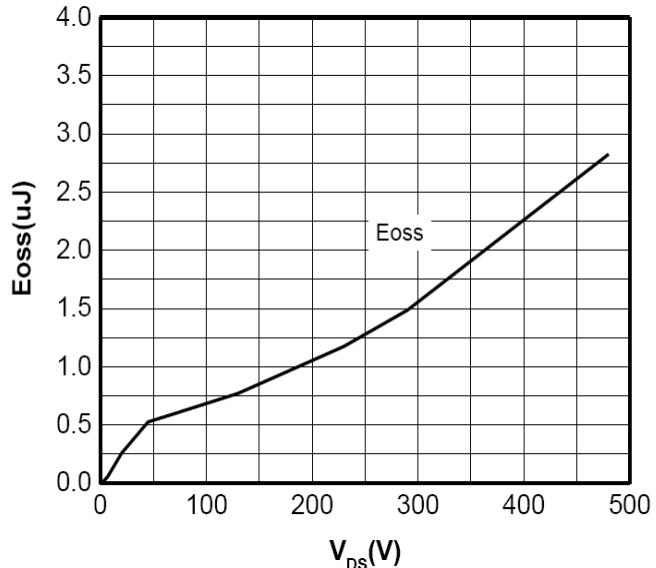
Forward characteristics of reverse diode



Drain-source breakdown voltage



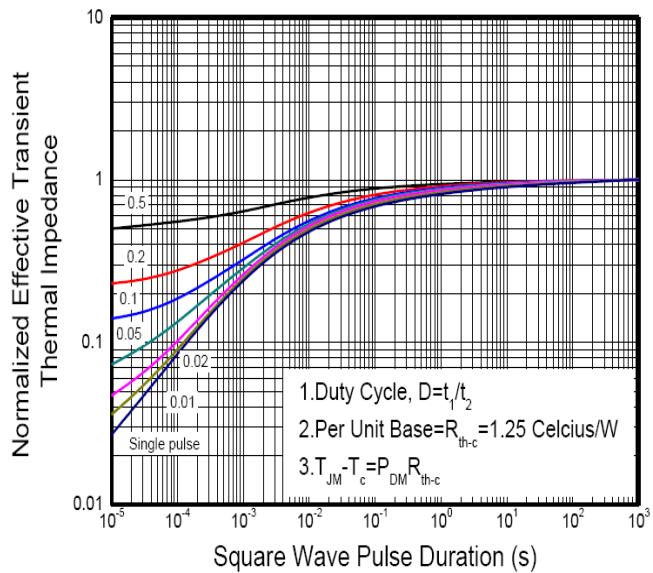
Cross stored energy





## Typical Performance Characteristics

Max. transient thermal impedance  
TO-220FullPAK



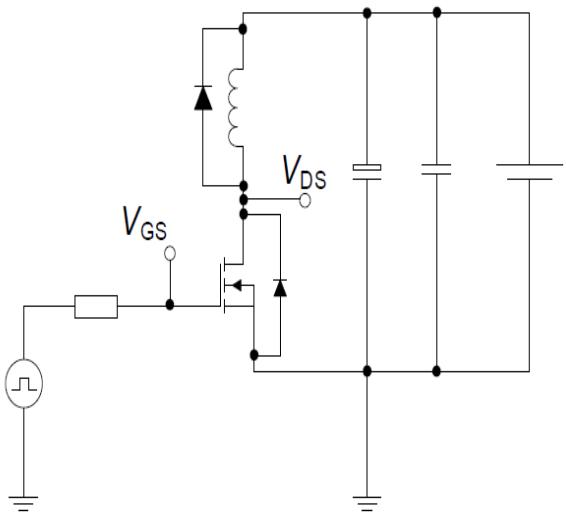


SUPER

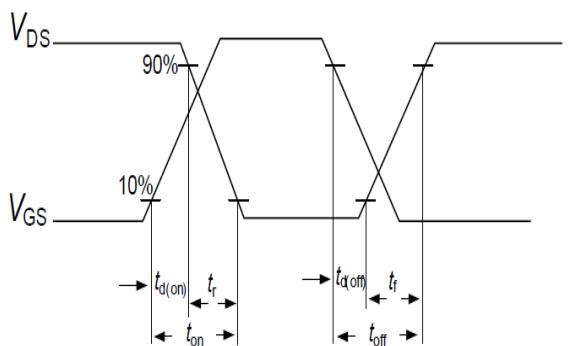
## Test circuits

### Switching times test circuit and waveform for inductive load

Switching times test circuit for inductive load

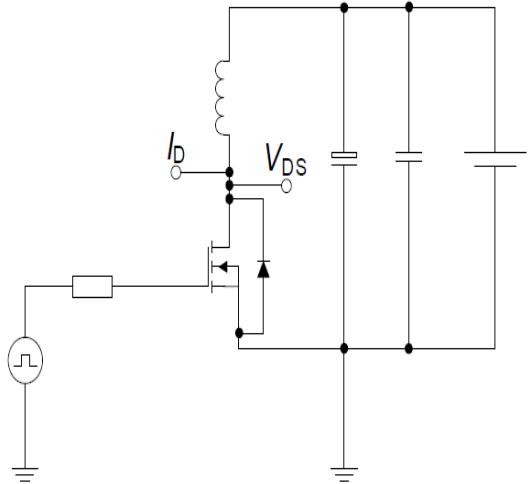


Switching time waveform

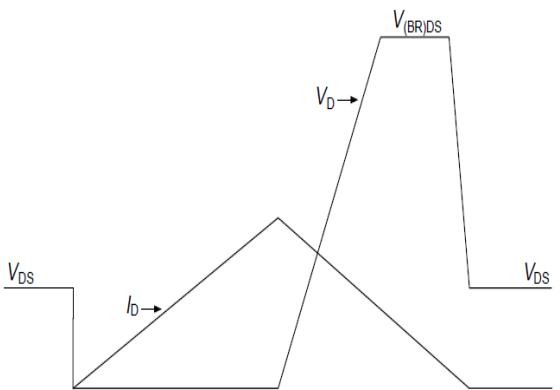


### Unclamped inductive load test circuit and waveform

Unclamped inductive load test circuit



Unclamped inductive waveform



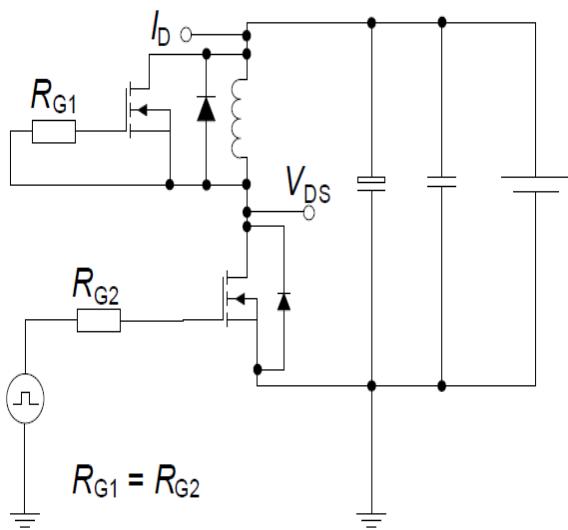


SUPER

## Test circuits

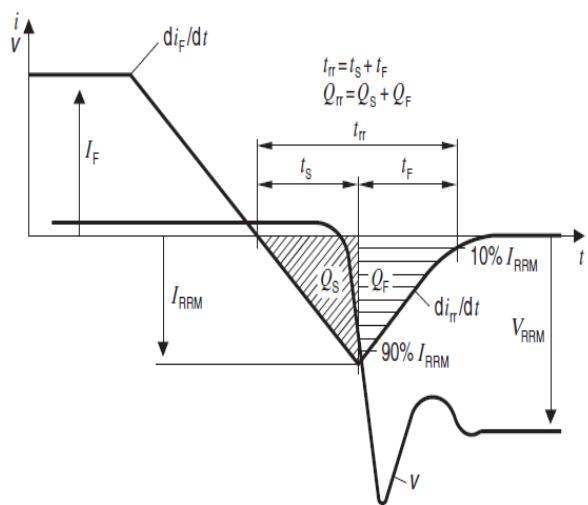
### Test circuit and waveform for diode characteristics

Test circuit for diode characteristics



$$R_{G1} = R_{G2}$$

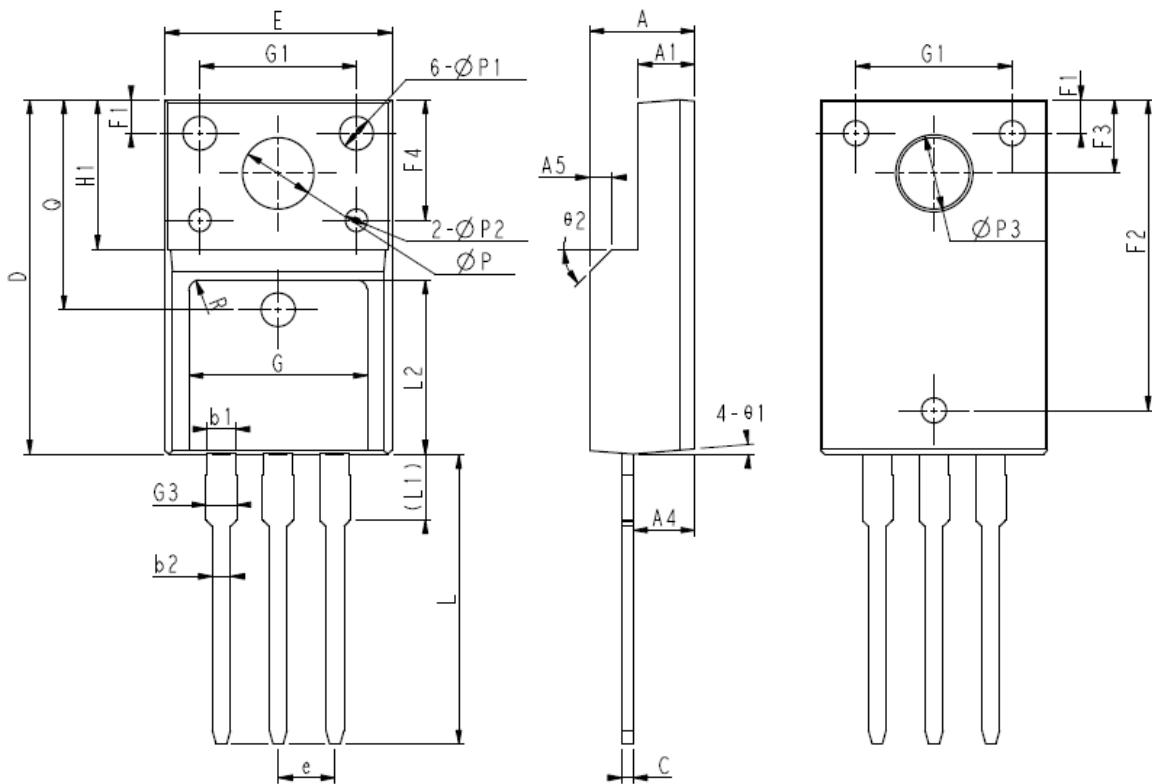
Diode recovery waveform



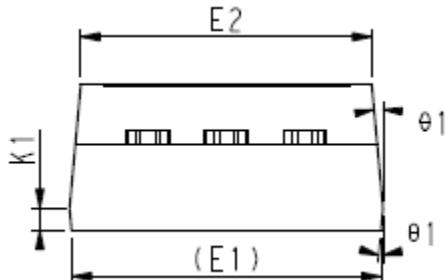


## Package Outline

TO-220 Full PAK



COMMON DIMENSIONS



SYMBOL	MM		
	MIN	NOM	MAX
E	10.00	10.16	10.32
E1	9.94	10.04	10.14
E2	9.36	9.46	9.56
A	4.50	4.70	4.90
A1	2.34	2.54	2.74
A4	2.66	2.76	2.86
A5		1.00REF	
c	0.45	0.50	0.60
D	15.67	15.87	16.07
Q		9.40REF	
H1		6.70REF	
e		2.54BSC	
ΦP		3.18REF	
L	12.78	12.98	13.18
L1	2.83	2.93	3.03
L2	7.70	7.80	7.90
ΦP1	1.40	1.50	1.60
ΦP2	0.95	1.00	1.05
ΦP3		3.45REF	
θ1	3°	5°	7°
θ2	-	45°	-
F1	1.00	1.50	2.00
F2	13.80	13.90	14.00
F3	3.20	3.30	3.40
F4	5.30	5.40	5.50
G	7.80	8.00	8.20
G1	6.90	7.00	7.10
G3	1.25	1.35	1.45
b1	1.23	1.28	1.38
b2	0.75	0.80	0.90
K1	0.65	0.70	0.75
R		0.50REF	



## DISCLAIMER

SUPER SEMICONDUCTOR reserves the right to make changes WITHOUT further notice to any products herein to improve reliability, function, or design.

For documents and material available from this datasheet, SUPER SEMICONDUCTOR does not warrant or assume any legal liability or responsibility for the accuracy, completeness of any product or technology disclosed hereunder.

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. With respect to any examples or hints given herein, any typical values stated herein and/or any information regarding the application of the device, SUPER SEMICONDUCTOR hereby disclaims any and all warranties and liabilities of any kind, including without limitation, warranties of non-infringement of intellectual property rights of any third party.

The products shown herein are not designed for use as critical components in medical, life-saving, or life-sustaining applications, whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness. Customers using or selling SUPER SEMICONDUCTOR products not expressly indicated for use in such applications do so entirely at their own risk and agree to fully indemnify SUPER SEMICONDUCTOR for any damages arising or resulting from such use or sale.

## INFORMATION

For further information on technology, delivery terms and conditions and prices, please contact SUPER SEMICONDUCTOR office or website ([www.supersemi.com.cn](http://www.supersemi.com.cn)).

# X-ON Electronics

Largest Supplier of Electrical and Electronic Components

***Click to view similar products for MOSFET category:***

***Click to view products by Super Semiconductor manufacturer:***

Other Similar products are found below :

[614233C](#) [648584F](#) [MCH3443-TL-E](#) [MCH6422-TL-E](#) [FDPF9N50NZ](#) [FW216A-TL-2W](#) [FW231A-TL-E](#) [APT5010JVR](#) [NTNS3A92PZT5G](#)  
[IRF100S201](#) [JANTX2N5237](#) [2SK2464-TL-E](#) [2SK3818-DL-E](#) [FCA20N60\\_F109](#) [FDZ595PZ](#) [STD6600NT4G](#) [FSS804-TL-E](#) [2SJ277-DL-E](#)  
[2SK1691-DL-E](#) [2SK2545\(Q,T\)](#) [D2294UK](#) [405094E](#) [423220D](#) [MCH6646-TL-E](#) [TPCC8103,L1Q\(CM](#) [367-8430-0972-503](#) [VN1206L](#)  
[424134F](#) [026935X](#) [051075F](#) [SBVS138LT1G](#) [614234A](#) [715780A](#) [NTNS3166NZT5G](#) [751625C](#) [873612G](#) [IRF7380TRHR](#)  
[IPS70R2K0CEAKMA1](#) [RJK60S3DPP-E0#T2](#) [RJK60S5DPK-M0#T0](#) [APT5010JVFR](#) [APT12031JFLL](#) [APT12040JVR](#) [DMN3404LQ-7](#)  
[NTE6400](#) [JANTX2N6796U](#) [JANTX2N6784U](#) [JANTXV2N5416U4](#) [SQM110N05-06L-GE3](#) [SIHF35N60E-GE3](#)