



SUPER-SEMI



SUPER-MOSFET

Super Junction Metal Oxide Semiconductor Field Effect Transistor

700V Super Junction Power Transistor
SS*70R750S

Rev. 1.2
May. 2018

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SSF70R750S/SST70R750S/SSU70R750S 700V N-Channel MOSFET

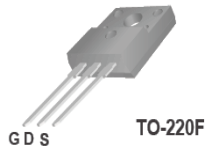
Description

SSMOS-FET is new generation of high voltage MOSFET family that is utilizing an advanced charge balance mechanism for outstanding low on-resistance and lower gate charge performance. This advanced technology has been tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate and higher avalanche energy. SJ-FET is suitable for various AC/DC power conversion in switching mode operation for higher efficiency.

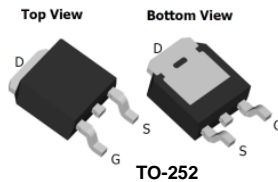
Features

- Multi-Epi process SJ-FET
- 750V @T_J = 150 °C
- Typ. R_{DS(on)} = 0.68Ω
- Ultra Low Gate Charge (typ. Q_g = 12.5nC)
- 100% avalanche tested

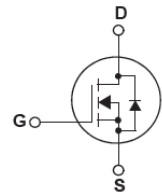
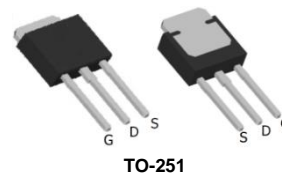
SSF70R750S



SST70R750S



SSU70R750S



Absolute Maximum Ratings

Symbol	Parameter	SST_U70R750S	SSF70R750S	Unit
V _{DSS}	Drain-Source Voltage	700		V
I _D	Drain Current -Continuous (TC = 25°C) -Continuous (TC = 100°C)	7* 4.5*		A
I _{DM}	Drain Current - Pulsed (Note 1)	20		A
V _{GSS}	Gate-Source voltage	±30		V
E _{AS}	Single Pulsed Avalanche Energy (Note 2)	86		mJ
I _{AR}	Avalanche Current (Note 1)	1.7		A
E _{AR}	Repetitive Avalanche Energy (Note 1)	0.2		mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)	15		V/ns
dV _{ds} /dt	Drain Source voltage slope (V _{ds} =480V)	50		V/ns
P _D	Power Dissipation (TC = 25°C)	63	28	W
T _J , T _{STG}	Operating and Storage Temperature Range	-55 to +150		°C
T _L	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds	300		°C

* Drain current limited by maximum junction temperature. Maximum duty cycle D=0.75.

Thermal Characteristics

Symbol	Parameter	SST_U70R750S	SSF70R750S	Unit
R _{θJC}	Thermal Resistance, Junction-to-Case	2.0	4.5	°C/W
R _{θCS}	Thermal Resistance, Case-to-Sink Typ.	0.5	-	°C/W
R _{θJA}	Thermal Resistance, Junction-to-Ambient	62	80	°C/W



Electrical Characteristics TC = 25°C unless otherwise noted

SSF70R750S/SST70R750S/SSU70R750S 700V N-Channel MOSFET

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250μA, T _J = 25°C	700	-	-	V
		V _{GS} = 0V, I _D = 250μA, T _J = 150°C	-	750	-	V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I _D = 250μA, Referenced to 25°C	-	0.6	-	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 700V, V _{GS} = 0V -T _J = 150°C	-	- 10	1 -	μA μA
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 30V, V _{DS} = 0V	-	-	100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -30V, V _{DS} = 0V	-	-	-100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250μA	2.5	3.5	4.5	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10V, I _D = 3.5A	-	0.68	0.75	Ω
g _{FS}	Forward Transconductance	V _{DS} = 40V, I _D = 7A	-	6	-	S
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} = 25V, V _{GS} = 0V, f = 1.0MHz	-	380	-	pF
C _{oss}	Output Capacitance		-	110	-	pF
C _{rss}	Reverse Transfer Capacitance		-	7	-	pF
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{DD} = 400V, I _D = 3.5A, R _G = 20Ω (Note 4)	-	13	-	ns
t _r	Turn-On Rise Time		-	10	-	ns
t _{d(off)}	Turn-Off Delay Time		-	85	-	ns
t _f	Turn-Off Fall Time		-	14	-	ns
Q _g	Total Gate Charge	V _{DS} = 480V, I _D = 3.5A, V _{GS} = 10V (Note 4)	-	12.5	-	nC
Q _{gs}	Gate-Source Charge		-	3.8	-	nC
Q _{gd}	Gate-Drain Charge		-	4.4	-	nC
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain-Source Diode Forward Current		-	-	7	A
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current		-	-	20	A
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0V, I _S = 3.5A	-	0.9	1.5	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0V, I _S = 3.5A, dI _F /dt = 100A/μs	-	190	-	ns
Q _{rr}	Reverse Recovery Charge		-	2.3	-	μC

NOTES:

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. I_{AS} = 1.7A, V_{DD} = 50V, Starting T_J = 25°C
3. I_{SD} ≤ I_D, di/dt ≤ 200A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C
4. Essentially Independent of Operating Temperature Typical Characteristics

Typical Performance Characteristics

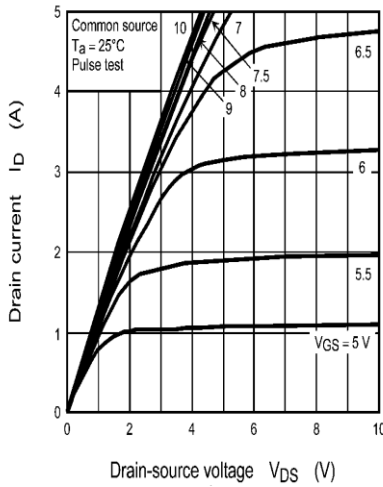


Figure 1: On-Region Characteristics@25°C

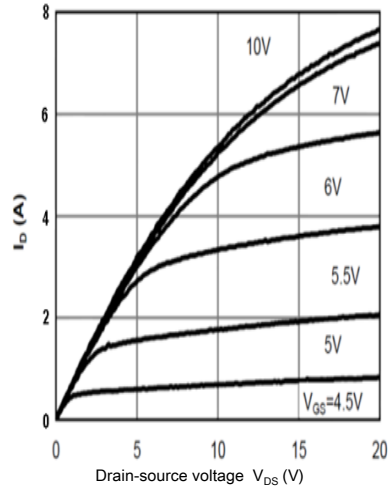


Figure 2: On-Region Characteristics@125°C

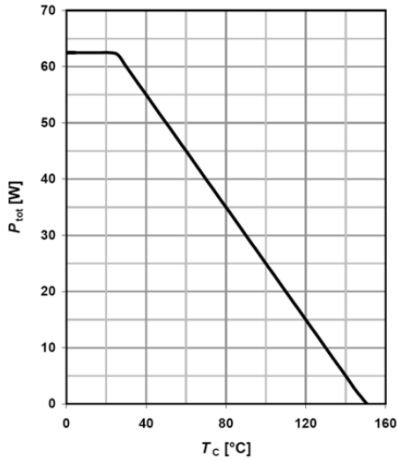


Figure 3: Power Dissipation
TO-252 , TO-251

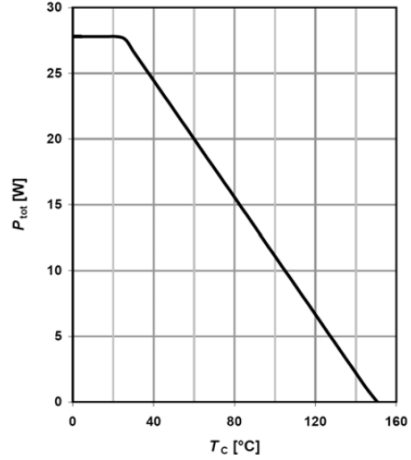


Figure 4 : Power dissipation
TO-220 FullPAK

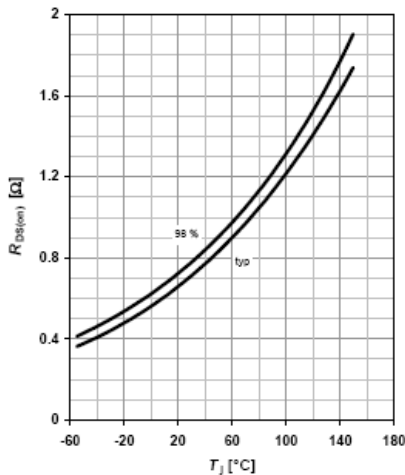


Figure 5: On-Resistance vs. Junction Temperature

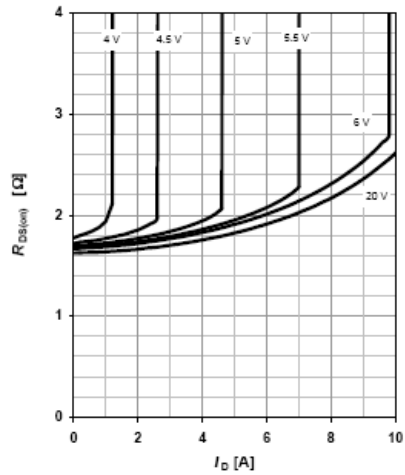


Figure 6: On-Resistance vs. Drain Current
T_J=150°C

Typical Performance Characteristics

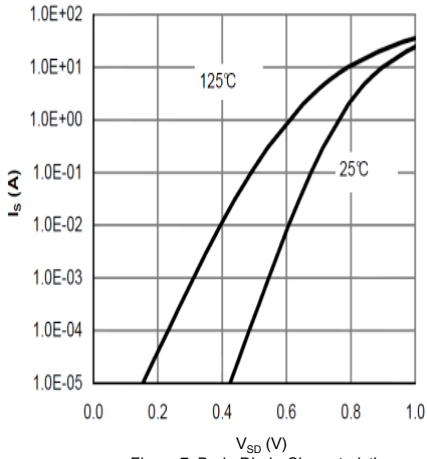


Figure 7: Body-Diode Characteristics

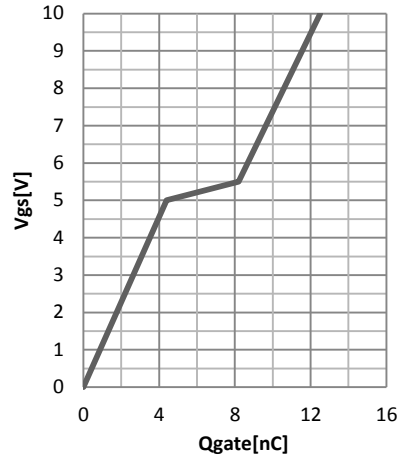


Figure 8: Gate-Charge Characteristics

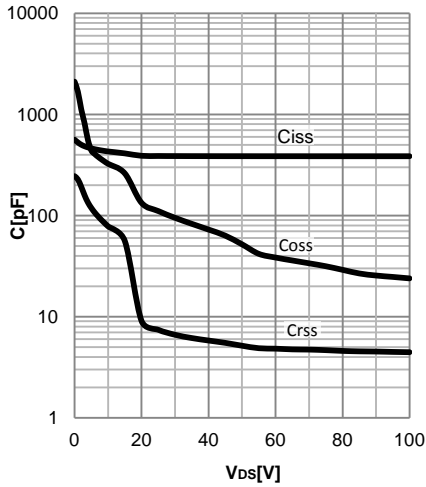


Figure 9: Capacitance Characteristics

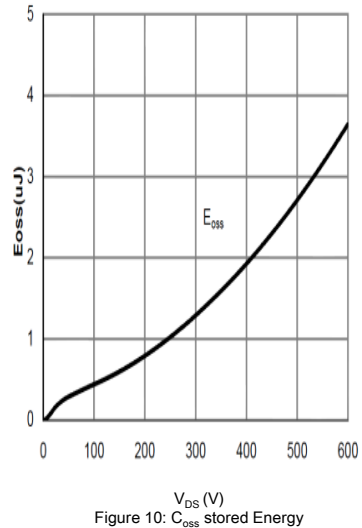


Figure 10: C_{oss} stored Energy

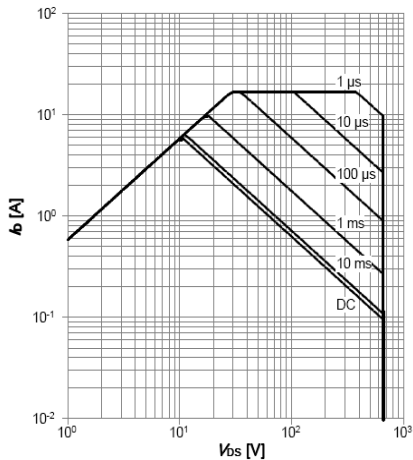


Figure 11: Maximum Forward Biased Safe Operating Area
Tc=25°C (TO-252, TO-251)

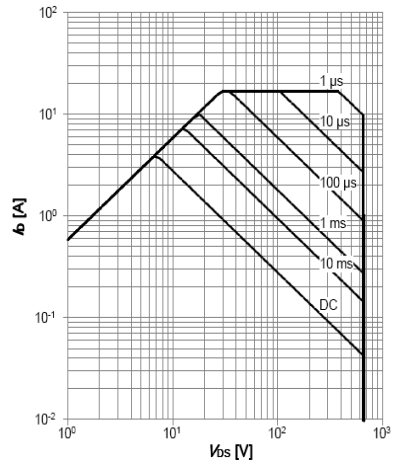


Figure 12: Maximum Forward Biased Safe Operating Area
Tc=25°C (TO-220 FullPAK)

Typical Performance Characteristics

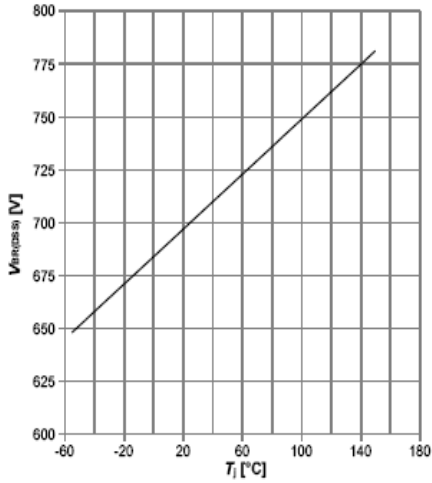


Figure 13: Break Down vs. Junction Temperature

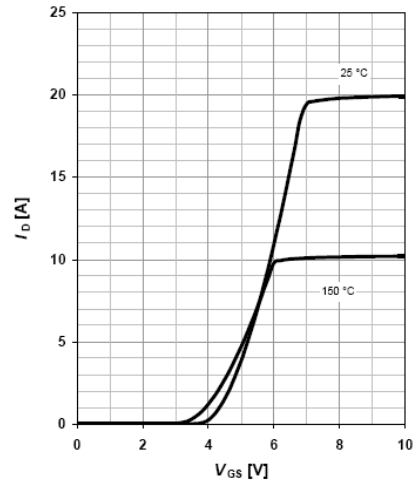


Figure 14: Typical transfer characteristics

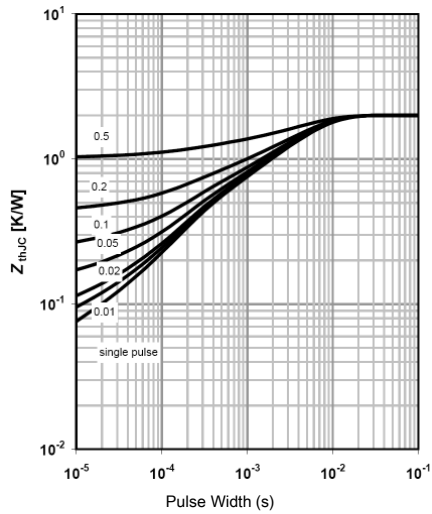


Figure 15: Maximum Transient Thermal Impedance TO-252 , TO-251

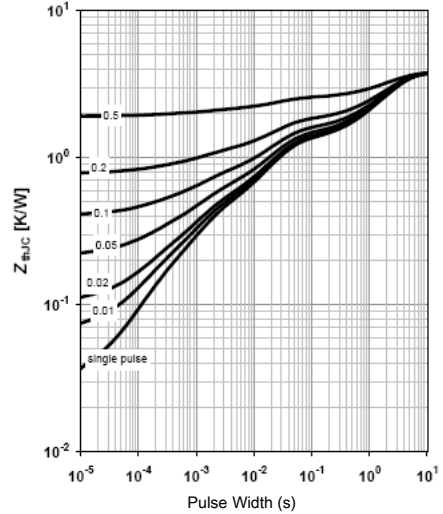


Figure 16: Maximum Transient Thermal Impedance TO-220 FULLPAK

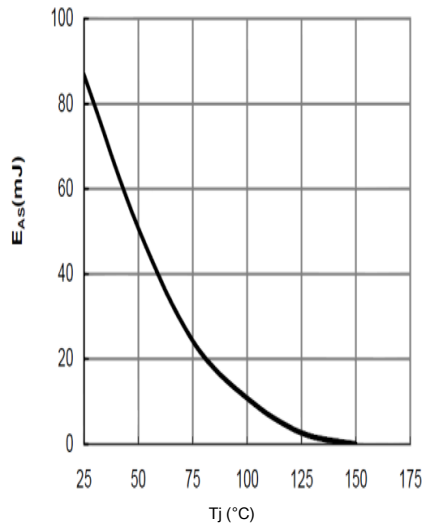
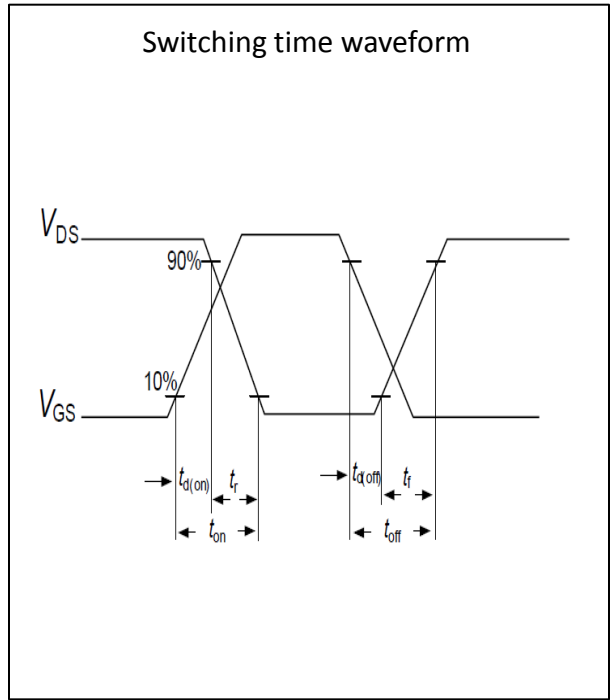
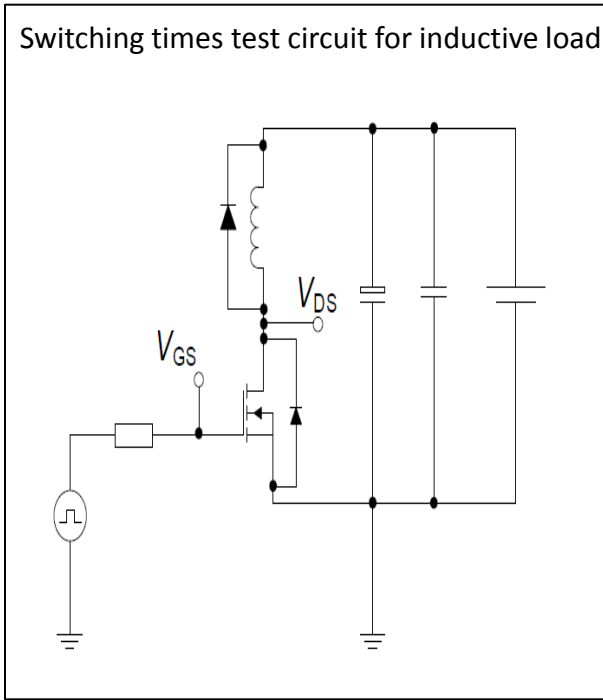
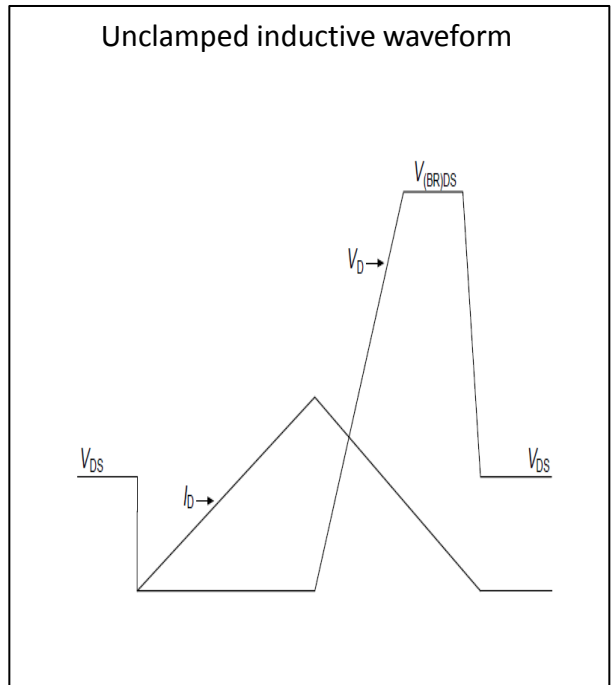
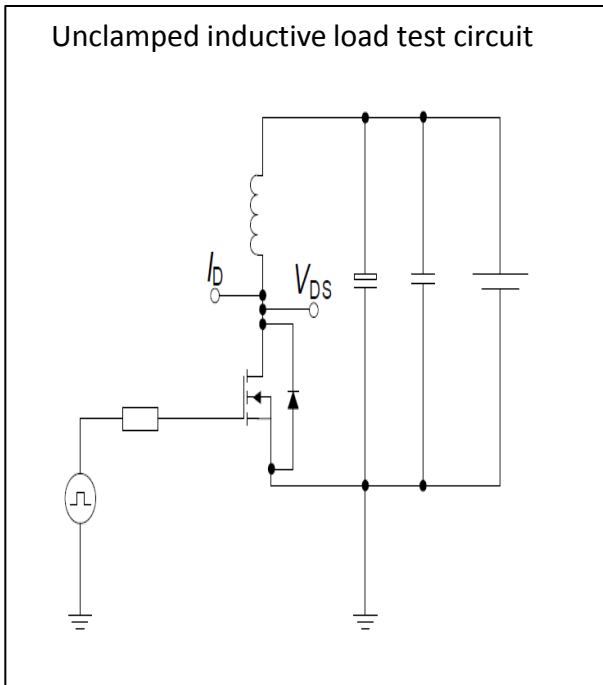


Figure 17: Avalanche energy

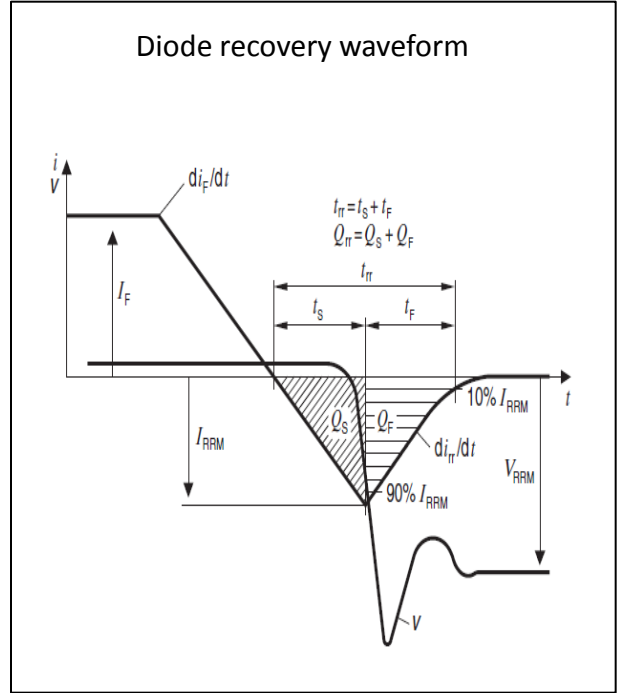
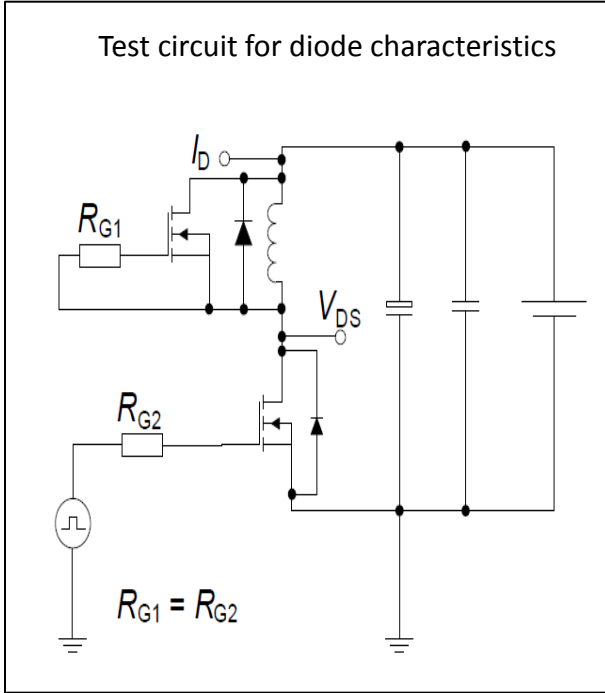
Switching times test circuit and waveform for inductive load

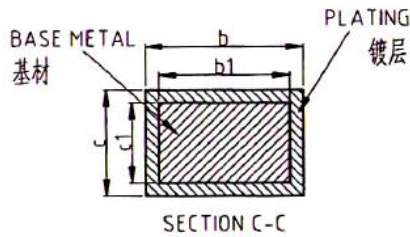
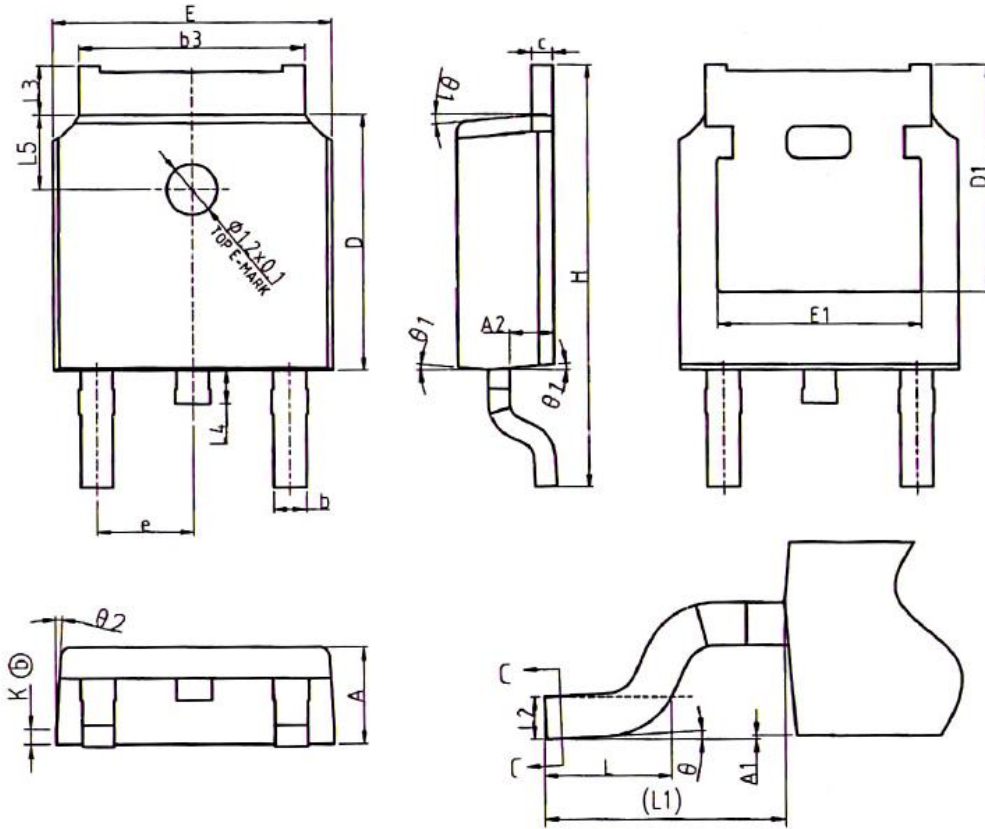


Unclamped inductive load test circuit and waveform



Test circuit and waveform for diode characteristics





COMMON DIMENSIONS

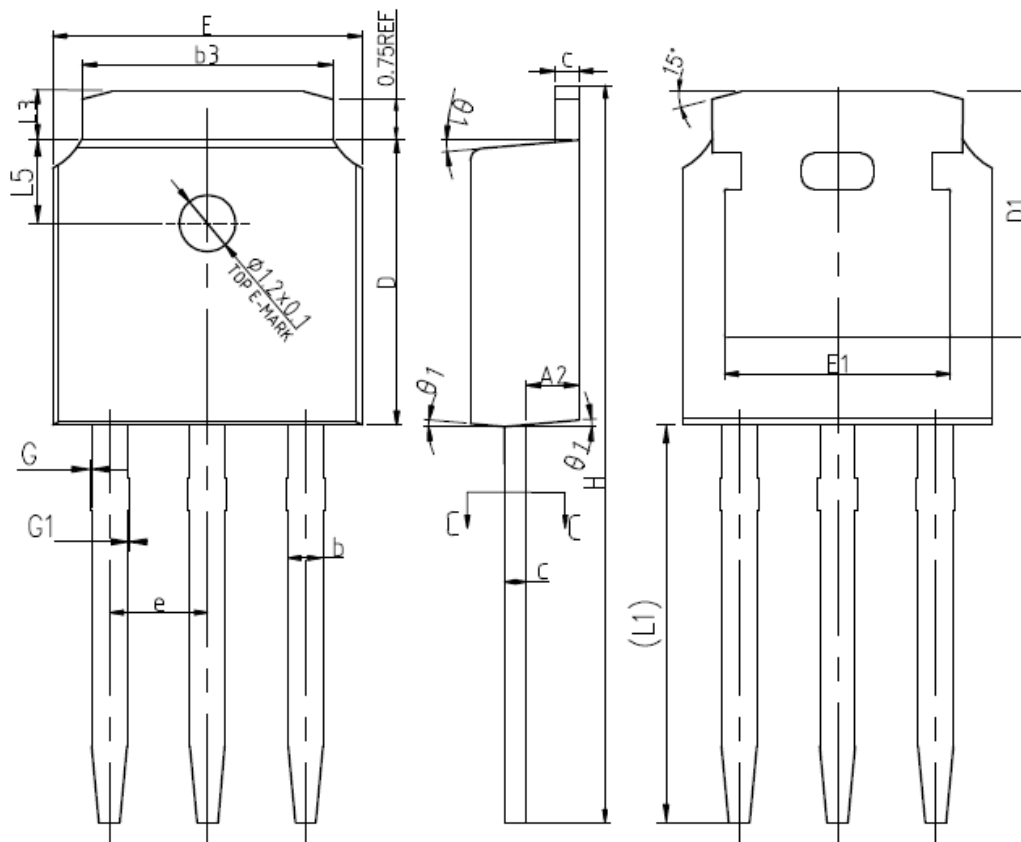
SYMBOL	MM		
	MIN	NOM	MAX
A	2.20	2.30	2.38
A1	0.00	-	0.10
A2	0.97	1.07	1.17
b	0.72	0.78	0.85
b1	0.71	0.76	0.81
b3	5.23	5.33	5.46
c	0.47	0.53	0.58
c1	0.46	0.51	0.56
D	6.00	6.10	6.20
D1	5.30REF		
E	6.50	6.60	6.70
E1	4.70	4.83	4.92
e	2.286BSC		
H	9.90	10.10	10.30
L	1.40	1.50	1.70
L1	2.90REF		
L2	0.51BSC		
L3	0.90	-	1.25
L4	0.60	0.80	1.00
L5	1.70	1.80	1.90
θ	0°	-	8°
θ_1	5°	7°	9°
θ_2	5°	7°	9°
K	0.40REF		



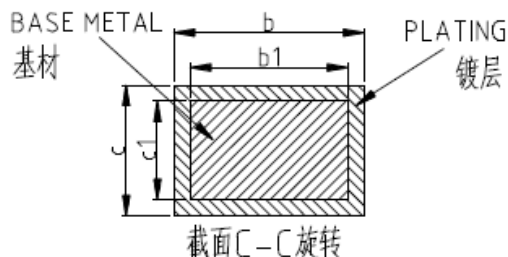
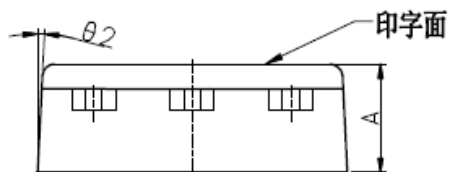
Package Outline

TO-251

SUPER

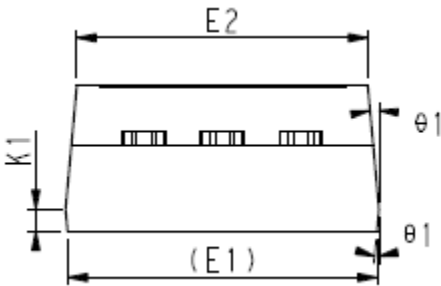
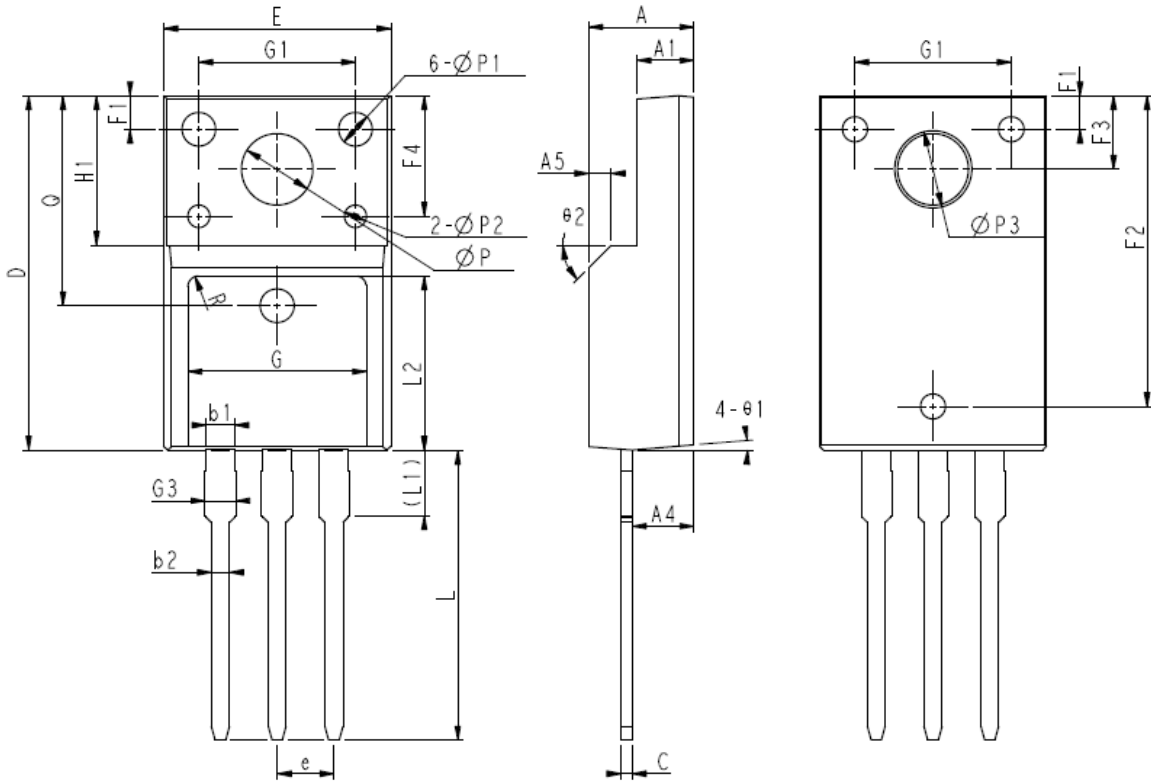


COMMON DIMENSIONS



SYMBOL	MM		
	MIN	NOM	MAX
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A2	0.97	1.07	1.17
b	0.72	0.78	0.85
b1	0.71	0.76	0.81
b3	5.23	5.33	5.46
c	0.47	0.53	0.58
c1	0.46	0.51	0.56
D	6.00	6.10	6.20
D1	5.30REF		
E	6.50	6.60	6.70
E1	4.70	4.83	4.92
e	2.286BSC		
H	16.10	16.40	16.60
L1	9.20	9.40	9.60
L3	0.90	1.02	1.25
L5	1.70	1.80	1.90
theta 1	5°	7°	9°
theta 2	5°	7°	9°

SSF70R750S/SST70R750S/SSU70R750S 700V N-Channel MOSFET



COMMON DIMENSIONS

SYMBOL	MM		
	MIN	NOM	MAX
E	10.00	10.16	10.32
E1	9.94	10.04	10.14
E2	9.36	9.46	9.56
A	4.50	4.70	4.90
A1	2.34	2.54	2.74
A4	2.66	2.76	2.86
A5	1.00REF		
c	0.45	0.50	0.60
D	15.67	15.87	16.07
Q	9.40REF		
H1	6.70REF		
e	2.54BSC		
ØP	3.18REF		
L	12.78	12.98	13.18
L1	2.83	2.93	3.03
L2	7.70	7.80	7.90
ØP1	1.40	1.50	1.60
ØP2	0.95	1.00	1.05
ØP3	3.45REF		
θ1	3°	5°	7°
θ2	-	45°	-
F1	1.00	1.50	2.00
F2	13.80	13.90	14.00
F3	3.20	3.30	3.40
F4	5.30	5.40	5.50
G	7.80	8.00	8.20
G1	6.90	7.00	7.10
G3	1.25	1.35	1.45
b1	1.23	1.28	1.38
b2	0.75	0.80	0.90
K1	0.65	0.70	0.75
R	0.50REF		



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