

CL1L, CL2L series, surface-mount thin film delay lines

This series permits ultra-precision timing adjustment from 20 to 200 picoseconds with accuracy of \pm picoseconds for ultrahigh-speed signal processing applications. Differential delay line:CL2L





GL1L/GL2L series, SOP(small outline package) thin film differential delay lines



These delay lines offer the same excellent frequency performance as SIP delay lines but come in a gullwinged surface mount package. The differential SMT is useful for PECL application, and contains two identical transmission lines matched for the time delay. Featuring a stripline shielded construction, these parts offer very low EMI/RFI, and are ideal for high frequency/tight tolerance timing and deskew applications.

S	PECIFICATIONS					
	13.66max			Туре	GL1L	GL2L
Mechanical			Electrical	Time Delay	0.1~5.0ns(0.1ns step)	0.1~3.0ns(0.1ns step)
						3.5~4.5ns(0.5ns step)
	•MS250SAE 6.06max			Time Delay Tolerance	±0.05ns	±0.05ns(0.1~2.9ns)
	8.35max					-0.5/+0.1ns(3.0ns)
						±0.1ns(3.5~4.5ns)
	1 + 8		Temp. coefficient of Td	0~150ppm/°C		
	1.27 ±0.15		Characteristic Impedance	$50{\pm}5\Omega$		
	Time De			Rise/fall time	200	os/ns
	Н			Rated Currrent	100)mA
		.0ns 2.37max L		Characteristic Impedance	−25~85°C	
Equivalent circuit	$\begin{array}{c} \text{GL1L} \\ 2 \\ \hline \\ 1 \\ 3 \\ 4 \\ 5 \\ 6 \\ 8 \\ 9 \\ 11 \\ 1 \\ 1 \\ 1 \\ 1 \\ 1 \\ 1 \\ 1 \\ $)	G	PART NUMBE	R 50 S -T* (-C)	Given to RoHS compliant product: Package(T1:100pcs, T5:500pcs) Circuit : S(GL1L),D(GL2L) Time Delay : (250=2.50ns) Lead Frame Height code Impedance : (5=50V) Part Code

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