Power Inductor

DFP252012TF-SERIES

	ECN HISTORY LIST								
REV	DATE	DESCRIPTION	APPROVED	CHECKED	DRAWN				
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Power Inductor

DFP252012TF-SERIES

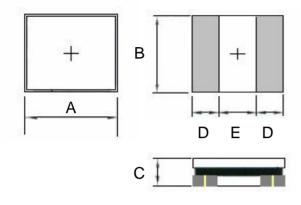
1. Features

- 1. This specification applies Low Profile Power Inductors.
- 2. 100% Lead(Pb) & Halogen-Free and RoHS compliant.

2. Dimension









Series	A(mm)	B(mm)	C(mm)	D(mm)	E(mm)
DFP252012TF	2.5 -0.1/+0.2	2.0 -0.05/+0.35	1.2Max	0.85 ref.	0.80 ref.

Units: mm

3. Part Numbering

DFP 252012 TF - **R47 M**

A: Series B: Dimension

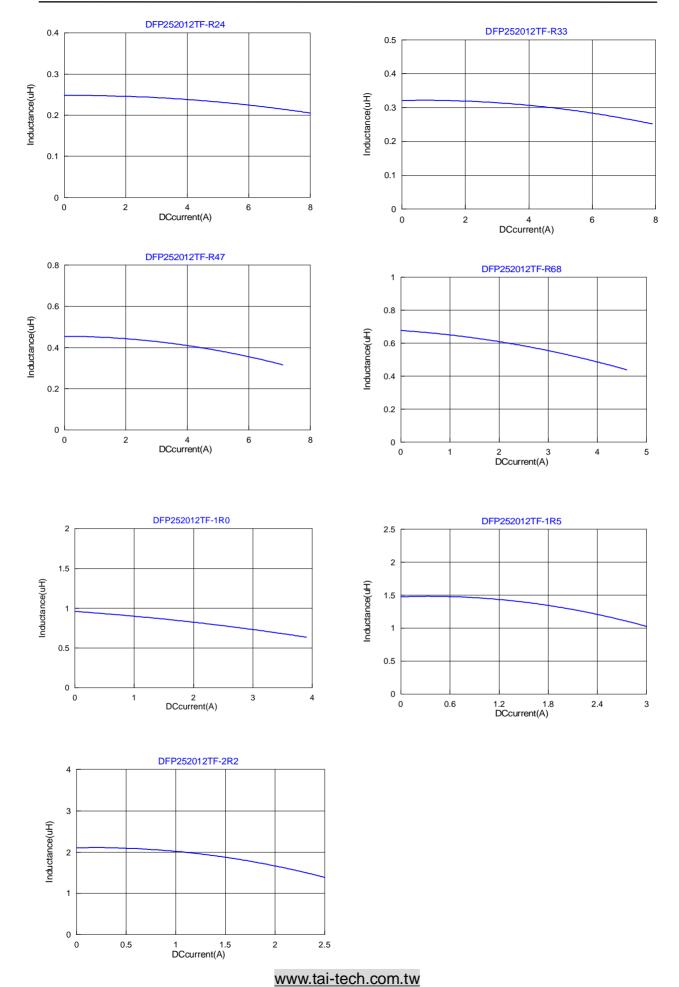
C: Lead Free Material
D: Inductance R47=0.47uH
E: Inductance Tolerance M=±20%

4. Specification

TAI-TECH Part Number	Inductance (uH)	Tolerance (%)	Test Frequency (Hz)	DCR (Ω) typ.	DCR (Ω) Max.	I sat (A) typ.	I sat (A) Max.	I rms (A) typ
DFP252012TF-R24M	<mark>0.24</mark>	<mark>±20%</mark>	<mark>0.1V/1M</mark>	<mark>0.020</mark>	<mark>0.024</mark>	<mark>8.00</mark>	<mark>6.50</mark>	<mark>4.70</mark>
DFP252012TF-R33M	0.33	±20%	0.1V/1M	0.027	0.032	5.70	4.60	4.50
DFP252012TF-R47M	0.47	±20%	0.1V/1M	0.027	0.032	5.50	4.50	4.40
DFP252012TF-R68M	0.68	±20%	0.1V/1M	0.036	0.043	4.50	3.80	3.60
DFP252012TF-1R0M	1.0	±20%	0.1V/1M	0.045	0.057	3.90	3.40	3.50
DFP252012TF-1R5M	1.5	±20%	0.1V/1M	0.080	0.096	3.00	2.60	2.50
DFP252012TF-2R2M	2.2	±20%	0.1V/1M	0.085	0.102	2.70	2.30	2.30

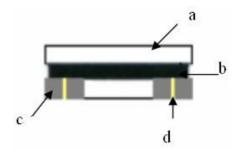
Note:

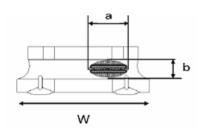
Irms: Based on temperature rise $(\triangle T : 40^{\circ}C.)$ Max



5. Material List

No.	Description	Specification
a.	Core	Ferrite Core
b.	Coating	Epoxy with magnetic powder
С	Termination	Tin(Pb Free)
d	Wire	Enameled Copper Wire





Exposed wire tolerance limit of coating resin part on product side.

Size of exposed wire occurring to coating resin is specified below.

- 1. Width direction (dimension a): Acceptable when $a \le w/2$ Nonconforming when a > w/2
- 2. Length direction (dimension b): Dimension b is not specified.
- 3. The total area of exposed wire occurring to each sides is not greater than 50% of coating resin area, and is acceptable.

6. Reliability and Test Condition

Item	Performance		Tes	t Condition	
Operating Temperature	-55~+125℃ (For products in unopened tape package, less than 40℃)				
Electrical Performance Te	est	•			
Inductance L	Agilent-4291, Agilent-4287		7		
	Refer to standard electrical characteristic list	Agilent-4291			
DC Resistance	Agilent-4338				
Rated Current	Base on temp. rise & △L/L0A≦30%.	Saturation DC Current (Isat) will cause L0 to drop approximately \triangle L(%).			
Temperature Rise Test	ΔT 40℃ Max	Heat Rated Current (Irms) will cause the coil temperature rise approximately △T(°C) without core loss. 1.Applied the allowed DC current. 2.Temperature measured by digital surface thermometer			
Mechanical Performance	Test				
Solder Heat Resistance	Appearance: No damage. Inductance: within±10% of initial value RDC: within±15% of initial value and shall not exceed the specification value	Temperature (°C) 260 ±5 (solder temp)	Time (s)	Temperature ramp/immersion and emersion rate	Number of heat cycles
		Depth: completely cover the termination			
Solderability Test	More than 95% of terminal electrode should be covered with solder.	Preheat: 150°C,60sec. ° Solder: Sn99.5%-Cu0. 5% ° Temperature: 245.5°C ° Flux for lead free: Rosin. 9.5% ° Dip time: 4±1sec ° Depth: completely cover the termination			

Item	Performance	Test Condition
Reliability Test		
Life Test		Preconditioning:Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DC lassification Reflow Profiles Temperature: 125±2°C (Bead) Temperature: 85±2°C (Inductor) Applied current: rated current Duration: 1000±12hrs
Thermal shock	Appearance: No damage. Inductance: within±10% of initial value RDC: within±15% of initial value and shall not exceed the specification value	Measured at room temperature after placing for 24±2 hrs Preconditioning:Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DC lassification Reflow Profiles Step1: -40±2°C 30±5min Step2: 25±2°C ≤0.5min Step3: 105±2°C 30±5min Number of cycles: 500 Measured at room femprature after placing for 24±2 hrs
Humidity Resistance Test		Preconditioning:Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles Humidity: 85±2% R.H, Temperature: 85°C±2°C Duration: 1000hrs Min. with 100% rated current Measured at room temperature after placing for 24±2 hrs
Vibration Test		Preconditioning:Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DC lassification Reflow Profiles Oscillation Frequency: 10~2K~10Hz for 20 minutes Equipment: Vibration checker Total Amplitude:1.52mm±10% Testing Time: 12 hours(20 minutes, 12 cycles each of 3 orientations) •

7. Soldering and Mounting

7-1. Soldering

Mildly activated rosin fluxes are preferred. TAI-TECH terminations are suitable for all wave and re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

7-1.1 Solder re-flow:

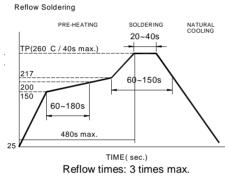
Recommended temperature profiles for re-flow soldering in Figure 1.

7-1.2 Soldering Iron(Figure 2):

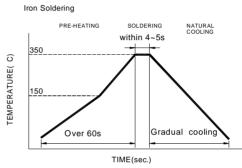
Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.

- Preheat circuit and products to 150℃
- Never contact the ceramic with the iron tip
- Use a 20 watt soldering iron with tip diameter of 1.0mm

- 355°C tip temperature (max)
- 1.0mm tip diameter (max)
- Limit soldering time to 4~5 sec.



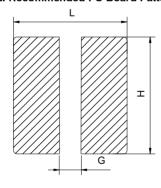
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Iron Soldering times: 1 times max.

Fig.2

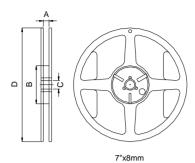
7-2. Recommended PC Board Pattern



L(mm)	G(mm)	H(mm)
2.9	0.8	2.4

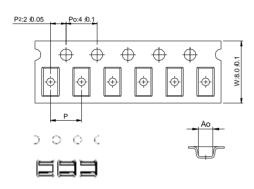
8. Packaging Information

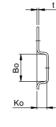
8-1. Reel Dimension



Туре	A(mm)	B(mm)	C(mm)	D(mm)
7"x8mm	8.4±1.0	50 min.	13±0.8	178±2

8-2. Tape Dimension / 8mm





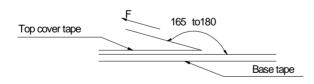
Series	Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)
DFP	252012	2.85±0.1	2.45±0.1	1.40±0.1	4.0±0.1	0.23±0.05

Bottom View

8-3. Packaging Quantity

Chip size	252012
Chip / Reel	2000

8-4. Tearing Off Force



The force for tearing off cover tape is 15 to 80 grams in the arrow direction under the following conditions.

Room Temp.	Room Humidity	Room atm	Tearing Speed
(℃)	(%)	(hPa)	mm/min
5~35	45~85	860~1060	300

Application Notice

· Storage Conditions

To maintain the solderability of terminal electrodes:

- ${\tt 1.\,TAI\text{-}TECH\ products\ meet\ IPC/JEDEC\ J\text{-}STD\text{-}020D\ standard\text{-}MSL,\ level\ 1.}$
- 3. Recommended products should be used within 12 months form the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
- 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

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