



Specification for Approval

Date: 2019/1/10







<u>Customer</u>	天誠

TAI-TECH P/N: FCM1005KF-800T05

	CUSTOMER P/N	\ :		
	DESCRIPTION:			
	QUANTITY:		pcs	
REM	MARK:			
	(Customer Approval Fe	eedback	

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TAI-TECH KBM01-190700755 P2.

Ferrite Chip Bead(Lead Free)

FCM1005KF-800T05

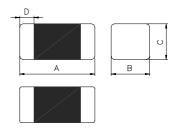
Certificate

Green Partner

1.Features

- 1. Monolithic inorganic material construction.
- 2. Closed magnetic circuit avoids crosstalk.
- 3. S.M.T. type.
- 4. Suitable for reflow soldering.
- 5. Shapes and dimensions follow E.I.A. spec.
- 6. Available in various sizes.
- 7. Excellent solderability and heat resistance.
- 8. High reliability.
- 9. 100% Lead(Pb) & Halogen-Free and RoHS compliant.
- 10. Operating Temperature: -55~+125°C (Including self-temperature rise)

2.Dimensions



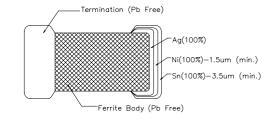
Chip Size				
Α	1.00±0.10			
В	0.50±0.10			
С	0.50±0.10			
D	0.25±0.10			

Units: mm

3.Part Numbering



E: Packaging T=Taping and Reel, B=Bulk(Bags) F: Rated Current 05=500mA

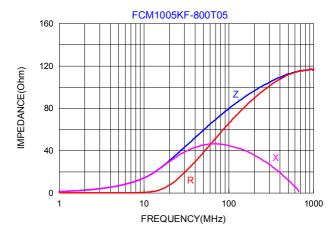


4.Specification

Tai-Tech Part Number	Impedance (Ω)	Test Frequency (Hz)	DC Resistance (Ω) max.	Rated Current (mA) max.
FCM1005KF-800T05	80±25%	60mV/100M	0.25	500

- Rated current: based on temperature rise test
- In compliance with EIA 595

■ Impedance-Frequency Characteristics



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TAI-TECH KBM01-190700755 P3.

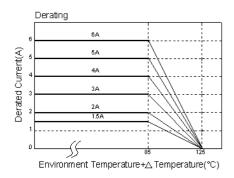
5. Reliability and Test Condition

Item	Performance	Test Condition			
Series No.	FCB FCM HCB GHB FCA				
Operating Temperature	-55~+125 $^{\circ}\mathbb{C}$ (Including self-temperature rise)				
Transportation Storage Temperature	-55~+125°ℂ (on board)	For long storage conditions, please see the Application Notice			
Impedance (Z)	Refer to standard electrical characteristics list	Agilent4291 Agilent E4991 Agilent4287 Agilent16192			
DC Resistance	Refer to standard electrical characteristics list	Agilent 4338			
Rated Current		DC Power Supply Over Rated Current requirements, there will be some risk			
Temperature Rise Test	Rated Current < 1A ΔT 20 ℃Max Rated Current ≧ 1A ΔT 40 ℃Max	Applied the allowed DC current. Temperature measured by digital surface thermometer.			
Life test	Appearance: no damage. Impedance: within±15% of initial value.	Preconditioning: Run through IR reflow for 2 times, (IPC/JEDEC J-STD-020D Classification Reflow Profiles) Temperature: 125±2°C Applied current: rated current. Duration: 1000±12hrs. Measured at room temperature after placing for 24±2 hrs. Preconditioning: Run through IR reflow for 2			
Load Humidity	Inductance: within±10%of initial value. Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	times. (IPC/JEDEC J-STD-020D Classification Reflow Profiles) Humidity: 85±2%R.H. Temperature: 85±2°C. Duration: 1000hrs Min. with 100% rated current. Measured at room temperature after placing for 24±2 hrs.			
Thermal shock	Appearance: no damage. Impedance: within±15%of initial value. Inductance: within±10%of initial value. Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times. (IPC/JEDEC J-STD-020D Classification Reflow Profiles) Condition for 1 cycle Step1: $-55\pm2^{\circ}$ C 30 ± 5 min. Step2: $25\pm2^{\circ}$ C 30 ± 5 min. Step3: $+125\pm2^{\circ}$ C 30 ± 5 min. Number of cycles: 500 Measured at room temperature after placing for 24 ± 2 hrs.			
Vibration	Appearance: No damage. Impedance: within±15% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020D Classification Reflow Profiles) Oscillation Frequency: $10\text{Hz} \sim 2\text{KHz} \sim 10\text{Hz}$ for 20 minutes Equipment: Vibration checker Total Amplitude:10g Testing Time: 12 hours(20 minutes, 12 cycles each of 3 orientations) \circ			
Bending	Appearance: No damage. Impedance: within±10% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within±15% of initial value and shall not exceed the specification value	Shall be mounted on a FR4 substrate of the following dimensions: >=0805inch(2012mm):40x100x1.2mm <0805inch(2012mm):40x100x0.8mm Bending depth: >=0805inch(2012mm):1.2mm <0805inch(2012mm):0.8mm Duration of 10 sec for a min.			
		Test condition:			
Shock	Appearance: No damage. Impedance: within±10% of initial value Inductance: within±10% of initial value	Type Value duration (g's) (D) (ms) Wave form change (Vi)ft/sec			
	Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	SMD 50 11 Half-sine 11.3			
	Takes and and and another the position of the property of the position of the	Lead 50 11 Half-sine 11.3			
Solderability	More than 95% of the terminal electrode should be covered with solder.	Preheat: 150°C,60sec. Solder: Sn96.5%-Ag3%-Cu0.5% Solder temperature: 245±5°C Flux for lead free: Rosin. 9.5% Depth: completely cover the termination. Dip time: 4±1sec.			

Item	Performance	Test Condition			
		Number of heat	cycles: 1		
Resistance to Soldering	Appearance: No damage. Impedance: within±15% of initial value			Time (s)	Temperature ramp/immersion and emersion rate
Heat	Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not	260 ±5 (solder temp)	10 ±1	25mm/s ±6 mm/s	
			Depth: complete	ely cover tl	ne termination
Terminal strength	Appearance: No damage. Impedance: within±15% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	racitive 0,5 mm DUT Wide wide which ness substants press tool shear force	Preconditioning: Run through IR reflow for times.(IPC/JEDEC J-STD-020D Classification Reflow Profiles) Component mounted on a PCB apply a force >0805inch(2012mm):1kg <=0805inch(2012mm):0.5kg to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to shoot the component being tested.		

**Derating Curve

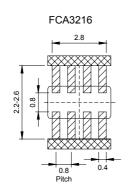
For the ferrite chip bead which withstanding current over 1.5A, as the operating temperature over $85^{\circ}\mathbb{C}$, the derating current information is necessary to consider with. For the detail derating of current, please refer to the Derated Current vs. Operating Temperature curve.



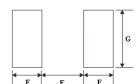
6. Soldering and Mounting

6-1. Recommended PC Board Pattern

	Chip Size							Land Patterns For Reflow Soldering		
Series	eries Type A(mm) B(mm) C(mm) D(mm)						F(mm)	G(mm)		
	0603	0.6±0.03	0.30±0.03	0.30±0.03	0.15±0.05	0.35	0.30	0.40		
FCB	<mark>1005</mark>	1.0±0.10	<mark>0.50±0.10</mark>	0.50±0.10	<mark>0.25±0.10</mark>	<mark>0.50</mark>	<mark>0.40</mark>	<mark>0.60</mark>		
FCM	1608	1.6±0.15	0.80±0.15	0.80±0.15	0.30±0.20	0.80	0.85	0.95		
HCB	2012	2.0±0.20	1.25±0.20	0.85±0.20	0.50±0.30	1.05	1.00	1.45		
GHB	2012	2.0±0.20	1.25±0.20	1.25±0.20	0.50±0.30	1.05				
FCI FHI	3216	3.2±0.20	1.60±0.20	1.10±0.20	0.50±0.30	1.05	2.20	1.80		
FCH	3225	3.2±0.20	2.50±0.20	1.30±0.20	0.50±0.30	1.05	2.20	2.70		
HCI	4516	4.5±0.20	1.60±0.20	1.60±0.20	0.50±0.30	1.05	3.30	1.80		
	4532	4.5±0.20	3.20±0.20	1.50±0.20	0.50±0.30	1.05	3.30	3.40		



Land
Solder Resist



PC board should be designed so that products can prevent damage from mechanical stress when warping the board.

6-2. Soldering

Mildly activated rosin fluxes are preferred. The terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

Note.

If wave soldering is used ,there will be some risk.

Re-flow soldering temperatures below 240 degrees, there will be non-wetting risk

TAI-TECH KBM01-190700755 P5.

6-2.1 Lead Free Solder re-flow:

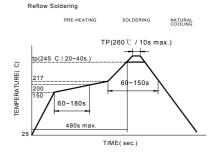
Recommended temperature profiles for lead free re-flow soldering in Figure 1. (Refered to J-STD-020C)

6-2.2 Soldering Iron:

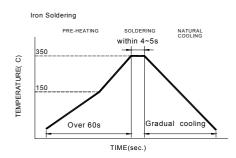
Products attachment with a soldering iron is discouraged due to the inherent process control limitations. If a soldering iron must be employed the following precautions are recommended. for Iron Soldering in Figure 2.

- Preheat circuit and products to 150℃
- Never contact the ceramic with the iron tip
- Use a 20 watt soldering iron with tip diameter of 1.0mm

- 350°C tip temperature (max) 1.0mm tip diameter
- 1.0mm tip diameter (max) Limit soldering time to 4~5sec.



Reflow times: 3 times max Fig.1



Iron Soldering times: 1 times max

6-2.3 Solder Volume:

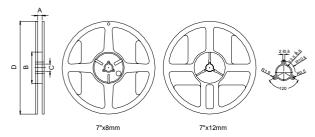
Accordingly increasing the solder volume, the mechanical stress to product is also increased. Exceeding solder volume may cause the failure of mechanical or electrical performance. Solder shall be used not to be exceed as shown in right side:

Minimum fillet height = soldering thickness + 25% product height



7. Packaging Information

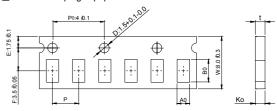
7-1. Reel Dimension



Туре	A(mm)	B(mm)	C(mm)	D(mm)
<mark>7"x8mm</mark>	9.0±0.5	60±2	13.5±0.5	<mark>178±2</mark>
7"x12mm	13.5±0.5	60±2	13.5±0.5	178±2

7-2.1 Tape Dimension / 8mm

■Material of taping is paper



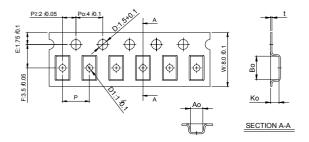
-	P22 0.1 P04 0.1 0.18 0.1 0.1 0.1 0.1 0.1 0.1 0.1 0.1 0.1 0.1	- t -
E:3.5.00.1		
置	P A0	Ko

Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)
060303	0.70±0.06	0.40±0.06	0.45max	2.0±0.05	0.45max
<mark>100505</mark>	1.12±0.03	<mark>0.62±0.03</mark>	<mark>0.60±0.03</mark>	<mark>2.0±0.05</mark>	<mark>0.60±0.03</mark>

Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)
160808	1.80±0.05	0.96+0.05/-0.03	0.95±0.05	4.0±0.10	0.95±0.05
201209	2.10±0.05	1.30±0.05	0.95±0.05	4.0±0.10	0.95±0.05

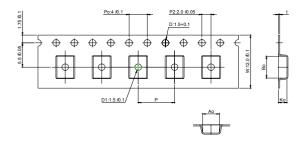
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■Material of taping is plastic



Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)	D1(mm)
201212	2.10±0.10	1.28±0.10	1.28±0.10	4.0±0.10	0.22±0.05	1.0±0.10
321611	3.35±0.10	1.75±0.10	1.25±0.10	4.0±0.10	0.23±0.05	1.0±0.10
322513	3.42±0.10	2.77±0.10	1.55±0.10	4.0±0.10	0.22±0.05	1.0±0.10
321609	3.40±0.10	1.77±0.10	1.04±0.10	4.0±0.10	0.22±0.05	1.0±0.10

7-2.2 Tape Dimension / 12mm

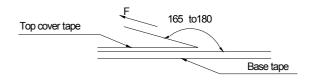


Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)	D1(mm)
451616	4.70±0.10	1.75±0.10	1.75±0.10	4.0±0.10	0.24±0.05	1.5±0.10
453215	4.70±0.10	3.45±0.10	1.60±0.10	8.0±0.10	0.24±0.05	1.5±0.10

7-3. Packaging Quantity

Chip Size	453215	451616	322513	321611	321609	201212	201209	160808	<mark>100505</mark>	060303
Chip / Reel	1000	2000	2500	3000	3000	2000	4000	4000	<mark>10000</mark>	15000
Inner box	4000	8000	12500	15000	15000	10000	20000	20000	<mark>50000</mark>	75000
Middle box	20000	40000	62500	75000	75000	50000	100000	100000	250000	375000
Carton	40000	80000	125000	150000	150000	100000	200000	200000	<mark>500000</mark>	750000

7-4. Tearing Off Force



The force for tearing off cover tape is 15 to 60 grams in the arrow direction under the following conditions.

Room Temp. Room Humidity		Room atm	Tearing Speed	
(℃)	(%)	(hPa)	mm/min	
5~35	45~85	860~1060	300	

Application Notice

Storage Conditions(component level)

To maintain the solder ability of terminal electrodes:

- 1. TAI-TECH products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
- 2. Temperature and humidity conditions: Less than 40 $^{\circ}$ C and 60% RH.
- 3. Recommended products should be used within 12 months from the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
- 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

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CZB1JGTTD202P MAF0603GWY551AT000 MAF1005GWZ102AT000 BLM18HE152SH1D 2944778302 BLM02PX600SN1D SMB2.5-1

EMI1206R-600 BLM02KX180SN1D BLM02BC100SN1D BLM02KX100SN1D BLM02BB101SN1D BLM02BC220SN1D

BLE32PN260SH1L BLE32PN260SN1L BLE32PN260SZ1L 74275013 7427503 BLM18HE601SH1D BLM15BD152SN1D

BLM15BD152SZ1D BLE18PS080SZ1D BLM21PG221BH1D WLBD1005HCU330TL BLM21AG471BH1D BLE18PS080BH1D

BLM21AG331BH1D BLM21PG300BH1D BLM21PG600BH1D BLM03HB401SZ1D BLM03HB401SN1D