High Frequency Chip Inductor (Lead Free)

HCI1005LF-2N7S-MS8

ECN HISTORY LIST								
REV	DATE	DESCRIPTION	APPROVED	CHECKED	DRAWN			
1.0	17/06/27	初版發行	楊祥忠	詹偉特	張嘉玲			
備			,					
註								

# **High Frequency Chip Inductor (Lead Free)**

HCI1005LF-2N7S-MS8

### 1.Features

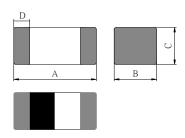
- 1. Monolithic inorganic material construction.
- 2. Closed magnetic circuit avoids crosstalk.
- 3. S.M.T. type.
- 4. Suitable for reflow soldering.
- 5. Shapes and dimensions follow E.I.A. spec.
- 6. Available in various sizes.
- 7. Excellent solder ability and heat resistance.
- 8. High SRF up to 6GHz and above.
- 9. 100% Lead(Pb) & Halogen-Free and RoHS compliant.







### 2. Dimensions



Chip Size					
Α	1.00±0.15				
В	0.50±0.15				
С	0.50±0.15				
D	0.25±0.10				

Units: mm

## 3. Part Numbering



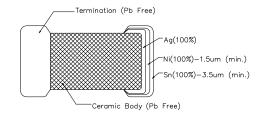
A: Series

B: Dimension L x W

C: Category Code

D: Material Lead Free Material
E: Inductance 2N7=2.7 nH
F: Inductance Tolerance S=±0.3

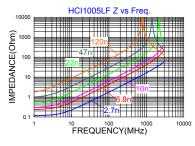
G: marking

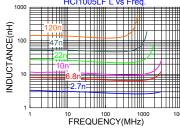


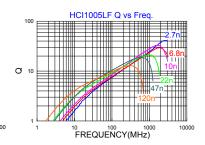
## 4.Specification

Tai-Tech	Inductance	Test Frequency	Q	Rated Current	DCR (Ω)	SRF (MHz) min.	
Part Number	(nH)	(Hz)	min.	(mA) max	max.		
HCI1005LF-2N7S-MS8	2.7±0.3	100M / 50mV	7	300	0.20	6000	

- Rated current: based on temperature rise test
- In compliance with EIA 595







**TAI-TECH** TBM01-170800332 P3.

# 5. Reliability and Test Condition

Item	Performance	Test Condition						
Series No.	HCI							
Operating Temperature	-40~+105°C (Including self-temperature rise)							
Transportation Storage Temperature	-40~+105°C (on board)	For long			ons, please	see the		
Inductance (Ls)		Agilent42						
Q Factor	Refer to standard electrical characteristics list	Agilent42						
DC Resistance								
Rated Current		DC Pow Over Ra some ris	ted Curi		ements, the	re will be		
Temperature Rise Test	Rated Current < 1A ∆T 20°CMax Rated Current ≧ 1A ∆T 40°CMax	2. Temp			current. by digital s	urface		
Life test	Appearance: no damage. Impedance: within±15%of initial value.	Preconditioning: Run through IR reflow for 2 times.( IPC/JEDEC J-STD-020D Classification Reflow Profiles) Temperature: 105±2°C Applied current: rated current. Duration: 1000±12hrs. Measured at room temperature after placing for 24±2 hrs.						
Load Humidity	Preconditioning: Run through IR reflow for 2 times. (IPC/JEDEC J-STD-020D Classification Reflow Profiles) Humidity: 85±2%R.H. Temperature: 85±2°C. Duration: 1000hrs Min. with 100% rated current. Measured at room temperature after placing for 24±2 hrs.							
Thermal shock	Appearance: no damage.  Impedance: within±15% of initial value. Inductance: within±10% of initial value. Q : Shall not exceed the specification value. RDC : within±15% of initial value and shall not exceed the specification value  times.( IPC/A Reflow Profi Condition for Step1: -40±2 Step2: 25±2 Step3: +105 Number of c					$\begin{array}{ll} \text{n for 1 cycle} \\ 0.12\% & 30\pm 5 \text{ min.} \\ 5\pm 2\% & \leq 0.5 \text{min.} \\ 105\pm 2\% & 30\pm 5 \text{min.} \\ \text{of cycles: } 500 \\ \text{d at room temperature after placing} \end{array}$		
Vibration	Appearance : No damage.  Impedance : within±15% of initial value Inductance : within±10% of initial value Q : Shall not exceed the specification value.  RDC : within±15% of initial value and shall not exceed the specification value  Total Amplitude:1.52mm±10% Testing Time : 12 hours(20 minute each of 3 orientations) •					ssification		
Bending	Appearance: No damage. Impedance: within±10% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within±15% of initial value and shall not exceed the specification value	Shall be mounted on a FR4 substrate of the following dimensions: >=0805inch(2012mm):40x100x1.2mm <0805inch(2012mm):40x100x0.8mm Bending depth: >=0805inch(2012mm):1.2mm <0805inch(2012mm):0.8mm Duration of 10 sec for a min.						
	Appearance: No damage. Impedance: within±10% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within±15% of initial value and shall not exceed the specification value		ndition	1:				
Shock			Peak Value (g's)	Normal duration (D) (ms)	Wave form	Velocity change (Vi)ft/sec		
			50 50	11	Half-sine Half-sine	11.3 11.3		
Insulation Resistance	IR>1GΩ	Chip Ind	uctor O			1		

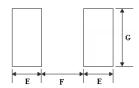
TAI-TECH TBM01-170800332 P4.

Item	Performance	Test Condition			
Solderability	More than 95% of the terminal electrode should be covered with solder.	Preheat: 150°C,60sec. Solder: Sn96.5%-Ag3%-Cu0.5% Solder temperature: 245±5°C Flux for lead free: Rosin. 9.5% Depth: completely cover the termination. Dip time: 4±1sec.			
		Number of heat cycles: 1			
Resistance to Soldering	Appearance : No damage. Impedance : within±15% of initial value	Temperature (°C) Time (s) Temperature ramp/immersion and emersion rate			
Heat	Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	260 ±5 (solder temp) 10 ±1 25mm/s ±6 mm/s			
		Depth: completely cover the termination			
Terminal strength	Appearance: No damage. Impedance: within±15% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not  exceed the specification value	Preconditioning: Run through IR reflow for 2 times. (IPC/JEDEC J-STD-020D Classification Reflow Profiles) Component mounted on a PCB apply a force >0805inch(2012mm):1kg to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to shool the component being tested.			

# 6. Soldering and Mounting

#### 6-1. Recommended PC Board Pattern

Chip Size							Land Patterns For Reflow Soldering			
Series	Type	A(mm)	B(mm)	C(mm)	D(mm)	E(mm)	F(mm)	G(mm)		
HCI	1005	1.00±0.15	0.50±0.15	0.50±0.15	0.25±0.10	0.50	0.40	0.60		



PC board should be designed so that products can prevent damage from mechanical stress when warping the board.

#### 6-2. Soldering

Mildly activated rosin fluxes are preferred. The terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

If wave soldering is used ,there will be some risk.

Re-flow soldering temperatures below 240 degrees, there will be non-wetting risk

#### 6-2.1 Lead Free Solder re-flow:

Recommended temperature profiles for lead free re-flow soldering in Figure 1. (Refered to J-STD-020C)

# **X-ON Electronics**

Largest Supplier of Electrical and Electronic Components

Click to view similar products for Fixed Inductors category:

Click to view products by TAITEC manufacturer:

Other Similar products are found below:

CR32NP-151KC CR32NP-180KC CR32NP-181KC CR32NP-1R5MC CR32NP-390KC CR32NP-3R9MC CR32NP-680KC CR32NP820KC CR32NP-8R2MC CR43NP-390KC CR43NP-560KC CR43NP-680KC CR54NP-181KC CR54NP-470LC CR54NP-820KC
CR54NP-8R5MC 70F224AI MGDQ4-00004-P MHL1ECTTP18NJ MHQ1005P10NJ MHQ1005P1N0S MHQ1005P2N4S MHQ1005P3N6S
MHQ1005P5N1S MHQ1005P8N2J PE-51506NL PE-53601NL PE-53602NL PE-53630NL PE-53824SNLT PE-92100NL PG0434.801NLT
PG0936.113NLT 9220-20 9310-16 PM06-2N7 PM06-39NJ A01TK 1206CS-471XJ HC2LP-R47-R HC2-R47-R HC3-2R2-R HCF13053R3-R 1206CS-151XG RCH664NP-140L RCH664NP-4R7M RCH8011NP-221L RCP1317NP-332L RCP1317NP-391L RCR1010NP-470M