		ECN HIST	ORY LIST		
REV	DATE	DESCRIPTION	APPROVED	CHECKED	DRAWN
1.0	18/06/20	新發行	羅宜春	梁周虎	許靜
備					
注					

SMD Power Inductor

TMPA1265SP-Series(N)-D

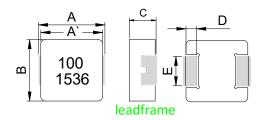
1. Features

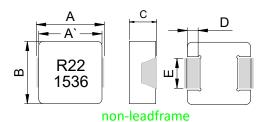
- 1. Shielded construction.
- 2. Capable of corresponding high frequency .
- 3. Low loss realized with low DCR.
- 4. High performance (Isat) realized by metal dust core.
- 5. Ultra low buzz noise, due to composite construction.
- 6. 100% Lead(Pb)-Free and RoHS compliant.

2. Applications

- 1. DC/DC converters in distributed power systems.
- 2. DC/DC converter for Field Programmable Gate Array(FPGA).
- 3. Battery powered devices.
- 4. Thin type on-board power supply module for exchanger.
- 5. VRM for server.
- 6. High current, low profile POL converters.
- 7. PDA/notebook/desktop/server and battery powered devices.

3. Dimensions







Recommend PC Board Pattern



G(mm)

8.0

L(mm)

14.5

Series	Α	A`	В	С	D	E	Inductance
						4.0±0.3	0.68~1.50uH among
TMPA1265	13.5±0.5	12.6±0.3	12.6±0.2	2 6.2±0.3 2.3±0	2.3±0.3	4.7±0.3	0.33uH and below 1.80uH and above

Note: 1. The above PCB layout reference only.	
Recommend solder paste thickness a	t
0.15mm and above	

H(mm)

5.0

Unit:mm

4. Part Numbering

TMPA	1265	SP	-	100	MN	-	D
Α	В	С		D	E		F

A: Series

F: Code

B: Dimension

BxC.

C: Type

Standard.

D: Inductance E: Inductance Tolerance

 $R10 = 0.1 uh, \ 1R0 = 1.0 uh, \ 100 = 10 uh, \ 101 = 100 uh, \ 102 = 1000 uh.$

ince Tolerance K=±10%, L=±15%, M=±20%, N=±25%, Y=±30%

Marking: Black.100 and 1536(15 YY, 36 WW, follow production date).





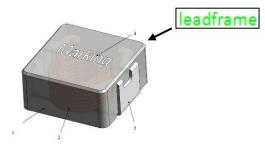
5. Specification

Part Number	rt Number Inductance L0 A(uH) ±20% Heat Rating Current DC (A) Irms.		Saturation Current DC (A)I sat		DCR (mΩ)	DCR (mΩ) Max	Туре	
	±20%	Тур	Max	Тур	Max	Тур	IVIAX	
TMPA1265SP-R10YN-D	0.10±30%	65	60	120	115	0.2	0.25	non-leadframe
TMPA1265SP-R22MN-D	0.22	53	42	112	105	0.4	0.46	non-leadframe
TMPA1265SP-R33MN-D	0.33	46	36	75	65	0.6	0.7	non-leadframe
TMPA1265SP-R68MN-D	0.68	36.5	33	55	46	1.25	1.5	non-leadframe
TMPA1265SP-1R0MN-D	1.00	33	29	45	36	1.5	1.8	non-leadframe
TMPA1265SP-1R5MN-D	1.50	29	25	35	30	2.2	2.53	non-leadframe
TMPA1265SP-1R8MN-D	1.80	27	23	31	27	3.2	3.6	leadframe
TMPA1265SP-2R2MN-D	2.20	25	21	28.5	24	3.7	4.2	leadframe
TMPA1265SP-2R7MN-D	2.70	24	20	27.5	23	4.2	5.0	leadframe
TMPA1265SP-3R3MN-D	3.30	22	19	27	22.5	5.3	6.2	leadframe
TMPA1265SP-4R7MN-D	4.70	20	17	25	21	6.8	8.0	leadframe
TMPA1265SP-5R6MN-D	5.60	18	15	23	19.5	8.3	9.8	leadframe
TMPA1265SP-6R8MN-D	6.80	16.5	14	21	18	9.8	11.3	leadframe
TMPA1265SP-8R2MN-D	8.20	15	12.5	19	17	12	13.8	leadframe
TMPA1265SP-100MN-D	10.0	13	11	17	15	13	15.8	leadframe
TMPA1265SP-150MN-D	15.0	11	9.5	13.5	12	22	26	leadframe
TMPA1265SP-220MN-D	22.0	10	8	10	9	31	35	leadframe
TMPA1265SP-270MN-D	27.0	9.5	7.2	9.0	8.0	36	45	leadframe
TMPA1265SP-330MN-D	33.0	9.0	6.5	9.0	8.0	46	55	leadframe
TMPA1265SP-470MN-D	47.0	8.0	5.7	7.6	6.8	58	67	leadframe
TMPA1265SP-680MN-D	68.0	5.8	4.8	6.0	5.0	82	100	leadframe
TMPA1265SP-820MN-D	82.0	5.0	4.0	5.0	4.2	110	132	leadframe
TMPA1265SP-101MN-D	100	5.0	3.8	5.0	4.0	140	161	leadframe

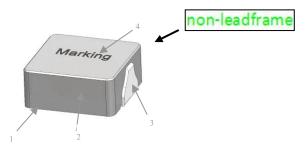
Note:

- 1. Test frequency: Ls: 100KHz /1.0V.
- 2. All test data referenced to 25℃ ambient.
- 3. Testing Instrument(or equ): L: HP4284A,CH11025,CH3302,CH1320,CH1320S LCR METER / Rdc:CH16502,Agilent33420A MICRO OHMMETER.
- 4. Heat Rated Current (Irms) will cause the coil temperature rise approximately $\,\Delta \, T$ of 40 $^{\circ} \! C$
- 5. Saturation Current (Isat) will cause L0 to drop approximately 30%.
- 6. The part temperature (ambient + temp rise) should not exceed 125°C under worst case operating conditions. Circuit design, component, PCB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.
- 7. Special inquiries besides the above common used types can be met on your requirement.

6. Material List



NO	Items	Materials
1	Core	Alloy Powder .
2	Wire	Polyester Wire or equivalent.
3	Clip	100% Pb free solder(Ni+SnPlating)
4	Ink	Halogen-free ketone



NO	Items	Materials
1	Core	Alloy Powder .
2	Wire	Polyester Wire or equivalent.
3	Solder	100% Pb free solder
4	Ink	Halogen-free ketone

7. Reliability and Test Condition

Item	Performance	Test Condition
Operating temperature	-40~+125 [°] C (Including self - temperature rise)	
Storage temperature	110~+40°C,50~60%RH (Product with taping) 240~+125°C (on board)	
Electrical Performance 1	Test .	·
Inductance	Refer to standard electrical characteristics list.	HP4284A,CH11025,CH3302,CH1320,CH1320S LCR Meter.
DCR	Total to standard spottings shall be standard spottings.	CH16502,Agilent33420A Micro-Ohm Meter.
Saturation Current (Isat)	Approximately △L30%.	Saturation DC Current (Isat) will cause L0 to drop △L(%)
Heat Rated Current (Irms)	Approximately △T40°C	Heat Rated Current (Irms) will cause the coil temperature rise △T(℃). 1.Applied the allowed DC current 2.Temperature measured by digital surface thermometer
Reliability Test		
Life Test		Preconditioning: Run through IR reflow for 2 times.(IPC/JEDECJ-STD-020DClassification Reflow Profiles) Temperature: 125±2°C (Inductor) Applied current : rated current Duration: 1000±12hrs Measured at room temperature after placing for 24±2 hrs.
Load Humidity		Preconditioning: Run through IR reflow for 2 times, (IPC/JEDECJ-STD-020DClassification Reflow Profiles) Humidity: $85\pm2\%$ R.H, Temperature: $85\%\pm2\%$ Duration: 1000hrs Min. with 100% rated current Measured at room temperature after placing for 24±2 hrs.
Moisture Resistance	Appearance: No damage. Impedance: within±15% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times. (IPC/JEDECJ-STD-020DClassification Reflow Profiles) 1. Baked at50°C for 25hrs, measured at room temperature after placing for 4 hrs. 2. Raise temperature to 65±2°C 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25°C in 2.5hrs. 3. Raise temperature to 65±2°C 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25°C in 2.5hrs. 4. Keep at 25°C for 2 hrs then keep at -10°C for 3 hrs 4. Keep at 25°C 80-100%RH for 15min and vibrate at the frequency of 10 to 55 Hz to 10 Hz, measure at room temperature after placing for 1~2 hrs.
Thermal shock		Preconditioning: Run through IR reflow for 2 times, (IPC/JEDECJ-STD-020DClassification Reflow Profiles) Condition for 1 cycle Step1: -40±2℃ 30±5min Step2: 25±2℃ ≤0.5min Step3: 125±2℃ 30±5minNumber of cycles: 500
Vibration		Measured at room fempraturc after placing for 24±2 hrs. Preconditioning: Run through IR reflow for 2 times, (IPC/JEDECJ-STD-020DClassification Reflow Profiles) Oscillation Frequency: 10~2K~10Hz for 20 minutes Equipment: Vibration checker Total Amplitude:1.52mm±10% Testing Time: 12 hours(20 minutes, 12 cycles each of 3 orientations)

TAI-TECH

Item	Performance	Test Condition			
Bending	Appearance : No damage. Impedance : within±15% of initial value	Shall be mounted on a FR4 substrate of the following dimensions: >=0805 inch(2012mm):40x100x1.2mm <0805 inch(2012mm):40x100x0.8mm Bending depth: >=0805 inch(2012mm):1.2mm <0805 inch(2012mm):0.8mm duration of 10 sec.			
	Inductance: within±15% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not	Type Value duration (D) Wave change (Vi)ft/sec			
Shock	exceed the specification value	SMD 50 11 Half-sine 11.3			
		Lead 50 11 Half-sine 11.3			
Solder ability	More than 95% of the terminal electrode should be covered with solder \circ	Preheat: 150°C,60sec. Solder: Sn96.5% Ag3% Cu0.5% Temperature: 245±5°C Flux for lead free: Rosin. 9.5% Dip time: 4±1sec Depth: completely cover the termination			
Resistance to Soldering Heat		Depth: completely cover the termination Temperature (°C) Time(s) Temperature ramp/immersion and emersion rate Number of heat cycles			
		260 ±5 (solder temp) 10 ±1 25mm/s ±6 mm/s 1			
Terminal Strength	Appearance: No damage. Impedance: within±15% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value e	Preconditioning: Run through IR reflow for 2 times.(IPC/J J-STD-020DClassification Reflow Profiles With the component mounted on a PCB with the device tested, apply a force(>0805.1kg, <-80805.0.5kg)to the sid device being tested. This force shall be applied for seconds. Also the force shall be applied gradually as apply a shock to the component being tested.			

Note: When there are questions concerning measurement result: measurement shall be made after 48 ± 2 hours of recovery under the standard condition.

8. Soldering and Mounting

(1) Soldering

Mildly activated rosin fluxes are preferred. The minimum amount of solder can lead to damage from the stresses caused by the difference in coefficients of expansion between solder, chip and substrate. TAI-TECH terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

(2) Solder re-flow:

Recommended temperature profiles for re-flow soldering in Figure 1.

(3) Soldering Iron:

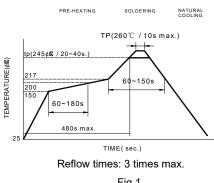
Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.

- · Never contact the ceramic with the iron tip
- · Use a 20 watt soldering iron with tip diameter of 1.0mm

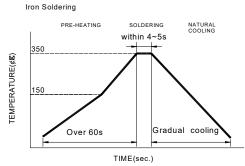
• 355℃ tip temperature (max)

Reflow Soldering

- 1.0mm tip diameter (max)
- · Limit soldering time to 4~5sec.







Iron Soldering times: 1 times max.

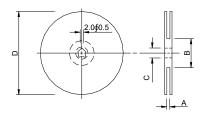
Fig.2

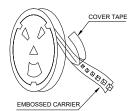
9. Friendly reminder

- (1) When there are questions concerning measurement result : measurement shall be made after 48 \pm 2 hours of recovery under the standard condition
- (2) This power choke coil itself does not have any protective function in abnormal condition such as overload, short-circuit and open-circuit conditions, etc. Therefore, it shall be confirmed as the end product that there is no risk of smoking, fire, dielectric withstand voltage, insulation resistance, etc. in abnormal conditions to provide protective devices and/or protection circuit in the end product.
- (3) When this power choke coil was used in a similar or new product to the original one, sometimes it might not be able to satisfy the specifications due to different condition of use.
- (4) Dielectric withstanding test with higher voltage than specific value will damage insulating material and shorten its life.
- (5) This power choke coil must not be used in wet condition by water, coffee or any liquid because insulation strength becomes very low in this condition.
- (6) Please consult our company to confirm the reliability of the process required to wash or use or exposure to a chemical solvent used in this product.

10. Packaging Information

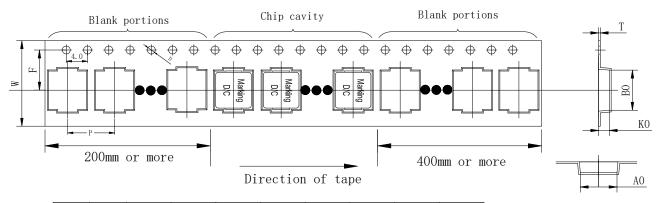
(1) Reel Dimension





Туре	A(mm)	B(mm)	C(mm)	D(mm)
13"x24mm	24.4+2/-0	100±2	13+0.5/-0.2	330

(2) Tape Dimension

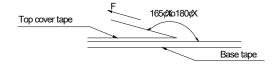


Series	Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	W(mm)	F(mm)	t(mm)	D(mm)
TMPA	1265	14.1±0.1	12.9±0.1	7.0±0.1	16.0±0.1	24±0.3	11.5±0.1	0.35±0.1	1.5±0.1

(3) Packaging Quantity

TMPA	1265
Chip / Reel	500
Inner box	1000
Carton	4000

(4) Tearing Off Force



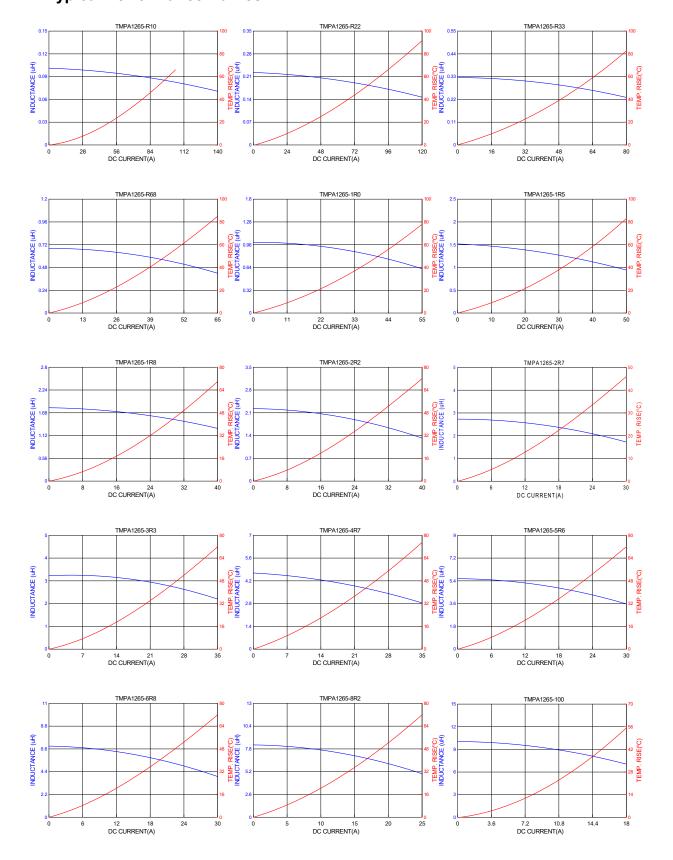
The force for tearing off cover tape is 10 to 130 grams in the arrow direction under the following conditions(referenced ANSI/EIA-481-D-2008 of 4.11 stadnard).

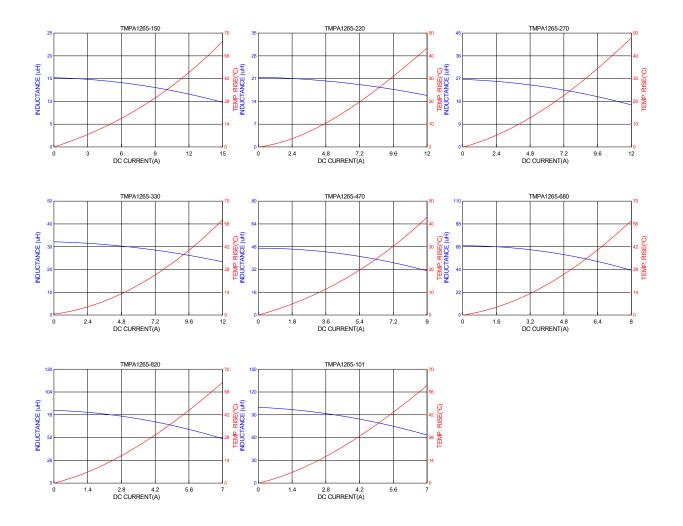
Room Temp.	Room Humidity	Room atm	Tearing Speed
(℃)	(%)	(hPa)	mm/min
5~35	45~85	860~1060	

Application Notice

- · Storage Conditions
- To maintain the solderability of terminal electrodes:
- 1. TAI-TECH products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
- 3. Recommended products should be used within 12 months form the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
 - 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
 - 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
 - 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

11. Typical Performance Curves





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CR54NP-8R5MC 70F224AI MGDQ4-00004-P MHL1ECTTP18NJ MHQ1005P10NJ MHQ1005P1N0S MHQ1005P2N4S MHQ1005P3N6S
MHQ1005P5N1S MHQ1005P8N2J PE-51506NL PE-53601NL PE-53602NL PE-53630NL PE-53824SNLT PE-92100NL PG0434.801NLT
PG0936.113NLT 9220-20 9310-16 PM06-2N7 PM06-39NJ A01TK 1206CS-471XJ HC2LP-R47-R HC2-R47-R HC3-2R2-R HCF13053R3-R 1206CS-151XG RCH664NP-140L RCH664NP-4R7M RCH8011NP-221L RCP1317NP-332L RCP1317NP-391L RCR1010NP-470M