

Specification for Approval

Date: 2019/10/31

<u>Customer</u>: 深圳臺慶

TAI-TECH P/N: TMPC1004H-470MG-D

CUSTOMER P/N:

DESCRIPTION:

QUANTITY:

pcs

REMARK:		
Cu	stomer Approval Feedba	ack

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SMD Power Inductor

TMPC1004H-470MG-D

	ECN HISTORY LIST						
REV	DATE	DESCRIPTION	APPROVED	CHECKED	DRAWN		
1.0	19/10/31	新發行	羅宜春	梁周虎	許靜		
備							
注							

SMD Power Inductor

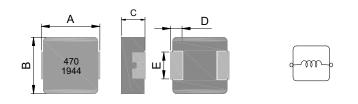
1. Features

- 1. Carbonyl Powder.
- 2. Compact design.
- 3. High current , low DCR , high efficiency.
- 4. Very low acoustic noise and very low leakage flux noise.
- 5. High reliability.
- 6. 100% Lead(Pb)-Free and RoHS compliant.
- 7. Operating temperature -40~+125°C (Including self temperature rise)

2. Applications

Note PC power system , incl. IMVP-6 DC/DC converter .

3. Dimensions



Series	A(mm)	B(mm)	C(mm)	D(mm)	E(mm)
TMPC1004H	11.0±0.5	10.0±0.3	3.8±0.2	2.3±0.3	3.0±0.3

4. Part Numbering

TMPC	1004	Η	-	470	MG	-	D
А	В	С		D	Е		F
A: Series B: Dimen C: Type D: Inducta E: Inducta F: Code	sion	се	470= M=±2			1944	14(19 YY, 44 WW,follow production date).

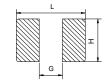
5. Specification

Part Number	Inductance L0 (uH)±20%	I rms(A) Typ	l sat(A) Typ	DCR (mΩ)Typ. @25℃	DCR (mΩ) Max. @25℃
TMPC1004H-470MG-D	47.0	3.0	4.5	145.0	167.0

Note:

- 1. Test frequency : Ls : 100KHz /1.0V.
- 2. All test data referenced to $25^\circ\!\!\mathbb{C}$ ambient.
- 3. Testing Instrument(or equ) : L: HP4284A,CH1025,CH3302,CH1320,CH1320S LCR METER / Rdc:CH16502,Agilent33420A MICRO OHMMETER.
- 4. Heat Rated Current (Irms) will cause the coil temperature rise approximately ΔT of 40°C
- 5. Saturation Current (Isat) will cause L0 to drop approximately 30%.
- 6. The part temperature (ambient + temp rise) should not exceed 125°C under worst case operating conditions. Circuit design, component, PCB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.
- 7. Special inquiries besides the above common used types can be met on your requirement.

Recommend PC Board Pattern



L(mm)	G(mm)	H(mm)
13.6	5.4	3.5

Note: 1. The above PCB layout reference only. 2. Recommend solder paste thickness at 0.15mm and above.

P2



6. Material List

4	5
Marking	
	3
2	

NO	Items	Materials
1	Core	Carbonyl Powder.
2	Wire	Polyester Wire or equivalent.
3	Clip	100% Pb free solder(Ni+SnPlating)
4	Ink	Halogen-free ketone
5	paint	Epoxy resin

7. Reliability and Test Condition

Item	Performance	Test Condition
Operating temperature	-40~+125°C (Including self - temperature rise)	
Storage temperature	110~+40℃,50~60%RH (Product with taping) 240~+125℃ (on board)	
Electrical Performance	Test	
Inductance	Refer to standard electrical characteristics list.	HP4284A,CH11025,CH3302,CH1320,CH1320S LCR Meter.
DCR		CH16502,Agilent33420A Micro-Ohm Meter.
Saturation Current (Isat)	Approximately △L30%.	Saturation DC Current (Isat) will cause L0 to drop \triangle L(%)
Heat Rated Current (Irms)	Approximately △T40℃	Heat Rated Current (Irms) will cause the coil temperature rise △T(℃). 1.Applied the allowed DC current 2.Temperature measured by digital surface thermometer
Reliability Test		
Life Test		Preconditioning: Run through IR reflow for 2 times.(IPC/JEDECJ-STD-020DClassification Reflow Profiles) Temperature : 125±2°C(Inductor) Applied current : rated current Duration : 1000±12hrs Measured at room temperature after placing for 24±2 hrs.
Load Humidity		Preconditioning: Run through IR reflow for 2 times.(IPC/JEDECJ-STD-020DClassification Reflow Profiles) Humidity: $85\pm2\%$ R.H., Temperature : $85^{\circ}C\pm2^{\circ}C$ Duration : 1000hrs Min. with 100% rated current Measured at room temperature after placing for 24±2 hrs.
Moisture Resistance	Appearance : No damage. Impedance : within±15% of initial value Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times.(IPC/JEDECJ-STD-020DClassification Reflow Profiles) 1. Baked at50°C for 25hrs, measured at room temperature after placing for 4 hrs. 2. Raise temperature to $65\pm2°C$ 90-100%RH in 2.5hrs, and keep 3 hours, cool down to $25°C$ in 2.5hrs. 3. Raise temperature to $65\pm2°C$ 90-100%RH in 2.5hrs, and keep 3 hours, cool down to $25°C$ in 2.5hrs,keep at $25°C$ for 2 hrs then keep at -10°C for 3 hrs 4. Keep at $25°C$ 80-100%RH for 15min and vibrate at the frequency of 10 to 55 Hz to 10 Hz, measure at room temperature after placing for 1~2 hrs.
Thermal shock		Preconditioning: Run through IR reflow for 2 times.(IPC/JEDECJ-STD-020DClassification Reflow Profiles) Condition for 1 cycle Step1 : $-40\pm2^{\circ}$ 30 \pm 5min Step2 : $25\pm2^{\circ}$ C ≤ 0.5 min Step3 : $125\pm2^{\circ}$ C $30\pm$ 5minNumber of cycles : 500 Measured at room femprature after placing for 24±2 hrs.
Vibration		Preconditioning: Run through IR reflow for 2 times. (IPC/JEDECJ-STD-020DClassification Reflow Profiles) Oscillation Frequency: 10Hz ~ 2KHz ~ 10Hz for 20 minutes Equipment: Vibration checker Total Amplitude: 10g Testing Time : 12 hours(20 minutes, 12 cycles each of 3 orientations).

TAI-TECH

Item	Performance	Test Condition						
	Appearance : No damage.	Shall be mounted on a FR4 substrate of the following dimensions: >=0805 inch(2012mm):40x100x1.2mm <0805 inch(2012mm):40x100x0.8mm Bending depth: >=0805 inch(2012mm):1.2mm <0805 inch(2012mm):0.8mm duration of 10 sec.						
	Impedance : within±15% of initial value Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not	Тур	Peak value (g's)	durat	ormal tion (D) ms)	Wave form	Velocity change (Vi)ft/sec	
Shock	exceed the specification value	SM	50		11	Half-sine	11.3	
		Lea	50		11	Half-sine	11.3	
Solder ability	More than 95% of the terminal electrode should be covered with solder。	Preheat: 150℃,60sec.。 Solder: Sn96.5% Ag3% Cu0.5% Temperature: 245±5℃ ∘ Flux for lead free: Rosin. 9.5% ∘ Dip time: 4±1sec ∘ Depth: completely cover the termination						
		Depth: completely cover the termination						
Resistance to Soldering Heat		Temperature(°C) Time(s) Temperature ramp/immersion and emersion rate		nmersion	Number of heat cycles			
		260 ±5 (solder temp) 10 ±1 25mm/s ±6 mm/s 1					1	
	Appearance : No damage. Impedance : within±15% of initial value Inductance : within±10% of initial value Q : Shall not exceed the specification value. BDC : within ±15% of initial value and shall not	J-STD- With th tested, device second	020DClass e compone apply a for being tes	ification F ent moun rce(>0805 led. This e force s le compo	Reflow Pri- ted on a 5:1kg, <= 5: force s shall be a nent bein	ofiles PCB with =0805:0.5kg hall be ap applied gra	nes.(IPC/JEDEC the device to be g)to the side of a p)ted for 60 +1 dually as not to	

Note : When there are questions concerning measurement result : measurement shall be made after 48 ± 2 hours of recovery under the standard condition.

8. Soldering and Mounting

(1) Soldering

Mildly activated rosin fluxes are preferred. The minimum amount of solder can lead to damage from the stresses caused by the difference in coefficients of expansion between solder, chip and substrate. TAI-TECH terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

(2) Solder re-flow:

Recommended temperature profiles for re-flow soldering in Figure 1.

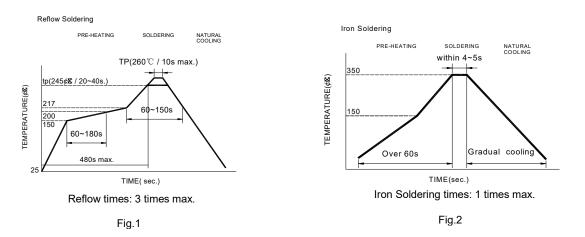
(3) Soldering Iron:

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.

Use a 20 watt soldering iron with tip diameter of 1.0mm

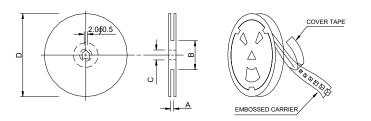
· Limit soldering time to 4~5sec.

- \cdot Preheat circuit and products to 150 $^\circ\!\!\mathbb{C}$ $}$ \cdot Never contact the ceramic with the iron tip
- 355°C tip temperature (max) 1.0mm tip diameter (max)
 - r (max)



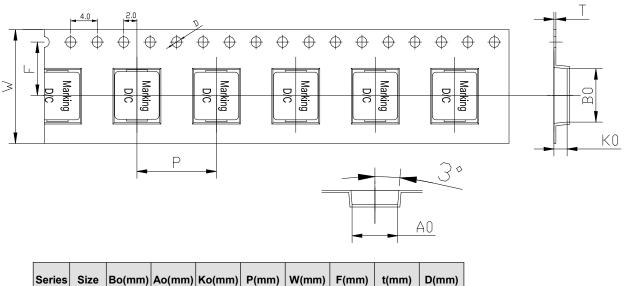
9. Packaging Information

(1) Reel Dimension



Туре	A(mm)	B(mm)	C(mm)	D(mm)
13"x24mm	24.4+2/-0	100±2	13+0.5/-0.2	330

(2) Tape Dimension



Genes	0120	Bo(iiiii)	Α υ(IIIII)	1.0(1111)	. ()	••()	1 (1111)	ų,	D(IIIII)
тмрс	1004	11.6±0.1	10.4±0.1	4.5±0.1	16.0±0.1	24±0.3	11.5±0.1	0.35±0.05	1.5±0.1

(3) Packaging Quantity

ТМРС	1004
Chip / Reel	500
Inner box	1000
Carton	4000

(4) Tearing Off Force

165¢%/0180¢X Top cover tape Base tape

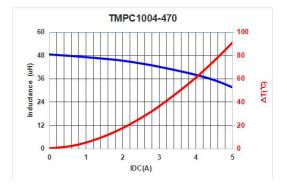
The force for tearing off cover tape is 10 to 130 grams in the arrow direction under the following conditions(referenced ANSI/EIA-481-D-2008 of 4.11 stadnard).

Room Temp. Room Humidity		Room atm	Tearing Speed	
(°C)	(°C) (%)		mm/min	
5~35	45~85	860~1060	300	

Application Notice

- · Storage Conditions
- To maintain the solderability of terminal electrodes:
- 1. TAI-TECH products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
- 2. Temperature and humidity conditions: Less than 40 $^\circ\!\!\!C$ and 60% RH.
- Recommended products should be used within 12 months form the time of delivery.
 The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
- 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

10. Typical Performance Curves



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