

# Specification for Approval

Date: 2019/10/31

Customer: 深圳臺慶

	TAI-TECH P/N:	TMPC1707HP-3R3	MG-D
	CUSTOMER P/N:		
	DESCRIPTION:		
	QUANTITY:	pcs	<u>3</u>
REM	MARK:		
	Cu	stomer Approval Feedba	ack

## □西北臺慶科技股份有限公司

TAI-TECH Advanced Electronics Co., Ltd <u>Headquarter:</u>

NO.1 YOU 4TH ROAD, YOUTH INDUSTRIAL DISTRICT, YANG-MEI, TAO-YUAN HSIEN, TAIWAN, R.O.C.

TEL: +886-3-4641148 FAX: +886-3-4643565

http://www.tai-tech.com.tw E-mail: sales@tai-tech.com.tw

#### □Office:

深圳辦公室

11BC,Building B Fortune Plaza,NO.7002, Shennan Avenue, Futian District Shenzhen

TEL: +86- 755-23972371 FAX: +86-755-23972340

#### □臺慶精密電子(昆山)有限公司

TAI-TECH ADVANCED ELECTRONICS(KUNSHAN) CO., LTD SHINWHA ROAD, KUNJIA HI-TECH INDUSTRIAL PARK, KUN-SHAN, JIANG-SU, CHINA

## ■慶邦電子元器件(泗洪)有限公司

TAIPAQ ELECTRONICS (SIHONG) CO., LTD JIN SHA JIANG ROAD, CONOMIC DEVELOPMENT ZONE SIHONG, JIANGSU, CHINA.

TEL: +86-527-88601191 FAX: +86-527-88601190

E-mail: sales@taipaq.cn

## Sales Dep.

APPROVED	CHECKED
管哲頎	曾詩涵
Eric Kuan	Angela Tseng

#### **R&D** Center

APPROVED	CHECKED	DRAWN
羅宜春	梁周虎	許靜

## **SMD Power Inductor**

TMPC1707HP-3R3MG-D

		ECN HISTO	DRY LIS	Т	
REV	DATE	DESCRIPTION	APPROVED	CHECKED	DRAWN
1.0	19/10/31	新發行	羅宜春	梁周虎	許靜
備					
注					

## **SMD Power Inductor**

TMPC1707HP-3R3MG-D

#### 1. Features

- 1. Carbonyl Powder.
- 2. Compact design.
- 3. High current , low DCR , high efficiency.
- 4. Very low acoustic noise and very low leakage flux noise.
- 5. High reliability.
- 6. 100% Lead(Pb)-Free and RoHS compliant.
- 7. Operating temperature -40~+125  $^{\circ}\text{C}\textsc{(Including self temperature rise)}$

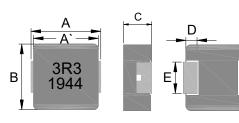




## 2. Applications

Note PC power system , incl. IMVP-6 DC/DC converter .

## 3. Dimensions





**Recommend PC Board Pattern** 

Series	A(mm)	A'(mm)	B(mm)	C(mm)	D(mm)	E(mm)	
TMPC1707HP	17.6±0.4	16.9±0.3	16.9±0.3	6.7±0.3	2.1±0.3	11.9±0.3	

#### L(mm) G(mm) H(mm) 18.5 12.2 12.5

Note: 1. The above PCB layout reference only. 2. Recommend solder paste thickness at 0.17mm and above.

## 4. Part Numbering

<b>TMPC</b>	<b>1707</b>	HP	-	<b>3R3</b>	MG -	D
Α	В	С		D	E	F

A: Series

B: Dimension

BxC C: Type HP:H:Carbonyl Powder,P:PAD broaden.

D: Inductance 3R3=3.30uH E: Inductance Tolerance M=±20%

F: Code Marking: Black.3R3 and 1944(19 YY, 44 WW,follow production date).

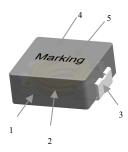
## 5. Specification

Part Number	Inductance L0 (uH)±20%	I rms ( A ) Typ	I sat 1( A ) Typ	I sat2 ( A ) Typ	DCR (mΩ) Typ. @25℃	DCR (mΩ) Max. @25℃
TMPC1707HP-3R3MG-D	3.30	28.0	45.0	54.0	3.5	3.9

## Note:

- 1. Test frequency: L/Q: 100KHz /1.0V
- 2. All test data referenced to 25°C ambient.
- 3. Testing Instrument: L/Q: HP4284A,CH11025,CH3302,CH1320 ,CH1320S LCR METER / Rdc:CH16502,Agilent33420A MICRO OHMMETER.
- 4. Heat Rated Current (Irms) will cause the coil temperature rise approximately  $\Delta t$  of 40°C.
- 5. Saturation Current (Isat1) will cause L0 to drop approximately 20% Saturation Current (Isat2) will cause L0 to drop approximately 30%.
- 6. The part temperature (ambient + temp rise) should not exceed 125°C under worst case operating conditions. Circuit design, component, PCB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.
- 7. Special inquiries besides the above common used types can be met on your requirement.

## 6. Material List



NO	Items	Materials
1	Core	Carbonyl Powder.
2	Wire	Polyester Wire or equivalent.
3	Clip	100% Pb free solder(Ni+SnPlating)
4	paint	Epoxy resin
5	Ink	Halogen-free ketone

## 7. Reliability and Test Condition

Item	Performance	Test Condition
Operating temperature	-40~+125℃ (Including self - temperature rise)	
Storage temperature	110~+40°C,50~60%RH (Product with taping) 240~+125°C (on board)	
Electrical Performance	Test	
Inductance	24	HP4284A,CH11025,CH3302,CH1320,CH1320S LCR Meter.
DCR	Refer to standard electrical characteristics list.	CH16502,Agilent33420A Micro-Ohm Meter.
Saturation Current (Isat)	Approximately △L20%. Approximately △L30%.	Saturation DC Current (Isat) will cause L0 to drop $\triangle$ L(%)
Heat Rated Current (Irms)	Approximately △T40°C	Heat Rated Current (Irms) will cause the coil temperature rise △T(°C) without core loss.  1.Applied the allowed DC current 2.Temperature measured by digital surface thermometer
Reliability Test		
Life Test		Preconditioning: Run through IR reflow for 2 times.( IPC/JEDEC J-STD-020DClassification Reflow Profiles) Temperature: 125±2°C (Inductor) Applied current: rated current Duration: 1000±12hrs Measured at room temperature after placing for 24±2 hrs
Load Humidity		Preconditioning: Run through IR reflow for 2 times.( IPC/JEDEC J-STD-020DClassification Reflow Profiles Humidity: 85±2% R.H, Temperature: 85°C±2°C Duration: 1000hrs Min. with 100% rated current Measured at room temperature after placing for 24±2 hrs
Moisture Resistance	Appearance: No damage. Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times.( IPC/JEDEC J-STD-020DClassification Reflow Profiles  1. Baked at50℃ for 25hrs, measured at room temperature after placing for 4 hrs.  2. Raise temperature to 65±2℃ 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25℃ in 2.5hrs.  3. Raise temperature to 65±2℃ 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25℃ in 2.5hrs, each keep 3 hours, cool down to 25℃ in 2.5hrs,keep at 25℃ for 2 hrs then keep at -10℃ for 3 hrs  4. Keep at 25℃ 80-100%RH for 15min and vibrate at the frequency of 10 to 55 Hz to 10 Hz, measure at room temperature after placing for 1~2 hrs.
Thermal shock		Preconditioning: Run through IR reflow for 2 times.( IPC/JEDEC J-STD-020DClassification Reflow Profiles Condition for 1 cycle Step1: -40±2°C 30±5min Step2: 25±2°C ≤0.5min Step3: 125±2°C 30±5min Number of cycles: 500 Measured at room temperature after placing for 24±2 hrs
Vibration		Preconditioning: Run through IR reflow for 2 times.( IPC/JEDECJ-STD-020DClassification Reflow Profiles) Oscillation Frequency: 10Hz~2KHz~10Hz for 20 minutes Equipment: Vibration checker Total Amplitude: 10g Testing Time: 12 hours(20 minutes, 12 cycles each of 3 orientations)

Item	Performance	Test Condition					
Bending	Appearance : No damage. Impedance : within±15% of initial value	Shall be mounted on a FR4 substrate of the following dimensions: >=0805 inch(2012mm):40x100x1.2mm <0805 inch(2012mm):40x100x0.8mm Bending depth: >=0805 inch(2012mm):1.2mm <0805 inch(2012mm):0.8mm duration of 10 sec.					
Shock	Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not	Type Value duration (D) Wave change (Vi)ft/sec					
Shock	exceed the specification value	SMD         50         11         Half-sine         11.3					
		Lead         50         11         Half-sine         11.3					
Solder ability	More than 95% of the terminal electrode should be covered with solder •	Preheat: 150°C,60sec. ° Solder: Sn96.5% Ag3% Cu0.5% Temperature: 245±5°C ° Flux for lead free: Rosin. 9.5% ° Dip time: 4±1sec ° Depth: completely cover the termination Depth: completely cover the termination					
Resistance to Soldering Heat		Temperature(°C) Time(s) Temperature ramp/immersion and emersion rate heat cycle  260 ±5 (solder temp) 10 ±1 25mm/s ±6 mm/s 1					
Terminal Strength	Appearance: No damage. Impedance: within±15% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value e	Preconditioning: Run through IR reflow for 2 times.( IPC/JEDE J-STD-020DClassification Reflow Profiles With the component mounted on a PCB with the device to be tested, apply a force(>0805:1kg, <=0805:0.5kg) to the side of device being tested. This force shall be applied for 60 4 seconds. Also the force shall be applied gradually as not apply a shock to the component being tested.					

Note : When there are questions concerning measurement result : measurement shall be made after 48  $\pm$  2 hours of recovery under the standard condition

## 8. Soldering and Mounting

## (1) Soldering

Mildly activated rosin fluxes are preferred. The minimum amount of solder can lead to damage from the stresses caused by the difference in coefficients of expansion between solder, chip and substrate. TAI-TECH terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

#### (2) Solder re-flow:

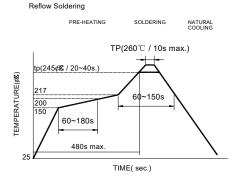
Recommended temperature profiles for re-flow soldering in Figure 1.

#### (3) Soldering Iron:

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.

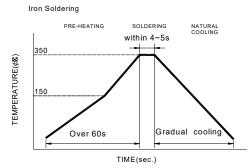
- $\bullet$  Preheat circuit and products to 150  $\!\!\!\!\!\!^{\circ}_{\circ}$
- Never contact the ceramic with the iron tip
- Use a 20 watt soldering iron with tip diameter of 1.0mm

- 355°C tip temperature (max)
- 1.0mm tip diameter (max)
- Limit soldering time to 4~5sec.



Reflow times: 3 times max.

Fig.1

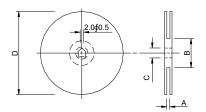


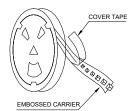
Iron Soldering times: 1 times max.

Fig.2

## 9. Packaging Information

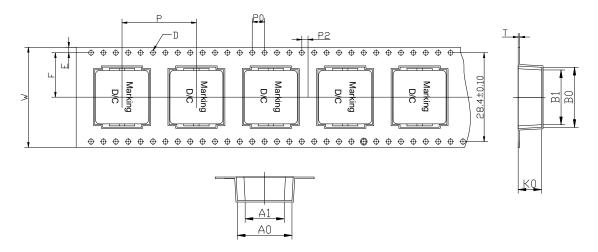
## (1) Reel Dimension





Туре	A(mm)	B(mm)	C(mm)	D(mm)	
13"x32mm	32.4+2/-0	100±2	13+0.5/-0.2	330	

#### (2) Tape Dimension

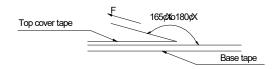


Series	Size	Bo(mm)	B1(mm)	Ao(mm)	A1(mm)	Ko(mm)	P(mm)	P0(mm)	P2(mm)	W(mm)	E(mm)	F(mm)	t(mm)	D(mm)
TMPC	1707	18.5±0.1	17.5±0.1	17.5±0.1	12.6±0.1	7.5±0.1	24±0.1	4.0±0.1	2.0±0.05	32±0.3	1.75±0.1	14.2±0.1	0.5±0.05	1.5±0.1

## (3) Packaging Quantity

ТМРС	1707	
Chip / Reel	200	
Inner box	200	
Carton	800	

## (4) Tearing Off Force



The force for tearing off cover tape is 10 to 130 grams in the arrow direction under the following conditions(referenced ANSI/EIA-481-D-2008 of 4.11 stadnard).

Room Temp.	Room Humidity	Room atm	Tearing Speed
(℃)	(%)	(hPa)	mm/min
5~35	45~85	860~1060	300

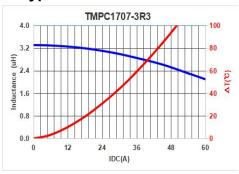
#### **Application Notice**

Storage Conditions

- To maintain the solderability of terminal electrodes:

  1. TAI-TECH products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
- 3. Recommended products should be used within 12 months form the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
- 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- $2. \ The \ use \ of \ tweezers \ or \ vacuum \ pick \ up \ is \ strongly \ recommended \ for \ individual \ components.$
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

## **10. Typical Performance Curves**



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