

# Power Inductor

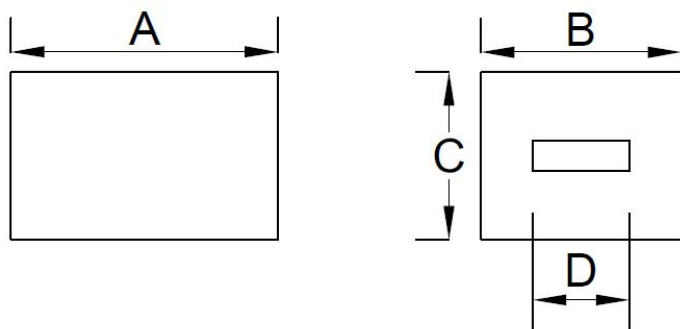
BPH853025W4-101T

## 1. Features

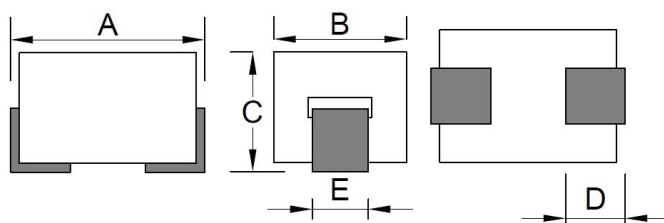
- 100% Lead(Pb) & Halogen-Free and RoHS compliant.
- Operating temperature -40~+125°C (Including self - temperature rise)



## 2. Dimension



CORE SIZE			
A(mm)	B(mm)	C(mm)	D(mm)
8.5 ±0.25	3.0 ±0.15	2.5 ±0.15	1.5 ±0.15



PRODUCT SIZE				
A(mm)	B(mm)	C(mm)	D(mm)	E(mm)
9.0 ±0.40	3.0 ±0.15	2.8 ±0.25	1.50±0.50	1.25±0.20

## 3. Part Numbering

**BPH**   **853025**   **W4**   -   **101**   **T**

A: Series

B: Dimension

C: Material

D: Impedance

E: Packaging

Ferrite Core

101=100Ω

T=Taping and Reel

## 4. Specification

TAI-TECH Part Number	ELECTRICAL REQUIREMENTS 1			ELECTRICAL REQUIREMENTS 2			DCR (mΩ) Max.	Rated Current(A)	
	Impedance (Ω)	Tolerance (%)	Test Frequency (MHz)	Impedance (Ω)	Tolerance (%)	Test Frequency (MHz)		ΔT=40°C TYP.	ΔT=60°C TYP.
BPH853025W4-101T	65	±25	25	100	±25	100	1.00	30(1) 13(2)	40(1) 18(2)

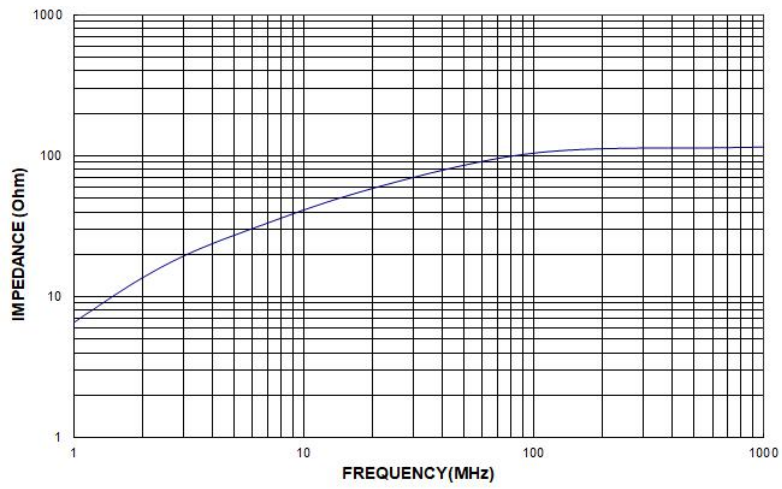
Note:

Rated Current :

(1) : Chroma high current test fixture.

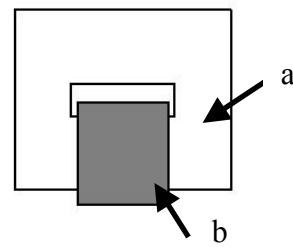
(2) :PCB test fixture ( 30x45mm copper pattern , 50um copper thickness).

### Typical Impedance v.s. Frequency Curve



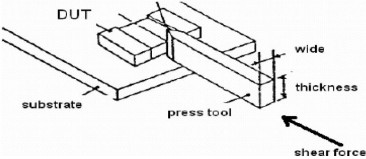
### 5. Material List

No.	Description	Specification
a.	Core	Ferrite Core
b.	Wire	Electroplated nickel-tin flat copper wire (1.25W X 0.20T)m/m



## 6. Reliability and Test Condition

Item	Performance	Test Condition
Operating temperature	-40~+125℃ (Including self - temperature rise)	
Storage temperature	-40~+125℃ (on board)	
<b>Electrical Performance Test</b>		
Z( Impedance)	Refer to standard electrical characteristics list.	Agilent E4991A,Agilent Fixture 16197A LCR Meter.
DCR		Agilent33420A Micro-Ohm Meter.
Heat Rated Current (Irms)	Approximately $\Delta T \leq 40^{\circ}\text{C}$	Heat Rated Current (Irms) will cause the coil temperature rise $\Delta T(^{\circ}\text{C})$ without core loss. 1.Applied the allowed DC current(keep 1 min.). 2.Temperature measured by digital surface thermometer
<b>Reliability Test</b>		
Life Test	Appearance : No damage. Impedance : within $\pm 15\%$ of initial value Inductance : within $\pm 10\%$ of initial value Q : Shall not exceed the specification value. RDC : within $\pm 15\%$ of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times.( IPC/JEDEC J-STD-020DClassification Reflow Profiles) Temperature : $125 \pm 2^{\circ}\text{C}$ (Inductor) Applied current : rated current Duration : $1000 \pm 12$ hrs Measured at room temperature after placing for $24 \pm 2$ hrs
Load Humidity		Preconditioning: Run through IR reflow for 2 times.( IPC/JEDEC J-STD-020DClassification Reflow Profiles) Humidity : $85 \pm 2\%$ R.H, Temperature : $85^{\circ}\text{C} \pm 2^{\circ}\text{C}$ Duration : 1000hrs Min. with 100% rated current Measured at room temperature after placing for $24 \pm 2$ hrs
Moisture Resistance		Preconditioning: Run through IR reflow for 2 times.( IPC/JEDEC J-STD-020DClassification Reflow Profiles) 1. Baked at $50^{\circ}\text{C}$ for 25hrs, measured at room temperature after placing for 4 hrs. 2. Raise temperature to $65 \pm 2^{\circ}\text{C}$ 90-100%RH in 2.5hrs, and keep 3 hours, cool down to $25^{\circ}\text{C}$ in 2.5hrs. 3. Raise temperature to $65 \pm 2^{\circ}\text{C}$ 90-100%RH in 2.5hrs, and keep 3 hours, cool down to $25^{\circ}\text{C}$ in 2.5hrs,keep at $25^{\circ}\text{C}$ for 2 hrs then keep at $-10^{\circ}\text{C}$ for 3 hrs 4. Keep at $25^{\circ}\text{C}$ 80-100%RH for 15min and vibrate at the frequency of 10 to 55 Hz to 10 Hz, measure at room temperature after placing for 1~2 hrs.
Thermal shock		Preconditioning: Run through IR reflow for 2 times.( IPC/JEDEC J-STD-020DClassification Reflow Profiles) Condition for 1 cycle Step1 : $-40 \pm 2^{\circ}\text{C}$ 30 $\pm 5$ min Step2 : $25 \pm 2^{\circ}\text{C}$ $\leq 0.5$ min Step3 : $125 \pm 2^{\circ}\text{C}$ 30 $\pm 5$ min Number of cycles : 500 Measured at room temperature after placing for $24 \pm 2$ hrs
Vibration		Oscillation Frequency: 10~2K~10Hz for 20 minutes Equipment : Vibration checker Total Amplitude:1.52mm $\pm 10\%$ Testing Time : 12 hours(20 minutes, 12 cycles each of 3 orientations) *

Item	Performance	Test Condition															
Bending	Appearance : No damage. Impedance : within±15% of initial value Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value	Shall be mounted on a FR4 substrate of the following dimensions: >=0805 inch(2012mm):40x100x1.2mm <0805 inch(2012mm):40x100x0.8mm Bending depth: >=0805 inch(2012mm):1.2mm <0805 inch(2012mm):0.8mm duration of 10 sec.															
Shock	Appearance : No damage. Impedance : within±15% of initial value Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value	<table border="1"> <thead> <tr> <th>Type</th> <th>Peak value (g's)</th> <th>Normal duration (D) (ms)</th> <th>Wave form</th> <th>Velocity change (Vi)/ft/sec</th> </tr> </thead> <tbody> <tr> <td>SMD</td> <td>50</td> <td>11</td> <td>Half-sine</td> <td>11.3</td> </tr> <tr> <td>Lead</td> <td>50</td> <td>11</td> <td>Half-sine</td> <td>11.3</td> </tr> </tbody> </table>	Type	Peak value (g's)	Normal duration (D) (ms)	Wave form	Velocity change (Vi)/ft/sec	SMD	50	11	Half-sine	11.3	Lead	50	11	Half-sine	11.3
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SMD	50	11	Half-sine	11.3													
Lead	50	11	Half-sine	11.3													
Solder ability	More than 95% of the terminal electrode should be covered with solder ◦	Preheat: 150℃,60sec. ◦ Solder: Sn96.5% Ag3% Cu0.5% Temperature: 245±5℃ ◦ Flux for lead free: Rosin. 9.5% ◦ Dip time: 4±1sec ◦ Depth: completely cover the termination Depth: completely cover the termination															
Resistance to Soldering Heat		<table border="1"> <thead> <tr> <th>Temperature(°C)</th> <th>Time(s)</th> <th>Temperature ramp/immersion and emersion rate</th> <th>Number of heat cycles</th> </tr> </thead> <tbody> <tr> <td>260 ±5 (solder temp)</td> <td>10 ±1</td> <td>25mm/s ±6 mm/s</td> <td>1</td> </tr> </tbody> </table>	Temperature(°C)	Time(s)	Temperature ramp/immersion and emersion rate	Number of heat cycles	260 ±5 (solder temp)	10 ±1	25mm/s ±6 mm/s	1							
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Terminal Strength	Appearance : No damage. Impedance : within±15% of initial value Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value e	Preconditioning: Run through IR reflow for 2 times.( IPC/JEDEC J-STD-020DClassification Reflow Profiles With the component mounted on a PCB with the device to be tested, apply a force(>0805:1kg , <=0805:0.5kg)to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to apply a shock to the component being tested.  															

## 7. Soldering and Mounting

### 7-1. Soldering

Mildly activated rosin fluxes are preferred. TAI-TECH terminations are suitable for all wave and re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

**7-1.1 Solder re-flow:**

Recommended temperature profiles for re-flow soldering in Figure 1.

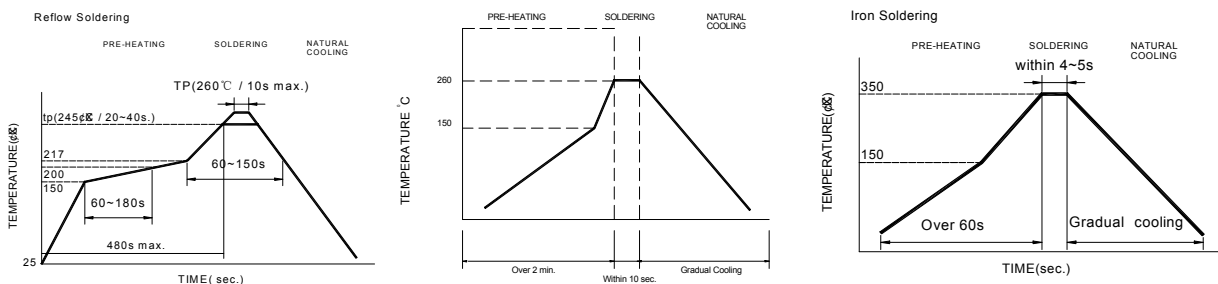
**7-1.2 Solder Wave:**

Wave soldering is perhaps the most rigorous of surface mount soldering processes due to the steep rise in temperature seen by the circuit when immersed in the molten solder wave. Due to the risk of thermal damage to products, wave soldering of large size products is discouraged. Recommended temperature profile for wave soldering is shown in Figure 2.

**7-1.3 Soldering Iron:**

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. If a soldering iron must be employed the following precautions are recommended. for Iron Soldering in Figure 3.

- Preheat circuit and products to 150°C
- Never contact the ceramic with the iron tip
- Use a 20 watt soldering iron with tip diameter of 1.0mm
- 350°C tip temperature (max)
- 1.0mm tip diameter (max)
- Limit soldering time to 4~5 sec.

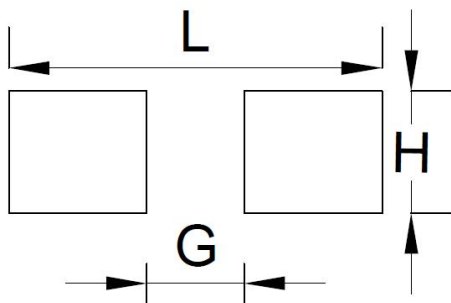


**7-2. Recommended PC Board Pattern**

Reflow times: 3 times max  
Fig.1

Wave Soldering  
Fig.2

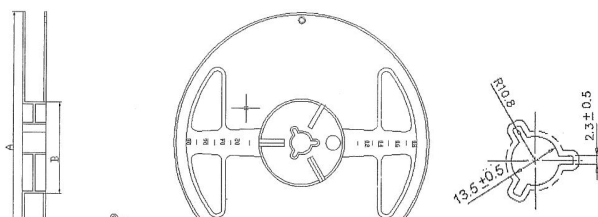
Iron Soldering times : 1 times max  
Fig.3



L(mm)	G(mm)	H(mm)
10.7	4.5	1.5

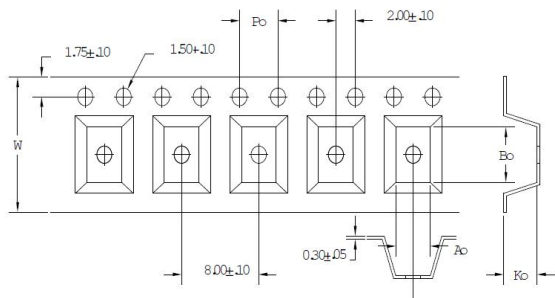
**8. Packaging Information**

**8-1. Reel Dimension**



Type	A(mm)	B(mm)	C(mm)	D(mm)
7"x16mm	178±2	60±2	13.5±0.5	16.7±0.5

8-2. Tape Dimension / 16mm

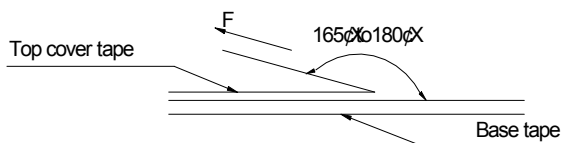


Series	Size	W(mm)	Po(mm)	Ao(mm)	Bo(mm)	Ko(mm)
BPH	853025	16.±0.30	4.0±0.10	3.25±0.10	9.25±0.10	3.05±0.10

8-3. Packaging Quantity

Chip size	Chip / Reel	Inner box	Middle box	Carton
BPH853025	500	2000	10000	20000

8-4. Tearing Off Force



The force for tearing off cover tape is 15 to 80 grams in the arrow direction under the following conditions.

Room Temp. (°C)	Room Humidity (%)	Room atm (hPa)	Tearing Speed mm/min
5~35	45~85	860~1060	300

**Application Notice**

- Storage Conditions(component level)  
To maintain the solderability of terminal electrodes:
  1. TAI-TECH products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
  2. Temperature and humidity conditions: Less than 40°C and 60% RH.
  3. Recommended products should be used within 12 months form the time of delivery.
  4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
  1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
  2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
  3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

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