



Specification for Approval

Date: 2021/10/9







Customer: TAI-TECH P/N: FHI2012F-100MT01 CUSTOMER P/N: FHI2012F-100MT01 DESCRIPTION: QUANTITY: pcs REMARK: Customer Approval Feedback 西北臺慶科技股份有限公司 TAI-TECH Advanced Electronics Co., Ltd

Sales Dep.

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TAI-TECH KBM01-181000510 P2.

Ferrite Chip Inductor (Lead Free)

FHI2012F-100MT01

1.Features

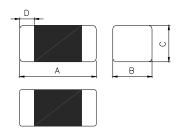
- 1. Monolithic inorganic material construction.
- 2. Closed magnetic circuit avoids crosstalk.
- 3. S.M.T. type.
- 4. Suitable for reflow soldering.
- 5. Shapes and dimensions follow E.I.A. spec.
- 6. Available in various sizes.
- 7. Excellent solder ability and heat resistance.
- 8. High reliability.
- 9.100% Lead(Pb) & Halogen-Free and RoHS compliant.







2. Dimensions



Chip Size							
Α	2.00±0.20						
В	1.25±0.20						
С	0.85±0.20						
D	0.50±0.30						

Units: mm

3. Part Numbering



A: Series

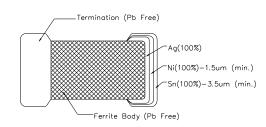
B: Dimension L x W

C: Material Lead Free Material

D: Inductance 100=10uH
E: Inductance Tolerance M=±20%

F: Packaging T=Taping and Reel, B=Bulk(Bags)

G: Rated Current

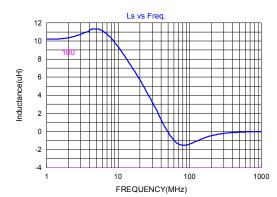


4. Specification

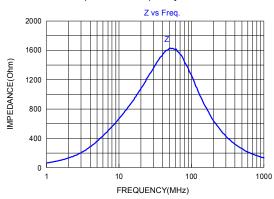
Tai-Tech	Inducta	ance(uH)	Rated Current	DCR	SRF
Part Number	Tolerance	Test Frequency (Hz)	(mA) max.	(Ω) max.	(MHz) min.
FHI2012F-100MT01	10±20%	60mV / 1M	100	0.50	30

- Rated current: based on temperature rise test
- In compliance with EIA 595

■ Inductance-Frequency Characteristics



■ Impedance-Frequency Characteristics



TAI-TECH KBM01-181000510 P3.

5. Reliability and Test Condition

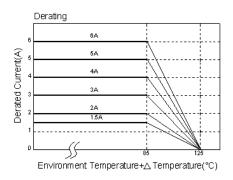
Item		Pe	erfor	mance			Tes	st Cond	dition	
Series No.	FCI	FHI		FCH	HCI					
Operating Temperature			-55+1 self-te	I05℃ emperature rise)						
Transportation Storage Temperature				l05℃ oard)		For long	_		ons, please	see the
Inductance (Ls)						Agilent4291 Agilent E4991				
Q Factor	Refer to standard	electrical charac	cteris	tics list		Agilent4: Agilent10				
DC Resistance						Agilent 4	1338			
Rated Current						DC Power Rassome ris	ted Curr		ements, the	ere will be
Temperature Rise Test		ted Current < 1A ΔT 20℃Max ted Current ≧ 1A ΔT 40℃Max							current. by digital s	urface
Life test	Appearance: no o	_	alue.			times.(II Reflow F Tempera Applied of Duration Measure for 24±2	PC/JEDI Profiles) ature: 10 current: : 1000±1 ed at roo hrs.	EC J-STD 5±2℃ rated curre 2hrs. om tempe	erature afte	er placing
Load Humidity	Q : Shall not exc	mpedance: within±15%of initial value. nductance: within±10%of initial value. ⊋ : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value					Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020D Classification Reflow Profiles) Humidity: 85±2%R.H. Temperature: 85±2°C. Duration: 1000hrs Min. with 100% rated current. Measured at room temperature after placing for 24±2 hrs.			
Thermal shock	Appearance: no of Impedance: within Inductance: within Q: Shall not excended.	n±15%of initial van±10%of initial vante	alue. ation v	/alue. shall not exceed the	specification value	Preconditioning: Run through IR reflow for 2 times. (IPC/IDEC J-STD-020D Classification Reflow Profiles) Condition for 1 cycle Step1: $-55\pm2^{\circ}$ C 30 ± 5 min. Step2: $25\pm2^{\circ}$ C ≤ 0.5 min Step3: $+105\pm2^{\circ}$ C 30 ± 5 min. Number of cycles: 500 Measured at room temperature after placing for 24 ± 2 hrs.				
Vibration	Appearance : No Impedance : with Inductance : with Q : Shall not exc RDC : within ±15	iin±15% of initial in±10% of initial eed the specifica	value ation v)	specification value	times.(II Reflow F Oscillation minutes Equipment Total Am	PC/JEDI Profiles) on Frequent: Vil ent: Vil plitude:1	uency: 10 pration che 1.52mm±1 2 hours(20		ssification
Bending	Impedance: with Inductance: with Q: Shall not exce	Appearance: No damage. Impedance: within±10% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within±15% of initial value and shall not exceed the specification value						ions: 2mm):40x	m	n
Shock	Impedance: with Inductance: with Q: Shall not exc	Appearance: No damage. Impedance: within±10% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value						Normal duration (D) (ms) 11	Wave form Half-sine	Velocity change (Vi)ft/sec 11.3
Insulation Resistance	IR>1GΩ					Chip Ind	uctor Or	ıly		
Solderability		the terminal elec	ctrode	e should be covered	with solder.	Preheat: Solder: S Solder to Flux for I	150°C,6 Sn96.5% emperatu lead free omplete	-Ag3%-Ci ure: 245±5 : Rosin. 9 ly cover th	u0.5% 5℃	on.

TAI-TECH KBM01-181000510 P4.

Item	Performance		Test Condition			
		Number of heat cycles: 1				
Resistance to Soldering	Appearance : No damage. Impedance : within±15% of initial value	Temperature (°C)	Time (s)	Temperature ramp/immersion and emersion rate		
Heat	Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	260 ±5 (solder temp)	10 ±1	25mm/s ±6 mm/s		
			Depth: complete	ly cover th	ne termination	
Terminal strength	Appearance: No damage. Impedance: within±15% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	times.(IPC/JED Reflow Profiles) Component mou >0805inch(2012 <=0805inch(201 to the side of a of shall be applied	unted on a mm):1kg 2mm):0.5 device bei I for 60 +	ng tested. This force 1 seconds. Also the dually as not to shock		

**Derating Curve

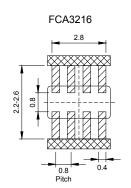
For the ferrite chip bead which withstanding current over 1.5A, as the operating temperature over $85^{\circ}\mathbb{C}$, the derating current information is necessary to consider with. For the detail derating of current, please refer to the Derated Current vs. Operating Temperature curve.



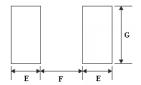
6. Soldering and Mounting

6-1. Recommended PC Board Pattern

		Land Patterns For Reflow Soldering						
Series	Type	A(mm)	B(mm)	C(mm)	D(mm)	E(mm)	F(mm)	G(mm)
	0603	0.6±0.03	0.30±0.03	0.30±0.03	0.15±0.05	0.35	0.30	0.40
FCB	1005	1.0±0.10	0.50±0.10	0.50±0.10	0.25±0.10	0.50	0.40	0.60
FCM	1608	1.6±0.15	0.80±0.15	0.80±0.15	0.30±0.20	0.80	0.85	0.95
нсв	2012	<mark>2.0±0.20</mark>	<mark>1.25±0.20</mark>	<mark>0.85±0.20</mark>	0.50±0.30	1.05	1.00	1.45
GHB	2012	2.0±0.20	1.25±0.20	1.25±0.20	0.50±0.30	1.05	1.00	1.40
FCI FHI	3216	3.2±0.20	1.60±0.20	1.10±0.20	0.50±0.30	1.05	2.20	1.80
FCH	3225	3.2±0.20	2.50±0.20	1.30±0.20	0.50±0.30	1.05	2.20	2.70
HCI	4516	4.5±0.20	1.60±0.20	1.60±0.20	0.50±0.30	1.05	3.30	1.80
1101	4532	4.5±0.20	3.20±0.20	1.50±0.20	0.50±0.30	1.05	3.30	3.40



Land
Solder Resist



PC board should be designed so that products can prevent damage from mechanical stress when warping the board.

6-2. Soldering

Mildly activated rosin fluxes are preferred. The terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools. Note.

If wave soldering is used ,there will be some risk.

Re-flow soldering temperatures below 240 degrees, there will be non-wetting risk

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6-2.1 Lead Free Solder re-flow:

Recommended temperature profiles for lead free re-flow soldering in Figure 1. (Refered to J-STD-020C)

6-2.2 Soldering Iron:

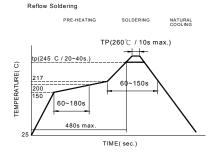
Products attachment with a soldering iron is discouraged due to the inherent process control limitations. If a soldering iron must be employed the following precautions are recommended. for Iron Soldering in Figure 2.

• 350°C tip temperature (max)

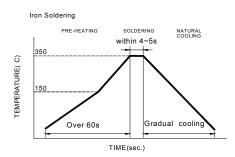
Never contact the ceramic with the iron tip

• 1.0mm tip diameter (max)

- Use a 20 watt soldering iron with tip diameter of 1.0mm
- Limit soldering time to 4~5sec.



Reflow times: 3 times max Fig.1



Iron Soldering times: 1 times max Fig.2

6-2.3 Solder Volume:

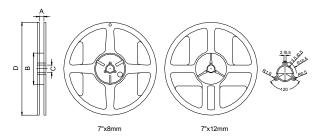
Accordingly increasing the solder volume, the mechanical stress to product is also increased. Exceeding solder volume may cause the failure of mechanical or electrical performance. Solder shall be used not to be exceed as shown in right side:

Minimum fillet height = soldering thickness + 25% product height



7. Packaging Information

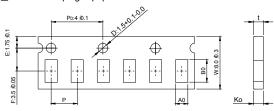
7-1. Reel Dimension

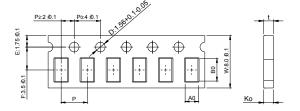


Туре	A(mm)	B(mm)	C(mm)	D(mm)		
<mark>7"x8mm</mark>	9.0±0.5	60±2	13.5±0.5	178±2		
7"x12mm	13.5±0.5	60±2	13.5±0.5	178±2		

7-2.1 Tape Dimension / 8mm

■Material of taping is paper



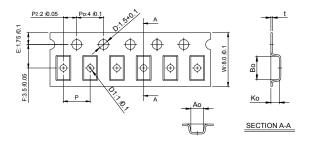


Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)	
060303	0.70±0.06	0.40±0.06	0.45max	2.0±0.05	0.45max	
100505	1.12±0.03	0.62±0.03	0.60±0.03	2.0±0.05	0.60±0.03	

Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)
160808	1.80±0.05	0.96+0.05/-0.03	0.95±0.05	4.0±0.10	0.95±0.05
<mark>201209</mark>	<mark>2.10±0.05</mark>	1.30±0.05	<mark>0.95±0.05</mark>	4.0±0.10	<mark>0.95±0.05</mark>

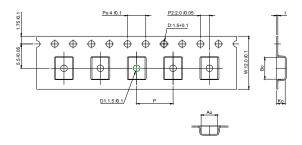
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■Material of taping is plastic



Size	Bo(mm) Ao(mm)		Ko(mm) P(mm)		t(mm)	D1(mm)
201212	2.10±0.10	1.28±0.10	1.28±0.10	4.0±0.10	0.22±0.05	1.0±0.10
321611	3.35±0.10	1.75±0.10	1.25±0.10	4.0±0.10	0.23±0.05	1.0±0.10
322513	3.42±0.10	2.77±0.10	1.55±0.10	4.0±0.10	0.22±0.05	1.0±0.10
321609	3.40±0.10	1.77±0.10	1.04±0.10	4.0±0.10	0.22±0.05	1.0±0.10

7-2.2 Tape Dimension / 12mm

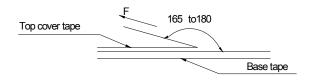


Size	Bo(mm)	Ao(mm)	Ao(mm) Ko(mm)		t(mm)	D1(mm)
451616	4.70±0.10	1.75±0.10	1.75±0.10	4.0±0.10	0.24±0.05	1.5±0.10
453215	4.70±0.10	3.45±0.10	1.60±0.10	8.0±0.10	0.24±0.05	1.5±0.10

7-3. Packaging Quantity

Chip Size	453215	451616	322513	321611	321609	201212	<mark>201209</mark>	160808	100505	060303
Chip / Reel	1000	2000	2500	3000	3000	2000	<mark>4000</mark>	4000	10000	15000
Inner box	4000	8000	12500	15000	15000	10000	<mark>20000</mark>	20000	50000	75000
Middle box	20000	40000	62500	75000	75000	50000	100000	100000	250000	375000
Carton	40000	80000	125000	150000	150000	100000	<mark>200000</mark>	200000	500000	750000

7-4. Tearing Off Force



The force for tearing off cover tape is 15 to 60 grams in the arrow direction under the following conditions.

Room Temp.	Room Humidity	Room atm	Tearing Speed
(℃)	(%)	(hPa)	mm/min
5~35	45~85	860~1060	300

Application Notice

Storage Conditions(component level)

To maintain the solder ability of terminal electrodes:

- 1. TAI-TECH products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
- 2. Temperature and humidity conditions: Less than 40°C and 60% RH.
- 3. Recommended products should be used within 12 months from the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
- 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

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CR54NP-8R5MC 70F224AI MGDQ4-00004-P MHL1ECTTP18NJ MHQ1005P10NJ MHQ1005P1N0S MHQ1005P2N4S MHQ1005P3N6S
MHQ1005P5N1S MHQ1005P8N2J PE-51506NL PE-53601NL PE-53602NL PE-53630NL PE-53824SNLT PE-92100NL PG0434.801NLT
PG0936.113NLT 9220-20 9310-16 PM06-2N7 PM06-39NJ A01TK 1206CS-471XJ HC2LP-R47-R HC2-R47-R HC3-2R2-R HCF13053R3-R 1206CS-151XG RCH664NP-140L RCH664NP-4R7M RCH8011NP-221L RCP1317NP-332L RCP1317NP-391L RCR1010NP-470M