SMD Power Inductor

TMPA0503S-Series(N)-D

| | ECN HISTORY LIST | | | | | | | |
|-----|------------------|-------------|----------|--|-------|--|--|--|
| REV | DATE | DESCRIPTION | APPROVED | CHECKED | DRAWN | | | |
| 1.0 | 17/11/03 | 新發行 | 羅宜春 | 梁周虎 | 卜文娟 | | | |
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SMD Power Inductor

1. Features

- 1. Shielded construction.
- 2. Capable of corresponding high frequency (5MHz).
- 3. Low loss realized with low DCR.
- 4. High performance (Isat) realized by metal dust core.
- 5. Ultra low buzz noise, due to composite construction.
- 6. 100% Lead(Pb)-Free and RoHS compliant.

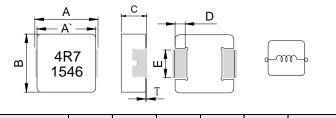
2. Applications

- 1. DC/DC converters in distributed power systems.
- 2. DC/DC converter for Field Programmable Gate Array(FPGA).
- 3. Battery powered devices.
- 4. Thin type on-board power supply module for exchanger.

5. VRM for server.

- 6. High current, low profile POL converters.
- 7. PDA/notebook/desktop/server and battery powered devices.

3. Dimensions



| Series | Α | A` | В | С | D | E | Т |
|----------|---------|---------|---------|---------|---------|---------|--------|
| TMPA0503 | 5.7±0.3 | 5.2±0.3 | 5.2±0.2 | 2.8±0.2 | 1.0±0.3 | 2.0±0.2 | 0~0.15 |
| Unit:mm | | | | | | | |

4. Part Numbering



BxC

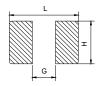
- A: Series
- B: Dimension C: Type
- D: Inductance

E: Inductance Tolerance

F: Code



Recommend PC Board Pattern



| L(mm) | G(mm) | H(mm) | | | | |
|---|-------|-------|--|--|--|--|
| 6.0 | 2.8 | 2.5 | | | | |
| Note: 1. The above PCB layout reference only. | | | | | | |

 Recommend solder paste thickness at 0.12mm and above.

TMPA0503S-Series(N)-D

5. Specification

| Part Number | Inductance L0 A(uH) ±20% | Heat Rating Current DC (A) Irms. | | Saturation Current DC (A)I sat | | DCR (mΩ)Typ | DCR (mΩ)Max |
|-------------------|--------------------------------|--|-----|--------------------------------------|-----|----------------|----------------|
| | 12070 | Тур | Max | Тур | Max | | |
| TMPA0503S-R47MN-D | 0.47 | 13.5 | 12 | 10 | 9.0 | 5.2 | 6.0 |
| TMPA0503S-R68MN-D | 0.68 | 12.5 | 11 | 9.0 | 8.0 | 7.4 | 8.5 |
| TMPA0503S-R82MN-D | 0.82 | 10 | 9.0 | 8.8 | 7.7 | 8.0 | 9.2 |
| TMPA0503S-1R0MN-D | 1.00 | 9.0 | 8.0 | 8.5 | 7.5 | 10.5 | 12 |
| TMPA0503S-1R5MN-D | 1.50 | 8.0 | 7.0 | 7.5 | 6.5 | 13.6 | 15.7 |
| TMPA0503S-2R2MN-D | 2.20 | 7.0 | 6.5 | 6.5 | 5.8 | 21.6 | 25 |
| TMPA0503S-3R3MN-D | 3.30 | 6.3 | 5.8 | 6.0 | 5.3 | 28 | 33 |
| TMPA0503S-4R7MN-D | 4.70 | 5.5 | 4.8 | 5.3 | 4.6 | 38 | 44 |
| TMPA0503S-5R6MN-D | 5.60 | 5.0 | 4.3 | 4.6 | 4.0 | 50 | 58 |
| TMPA0503S-6R8MN-D | 6.80 | 4.3 | 3.7 | 3.5 | 3.1 | 57 | 66 |
| TMPA0503S-100MN-D | 10.0 | 3.8 | 3.4 | 2.5 | 2.1 | 88 | 103 |

Note:

1. Test frequency : Ls : 100KHz /1.0V.

2. All test data referenced to $25^\circ\!\!\mathbb{C}$ ambient.

3. Testing Instrument(or equ) : L: HP4284A,CH11025,CH3302,CH1320,CH1320S LCR METER / Rdc:CH16502,Agilent33420A MICRO OHMMETER.

4. Heat Rated Current (Irms) will cause the coil temperature rise approximately $\, {\vartriangle} T$ of 40 ${^\circ} C$

5. Saturation Current (Isat) will cause L0 to drop approximately 30%.

6. The part temperature (ambient + temp rise) should not exceed 125 °C under worst case operating conditions. Circuit design, component, PCB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.

7. Special inquiries besides the above common used types can be met on your requirement.

6. Material List

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|---|--------|---------------|-----|-----|
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| NO | Items | Materials |
|--------|-------|-----------------------------------|
| 1 | Core | Alloy Powder . |
| 2 | Wire | Polyester Wire or equivalent. |
| 3 Clip | | 100% Pb free solder(Ni+SnPlating) |
| 4 | Ink | Halogen-free ketone |

7. Reliability and Test Condition

| Item | Performance | Test Condition |
|---------------------------|---|---|
| Operating temperature | -40~+125°C (Including self - temperature rise) | |
| Storage temperature | 110~+40℃,50~60%RH (Product without taping) 240~+125℃ (on board) | |
| Electrical Performance | Test | |
| Inductance | | HP4284A,CH11025,CH3302,CH1320,CH1320S LCR Meter. |
| DCR | Refer to standard electrical characteristics list. | CH16502, Agilent33420A Micro-Ohm Meter. |
| Saturation Current (Isat) | Approximately | Saturation DC Current (Isat) will cause L0 to drop △L(%) |
| Heat Rated Current (Irms) | Approximately △T40°C | Heat Rated Current (Irms) will cause the coil temperature rise △T(℃). 1.Applied the allowed DC current 2.Temperature measured by digital surface thermometer |
| Reliability Test | | |
| Life Test | | Preconditioning: Run through IR reflow for 2 times.(IPC/JEDECJ-STD-020DClassification Reflow Profiles) Temperature : 125±2°C(Inductor) Applied current : rated current Duration : 1000±12hrs Measured at room temperature after placing for 24±2 hrs. |
| Load Humidity | | Preconditioning: Run through IR reflow for 2 times.(IPC/JEDECJ-STD-020DClassification Reflow Profiles) Humidity: 85±2% R.H, Temperature : 85℃±2℃ Duration : 1000hrs Min. with 100% rated current Measured at room temperature after placing for 24±2 hrs. |
| Moisture Resistance | Appearance : No damage. Impedance : within±15% of initial value Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value | Preconditioning: Run through IR reflow for 2 times. (IPC/JEDECJ-STD-020DClassification Reflow Profiles) 1. Baked at50°C for 25hrs, measured at room temperature after placing for 4 hrs. 2. Raise temperature to $65\pm2°C$ 90-100%RH in 2.5hrs, and keep 3 hours, cool down to $25°C$ in 2.5hrs. 3. Raise temperature to $65\pm2°C$ 90-100%RH in 2.5hrs, and keep 3 hours, cool down to $25°C$ in 2.5hrs,keep at $25°C$ for 2 hrs then keep at $-10°C$ for 3 hrs 4. Keep at $25°C$ 80-100%RH for 15min and vibrate at the frequency of 10 to 55 Hz to 10 Hz, measure at room temperature after placing for 1–2 hrs. |
| Thermal shock | | Preconditioning: Run through IR reflow for 2 times. (IPC/JEDECJ-STD-020DClassification Reflow Profiles) Condition for 1 cycle Step1 : -40±2°C 30±5min Step2 : 25±2°C ≤0.5min Step3 : 125±2°C 30±5minNumber of cycles : 500 Measured at room fempraturc after placing for 24±2 hrs. |
| Vibration | | Preconditioning: Run through IR reflow for 2 times.(IPC/JEDECJ-STD-020DClassification Reflow Profiles) Oscillation Frequency: 10~2K~10Hz for 20 minutes Equipment : Vibration checker Total Amplitude:1.52mm±10% Testing Time : 12 hours(20 minutes, 12 cycles each of 3 orientations) • |

TAI-TECH

| Performance | Test Condition | | | |
|---|---|--|--|--|
| Appearance : No damage. | Shall be mounted on a FR4 substrate of the following dimensions: >=0805 inch(2012mm):40x100x1.2mm <0805 inch(2012mm):40x100x0.8mm Bending depth: >=0805 inch(2012mm):1.2mm <0805 inch(2012mm):0.8mm duration of 10 sec. | | | |
| Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not | Type Peak value (g's) Normal duration (D) (ms) Wave form Velocity change (Vi)ft/sec | | | |
| exceed the specification value | SMD 50 11 Half-sine 11.3 Lead 50 11 Half-sine 11.3 | | | |
| More than 95% of the terminal electrode should be covered with solder $^{\circ}$ | Preheat: 150℃,60sec. ∘ Solder: Sn96.5% Ag3% Cu0.5% Temperature: 245±5℃ ∘ Flux for lead free: Rosin. 9.5% ∘ Dip time: 4±1sec ∘ Depth: completely cover the termination | | | |
| | Depth: completely cover the termination Temperature(°C) Time(s) Temperature ramp/immersion and emersion rate Number of heat cycles 260 ±5 (solder temp) 10 ±1 25mm/s ±6 mm/s 1 | | | |
| Appearance : No damage. Impedance : within±15% of initial value Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value e | Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles With the component mounted on a PCB with the device to be tested, apply a force(>0805.1tg, <<0805.1cg, <<0805.1cg, <<0805.1cg, <<0805.1cg, <<0805.1cg, <<0805.1cg, <<0805.1cg, 0805.1cg, </0805.1cg, </0</td | | | |
| | Appearance : No damage. Impedance : within±15% of initial value Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value More than 95% of the terminal electrode should be covered with solder - More than 95% of the terminal electrode should be covered with solder - More than 95% of the terminal electrode should be covered with solder - More than 95% of the terminal electrode should be covered with solder - More than 95% of the terminal electrode should be covered with solder - More than 95% of the terminal electrode should be covered with solder - More than 95% of the terminal electrode should be covered with solder - More than 95% of the terminal electrode should be covered with solder - More than 95% of the terminal electrode should be covered with solder - More than 95% of the terminal electrode should be covered with solder - More than 95% of the terminal electrode should be covered with solder - More than 95% of the terminal electrode should be covered with solder - More than 95% of the terminal electrode should be covered with solder - More than 95% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not | | | |

Note : When there are questions concerning measurement result : measurement shall be made after 48 ± 2 hours of recovery under the standard condition.

8. Soldering and Mounting

(1) Soldering

Mildly activated rosin fluxes are preferred. The minimum amount of solder can lead to damage from the stresses caused by the difference in coefficients of expansion between solder, chip and substrate. TAI-TECH terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

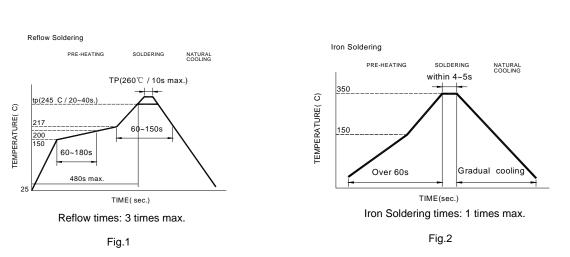
(2) Solder re-flow:

Recommended temperature profiles for re-flow soldering in Figure 1.

(3) Soldering Iron:

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.

- Preheat circuit and products to 150°C
 Never contact the ceramic with the iron tip
- 355°C tip temperature (max) • 1.0mm tip diameter (max)



9. Friendly reminder

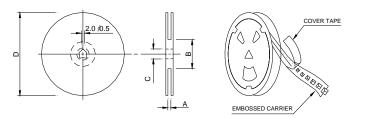
- (1) When there are questions concerning measurement result : measurement shall be made after 48 \pm 2 hours of recovery under the standard condition
- (2) This power choke coil itself does not have any protective function in abnormal condition such as overload, short-circuit and open-circuit conditions, etc. Therefore, it shall be confirmed as the end product that there is no risk of smoking, fire, dielectric withstand voltage, insulation resistance, etc. in abnormal conditions to provide protective devices and/or protection circuit in the end product.
- (3) When this power choke coil was used in a similar or new product to the original one, sometimes it might not be able to satisfy the specifications due to different condition of use.
- (4) Dielectric withstanding test with higher voltage than specific value will damage insulating material and shorten its life.
- (5) This power choke coil must not be used in wet condition by water, coffee or any liquid because insulation strength becomes very low in this condition.
- (6) Please consult our company to confirm the reliability of the process required to wash or use or exposure to a chemical solvent used in this product. PCB washing tested to MIL-STD-202 Method. Use only alcohol to wash the PCB and dry it off immediately (Marking will be washed away if using alcohol).

Use a 20 watt soldering iron with tip diameter of 1.0mm

· Limit soldering time to 4~5sec.

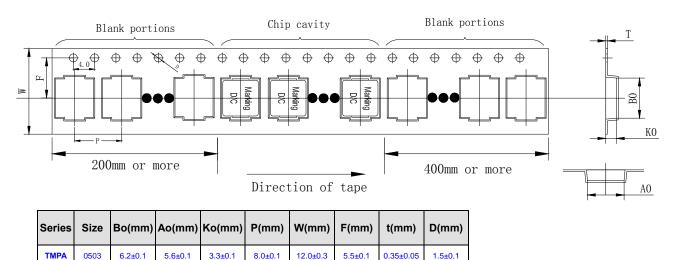
10. Packaging Information

(1) Reel Dimension



| Туре | A(mm) | B(mm) | C(mm) | D(mm) |
|----------|-----------|----------------|-------------|-------|
| 13"x12mm | 12.4+2/-0 | 100 <u>±</u> 2 | 13+0.5/-0.2 | 330 |

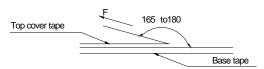
(2) Tape Dimension



(3) Packaging Quantity

| TMPA | 0503 |
|-------------|-------|
| Chip / Reel | 2000 |
| Inner box | 4000 |
| Carton | 16000 |

(4) Tearing Off Force

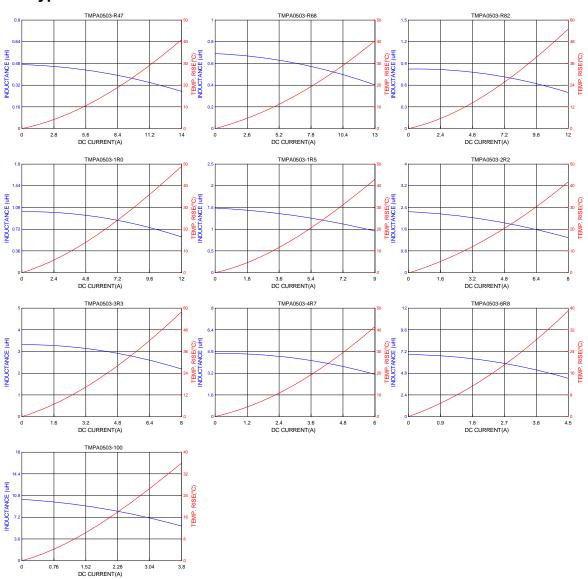


The force for tearing off cover tape is 10 to 130 grams in the arrow direction under the following conditions(referenced ANSI/EIA-481-C-2003 of 4.11 standard).

| Room Temp. Room Humidity | | Room atm | Tearing Speed |
|--------------------------|-------|----------|---------------|
| (°C) | (%) | (hPa) | mm/min |
| 5~35 | 45~85 | 860~1060 | 300 |

Application Notice

- Storage Conditions(component level)
- To maintain the solderability of terminal electrodes:
- 1. TAI-TECH products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
- 2. Temperature and humidity conditions: Less than 40 $^\circ\!\mathrm{C}$ and 60% RH.
- 3. Recommended products should be used within 12 months form the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
 A Deducts about the handled with each to quaid democra as contamination from perspiration
- 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils. 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- Bulk handling should ensure that abrasion and mechanical shock are minimized.



11. Typical Performance Curves

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