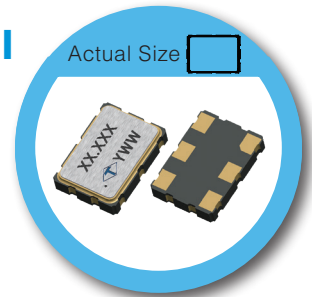


OT Type 7.0 x 5.0 mm SMD LVPECL/LVDS Crystal Oscillator

FEATURE

- Typical 7.0 x 5.0 x 1.45 mm hermetically sealed ceramic package.
- Very low jitter performance: typical 0.3 pS RMS from 12k-20MHz.
- Fundamental/3rd overtone crystal design.
- Output frequency up to 320 MHz.
- Operating temperature up to 125°C
- Tri-state enable/disable

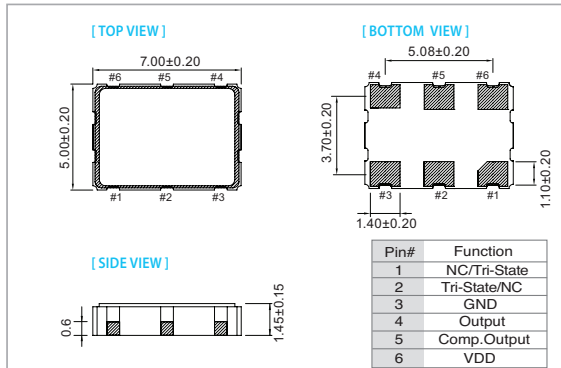


RoHS Compliant

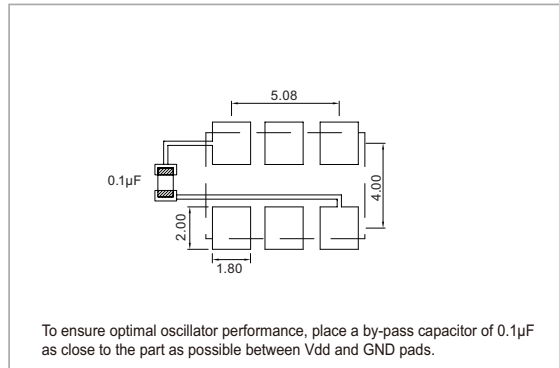
TYPICAL APPLICATION

- 10Gbit Ethernet, Fiber Channel, Storage Area Network, SONET
- Enterprise Servers, Reference clocks for ADC and DAC
- Telecom

DIMENSION (mm)



SOLDER PAD LAYOUT (mm)



ELECTRICAL SPECIFICATION

Parameter	LVPECL				LVDS				unit
	3.3 V		2.5 V		3.3 V		2.5 V		
	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	
Supply Voltage Variation (V_{DD})	VDD-5%	VDD+5%	VDD-5%	VDD+5%	VDD-5%	VDD+5%	VDD-5%	VDD+5%	V
Frequency Range	10	320	10	320	10	320	10	320	MHz
Standard Frequency	77.76, 106.25, 125, 155.52, 156.25, 187.5, 212.5, 312.5								
Supply Current	10 MHz ≤ F _o < 160 MHz		160 MHz ≤ F _o < 250 MHz		250 MHz ≤ F _o ≤ 320 MHz				
	-	75	-	75	-	50	-	50	mA
	-	100	-	100	-	50	-	50	
	-	100	-	100	-	65	-	65	
Output Level	Output High		Output Low		Output High		Output Low		
	2.275	-	1.475	-	1.6	-	0.9	-	V
	-	1.68	-	0.88	-	0.9	-	0.9	
Transition Time: Rise/Fall Time⁺	-	1.0	-	1.0	-	1.0	-	1.0	nSec
Start Time	-	10	-	10	-	10	-	10	mSec
Tri-State(Input to Pin 2 or Pin 1)									
Enable (High voltage or floating)	2.31	-	1.75	-	2.31	-	1.75	-	V
Disable (Low voltage or GND)	-	0.99	-	0.75	-	0.99	-	0.75	
RMS Phase Jitter (Integrated 12 KHz ~ 20 MHz)									
F _o < 80 MHz	-	1	-	1	-	1	-	1	pSec
80 MHz ≤ F _o < 125 MHz	-	0.5	-	0.5	-	0.5	-	0.5	
125 MHz ≤ F _o < 170 MHz	-	0.3	-	0.3	-	0.3	-	0.3	
170 MHz ≤ F _o < 200 MHz	-	0.5	-	0.5	-	0.5	-	0.5	
200 MHz ≤ F _o	-	0.3	-	0.3	-	0.3	-	0.3	
Phase Noise @ 156.25 MHz	100Hz		1 kHz		10 kHz				
	-	-100	-	-100	-	-100	-	-100	dBc/Hz
	-	-130	-	-130	-	-130	-	-130	
	-	-145	-	-145	-	-145	-	-145	
Aging (@ 25°C 1st year)	-	±3	-	±3	-	±3	-	±3	ppm
Storage Temp. Range	-55	125	-55	125	-55	125	-55	125	°C

Standard frequencies are frequencies which the crystal has been designed and does not imply a stock position.

+ Transition times are measured between 20% and 80% of V_{DD}.

FREQ. STABILITY vs. TEMP. RANGE

Temp. (°C)	ppm	
	±25	±50
-10 ~ +60	○	○
-20 ~ +70	○	○
-40 ~ +85	△	○
-40 ~ +125	×	○

* ○: Available △: Conditional X: Not available

* Inclusive of calibration @ 25 °C, operating temperature range, input voltage variation, load variation, aging (1st year), shock, and vibration

Note: not all combination of options are available. Other specifications may be available upon request.

Specifications subject to change without notice.

X-ON Electronics

Largest Supplier of Electrical and Electronic Components

Click to view similar products for [Standard Clock Oscillators](#) category:

Click to view products by [TAITIEN](#) manufacturer:

Other Similar products are found below :

[EP1400SJTSC-125.000M](#) [601137](#) [601252](#) [CSX750FBC-24.000M-UT](#) [CSX750FBC-33.333M-UT](#) [CSX750FCC-3.6864M-UT](#) [F335-12](#) [F335-25](#) [F535L-50](#) [DSC506-03FM2](#) [ASA-20.000MHZ-L-T](#) [ASA-25.000MHZ-L-T](#) [ASA-27.000MHZ-L-T](#) [ASV-20.000MHZ-LR-T](#) [ECS-2018-160-BN-TR](#) [EL13C7-H2F-125.00M](#) [MXO45HS-2C-66.6666MHZ](#) [NBXDBB017LN1TAG](#) [NBXHBA019LN1TAG](#) [SiT1602BI-22-33E-50.000000E](#) [SIT8003AC-11-33S-2.04800X](#) [SiT8256AC-23-33E-156.250000X](#) [SIT8918AA-11-33S-50.000000G](#) [SM4420TEV-40.0M-T1K](#) [SMA4306-TL-H](#) [F335-24](#) [F335-40](#) [F335-50](#) [F535L-10](#) [F535L-12](#) [F535L-16](#) [F535L-24](#) [F535L-27](#) [F535L-48](#) [PE7744DW-100.0M](#) [CSX750FBC-20.000M-UT](#) [CSX-750FBC33333000T](#) [CSX750FBC-4.000M-UT](#) [CSX750FBC-7.3728M-UT](#) [CSX750FBC-8.000M-UT](#) [CSX-750FCC14745600T](#) [CSX750FCC-16.000M-UT](#) [CSX-750FCC40000000T](#) [CSX750FCC-4.000M-UT](#) [ASA-22.000MHZ-L-T](#) [ASA2-26.000MHZ-L-T](#) [ASA-40.000MHZ-L-T](#) [ASA-48.000MHZ-L-T](#) [ASA-60.000MHZ-L-T](#) [ASF1-3.686MHZ-N-K-S](#)