

Lead: Pure tin plated, lead free, solderable per MIL-STD-202, Method 208 guaranteed

♦ High temperature soldering guaranteed: 260°C/10s /.375", (9.5mm) lead lengths at 5 lbs,

♦ Polarity: Color band denotes cathode end

(2.3kg) tension

♦ Weight: 0.34 grams

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### BA157 - BA159 t Recovery Rectifiers

1.0AMP Fast Recovery Rectifiers



**Maximum Ratings and Electrical Characteristics** Rating at 25  $^{\circ}$ C ambient temperature unless otherwise specified. Single phase, half wave, 60 Hz, resistive or inductive load. For capacitive load, derate current by 20%

Type Number	Symbol	BA157	BA158	BA159	Units
Maximum Recurrent Peak Reverse Voltage	V <sub>RRM</sub>	400	600	1000	V
Maximum RMS Voltage	V <sub>RMS</sub>	280	420	700	V
Maximum DC Blocking Voltage	V <sub>DC</sub>	400	600	1000	V
Maximum Average Forward Rectified Current .375 (9.5mm) Lead Length @ $T_A$ =45 $^\circ$ C	I <sub>F(AV)</sub>	1			A
Peak Forward Surge Current, 8.3 ms Single Half Sine- wave Superimposed on Rated Load (JEDEC method)	I <sub>FSM</sub>	30			A
Maximum Instantaneous Forward Voltage (Note 1) @ 1 A	V <sub>F</sub>	1.2			V
Maximum DC Reverse Current at @ T <sub>A</sub> =25 °C		5		uA	
Rated DC Blocking Voltage @ $T_A=125$ °C	IR	150		uA	
Maximum Reverse Recovery Time (Note 2)	Trr	1	50	250	nS
Typical Junction Capacitance (Note 3)	Cj	10		pF	
Typical Thermal Resistance (Note 4)	R <sub>θjA</sub>	65		°C/W	
Operating Temperature Range	TJ	- 65 to + 150		OO	
Storage Temperature Range	T <sub>STG</sub>	- 65 to + 150			°C

Note 1: Pulse Test with PW=300 usec, 1% Duty Cycle

Note 2: Reverse Recovery Test Conditions:  $I_{\rm F} {=} 0.5 \text{A}, \, I_{\rm R} {=} 1.0 \text{A}, \, I_{\rm RR} {=} 0.25 \text{A}$ 

Note 3: Measured at 1 MHz and Applied Reverse Voltage of 4.0V D.C.

Note 4: Mount on Cu-Pad Size 5mm x 5mm on PCB



## RATINGS AND CHARACTERISTIC CURVES (BA157 THRU BA159)



FIG. 2- TYPICAL REVERSE CHARACTERISTICS PER LEG 1000 INSTANTANEOUS REVERSE CURRENT (uA) 100 TA=125 10 TA=75℃ 1 TA=25℃ 0.1 0 20 40 60 80 100 120 140 PERCENT OF RATED PEAK REVERSE VOLTAGE (%)



FIG. 4- TYPICAL JUNCTION CAPACITANCE



FIG. 5- TYPICAL FORWARD CHARACTERISRICS



#### FIG.6- REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM



Version:C10

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