

# 1A, 50V - 600V Surface Mount Super Fast Rectifiers

#### **FEATURES**

- Glass passivated junction chip
- Ideal for automated placement
- Low profile package
- Low power loss, high efficiency
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition

### MECHANICAL DATA

### Case: Sub SMA

Molding compound, UL flammability classification rating 94V-0 Moisture sensitivity level: level 1, per J-STD-020 Part No. with suffix "H" means AEC-Q101 qualified Packing code with suffix "G" means green compound (halogen-free) **Terminal:** Matte tin plated leads, solderable per JESD22-B102 Meet JESD 201 class 2 whisker test **Polarity:** Indicated by cathode band **Weight:** 0.019 g (approximately)







Sub SMA

		ES	ES	ES	ES	ES	ES	ES	ES	UNIT
PARAMETER	SYMBOL	1AL	1BL	1CL	1DL	1FL	1GL	1HL	1JL	
Marking code		EAL	EBL	ECL	EDL	EFL	EGL	EHL	EJL	
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	50	100	150	200	300	400	500	600	V
Maximum RMS voltage	V <sub>RMS</sub>	35	70	105	140	210	280	350	420	V
Maximum DC blocking voltage	V <sub>DC</sub>	50	100	150	200	300	400	500	600	V
Maximum average forward rectified current	I <sub>F(AV)</sub>	1			А					
Peak forward surge current, 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	30				A				
Maximum instantaneous forward voltage (Note 1) @ 1 A	V <sub>F</sub>	0.95 1.3 1.7		.7	V					
Maximum reverse current @ rated $V_R$ T <sub>J</sub> =25°C T <sub>J</sub> =125°C	I <sub>R</sub>	5 100		μA						
Typical junction capacitance (Note 2)	CJ	10 8			pF					
Maximum reverse recovery time (Note 3)	t <sub>rr</sub>	35		ns						
Typical thermal resistance	R <sub>θJL</sub> R <sub>θJA</sub>	35 85		°C/W						
Operating junction temperature range	TJ	- 55 to +150						°C		
Storage temperature range	T <sub>STG</sub>	- 55 to +150					°C			

Note 1: Pulse test with PW=300µs, 1% duty cycle

Note 2: Measured at 1 MHz and Applied  $V_R$ =4.0 Volts.

Note 3: Reverse Recovery Test Conditions:  $I_F$ =0.5A,  $I_R$ =1.0A,  $I_{RR}$ =0.25A



### Taiwan Semiconductor

ORDERING INFORMATION						
PART NO.	PART NO.	PACKING CODE	PACKING CODE	PACKAGE	PACKING	
	SUFFIX		SUFFIX			
		RU	G	Sub SMA	1,800 / 7" Plastic reel (8mm tape)	
		RV		Sub SMA	3,000 / 7" Plastic reel (8mm tape)	
	ES1xL	RT		Sub SMA	7,500 / 13" Paper reel (8mm tape)	
		MT		Sub SMA	7,500 / 13" Plastic reel (8mm tape)	
		RQ		Sub SMA	10,000 / 13" Paper reel (8mm tape)	
ES1xL		MQ		Sub SMA	10,000 / 13" Plastic reel (8mm tape)	
(Note 1)	п	R3		Sub SMA	1,800 / 7" Plastic reel (12mm tape)	
		RF		Sub SMA	3,000 / 7" Plastic reel (12mm tape)	
		R2		Sub SMA	7,500 / 13" Paper reel (12mm tape)	
		M2		Sub SMA	7,500 / 13" Plastic reel (12mm tape)	
		RH		Sub SMA	10,000 / 13" Paper reel (12mm tape)	
		MH		Sub SMA	10,000 / 13" Plastic reel (12mm tape)	

Note 1: "x" defines voltage from 50V (ES1AL) to 600V (ES1JL)

EXAMPLE						
PREFERRED P/N	PART NO.	PART NO.	PACKING CODE	PACKING CODE	DESCRIPTION	
		SUFFIX		SUFFIX		
ES1JLHRUG	ES1JL	Н	RU	G	AEC-Q101 qualified Green compound	

#### RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub>=25°C unless otherwise noted)

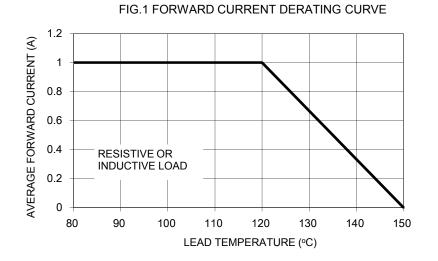
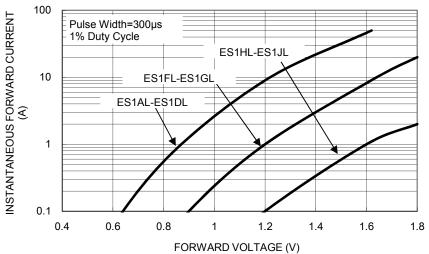
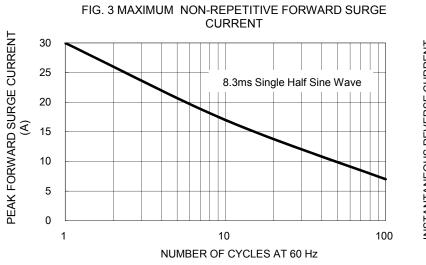
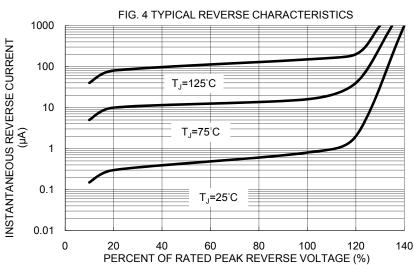


FIG. 2 TYPICAL FORWARD CHARACTERISTICS









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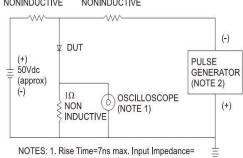
12

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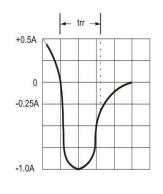
#### FIG. 5 TYPICAL JUNCTION CAPACITANCE

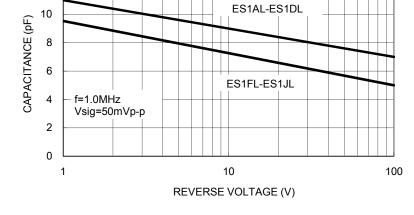


#### 50Ω 10Ω NONINDUCTIVE 10Ω



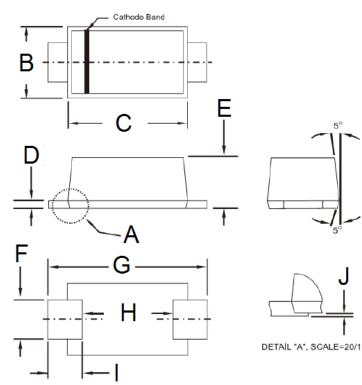
NOTES: 1. Rise Time=7ns max. Input Impedance= 1 megohm 22pf 2. Rise Time=10ns max. Sourse Impedance= 50 ohms





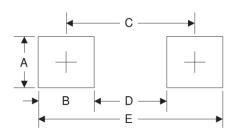
## PACKAGE OUTLINE DIMENSIONS

Sub SMA



DIM.	Unit	(mm)	Unit (inch)		
Dilvi.	Min Max		Min	Max	
В	1.70	1.90	0.067	0.075	
С	2.70	2.90	0.106	0.114	
D	0.16	0.30	0.006	0.012	
E	1.23	1.43	0.048	0.056	
F	0.80	1.20	0.031	0.047	
G	3.40	3.80	0.134	0.150	
Н	2.45	2.60	0.096	0.102	
I	0.35	0.85	0.014	0.033	
J	0.00	0.10	0.000	0.004	

#### SUGGESTED PAD LAYOUT



P/N

YW

G

F

Symbol	Unit (mm)	Unit (inch)
A	1.4	0.055
В	1.2	0.047
С	3.1	0.122
D	1.9	0.075
E	4.3	0.169

#### **MARKING DIAGRAM**



= Marking Code

= Green compound Code

= Date Code

= Factory Code

Document Number: DS\_D1410026

Version: K15



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