# HS1DFS - HS1MFS

Taiwan Semiconductor

# 1A, 200V-1000V High Efficient Surface Mount Rectifiers

## FEATURES

- Glass passivated junction chip
- Ideal for automated placement
- Low power loss, high efficiency
- Fast switching for high efficiency
- Low profile package
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21

## APPLICATIONS

- Freewheeling application
- Switching mode converters and inverters, computer and telecommunication.

## MECHANICAL DATA

- Case: SOD-128
- Molding compound meets UL 94V-0 flammability rating
- Moisture sensitivity level: level 1, per J-STD-020
- Terminal: Pure tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- Polarity: As marked
- Weight: 0.028 g (approximately)

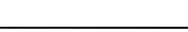
KEY PARAMETERS			
PARAMETER	VALUE	UNIT	
I <sub>F(AV)</sub>	1	А	
V <sub>RRM</sub>	200 - 1000	V	
I <sub>FSM</sub>	35	А	
T <sub>J MAX</sub>	150	°C	
Package	SOD-128		
Configuration	Single Die		





SOD-128

ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25°C unless otherwise noted)							
PARAMETER	SYMBOL	HS1DFS	HS1GFS	HS1JFS	HS1KFS	HS1MFS	UNIT
Marking code on the device		HS1DFS	HS1GFS	HS1JFS	HS1KFS	HS1MFS	
Repetitive peak reverse voltage	$V_{\text{RRM}}$	200	400	600	800	1000	V
Reverse voltage, total rms value	V <sub>R(RMS)</sub>	140	280	420	560	700	V
Forward current	$I_{F(AV)}$			1			А
Surge peak forward current, single half sine-wave $A = 25^{\circ}C$				35			А
superimposed on rated load per diode $1.0$ ms at T <sub>A</sub> = 25°C	IFSM			90			А
Junction temperature	TJ			-55 to +150	0		°C
Storage temperature	T <sub>STG</sub>			-55 to +150	0		°C







THERMAL PERFORMANCE				
PARAMETER	SYMBOL	ТҮР	UNIT	
Junction-to-lead thermal resistance	R <sub>ejl</sub>	29	°C/W	
Junction-to-ambient thermal resistance	R <sub>ejA</sub>	51	°C/W	
Junction-to-case thermal resistance	R <sub>eJC</sub>	22	°C/W	

Thermal Performance Note: Units mounted on PCB (5mm x 5mm Cu pad test board)

PARAMETER		CONDITIONS	SYMBOL	ТҮР	MAX	UNIT
	HS1DFS	I <sub>F</sub> = 0.5A, T <sub>J</sub> = 25°C		0.80	-	V
		I <sub>F</sub> = 1.0A, T <sub>J</sub> = 25°C	-	0.85	1.00	V
		I <sub>F</sub> = 0.5A, T <sub>J</sub> = 125°C		0.65	-	V
		I <sub>F</sub> = 1.0A, T <sub>J</sub> = 125°C		0.71	0.80	V
		I <sub>F</sub> = 0.5A, T <sub>J</sub> = 25°C		0.84	-	V
	HS1GFS	I <sub>F</sub> = 1.0A, T <sub>J</sub> = 25°C		0.91	1.30	V
		I <sub>F</sub> = 0.5A, T <sub>J</sub> = 125°C		0.68	-	V
Forward voltage per diode <sup>(1)</sup>		I <sub>F</sub> = 1.0A, T <sub>J</sub> = 125°C	V	0.76	0.86	V
-orward voltage per diode		I <sub>F</sub> = 0.5A, T <sub>J</sub> = 25°C	V <sub>F</sub>	0.92	-	V
		I <sub>F</sub> = 1.0A, T <sub>J</sub> = 25°C		1.02	1.70	V
	HS1JFS	I <sub>F</sub> = 0.5A, T <sub>J</sub> = 125°C		0.73	-	V
		I <sub>F</sub> = 1.0A, T <sub>J</sub> = 125°C		0.83	1.02	V
	HS1KFS HS1MFS	I <sub>F</sub> = 0.5A, T <sub>J</sub> = 25°C		1.32	-	V
		I <sub>F</sub> = 1.0A, T <sub>J</sub> = 25°C		1.49	1.70	V
		I <sub>F</sub> = 0.5A, T <sub>J</sub> = 125°C		0.98	-	V
		I <sub>F</sub> = 1.0A, T <sub>J</sub> = 125°C		1.16	1.39	V
Reverse current @ rated V <sub>R</sub> per di	odo <sup>(2)</sup>	T <sub>J</sub> = 25°C		-	1	μA
Reverse current @ rated v <sub>R</sub> per di	ode	T <sub>J</sub> = 125°C	I <sub>R</sub>	-	35	μA
	HS1DFS HS1GFS		t <sub>rr</sub>	-	50	ns
Reverse recovery time	HS1JFS HS1KFS HS1MFS	─ I <sub>F</sub> =0.5A,I <sub>R</sub> =1.0A, Irr=0.25A		-	75	ns
Junction capacitance per diode	HS1DFS		CJ	20	-	pF
	HS1GFS			17	-	pF
	HS1JFS	1 MHz, V <sub>R</sub> =4.0V		13	-	pF
	HS1KFS HS1MFS			8	-	pF

### Notes:

(1) Pulse test with PW=0.3 ms

(2) Pulse test with PW=30 ms



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## ORDERING INFORMATION

ORDERING CODE <sup>(1)</sup>	PACKAGE	PACKING
HS1xFS M3G	SOD-128	3,500 / 7" reel
HS1xFS M2G	SOD-128	14,000 / 13" reel

Notes:

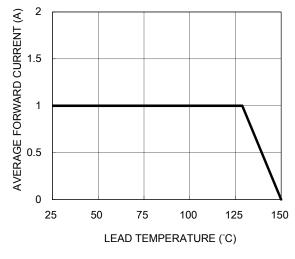
(1) "x" defines voltage from 200V(HS1DFS) to 1000V(HS1MFS)



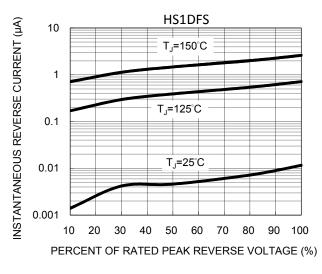
## **CHARACTERISTICS CURVES**

(T<sub>A</sub> = 25°C unless otherwise noted)

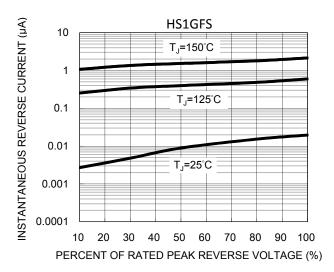
#### Fig.1 Forward Current Derating Curve



#### **Fig.3 Typical Reverse Characteristics**



**Fig.5 Typical Reverse Characteristics** 



100 HS1DFS HS1JFS HS1JFS HS1K/MFS HS1(K)MFS HS1(K

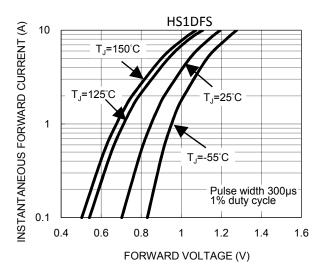
#### **Fig.2 Typical Junction Capacitance**

HS1DFS - HS1MFS

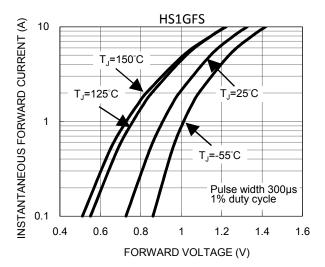
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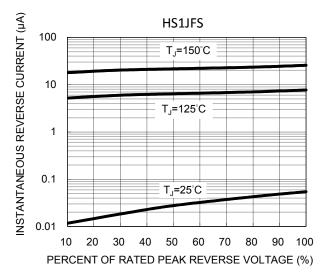


**Fig.6 Typical Forward Characteristics** 

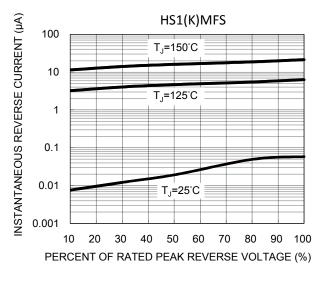


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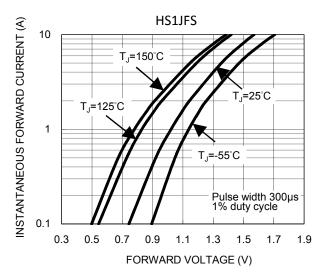
#### **Fig.7 Typical Reverse Characteristics**



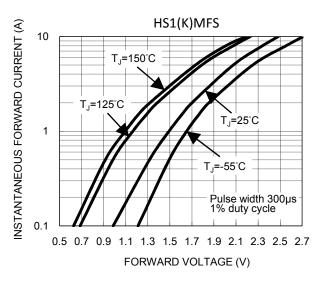
**Fig.9 Typical Reverse Characteristics** 



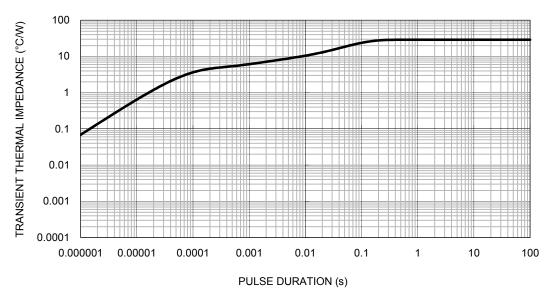
#### **Fig.8 Typical Forward Characteristics**



#### **Fig.10 Typical Forward Characteristics**





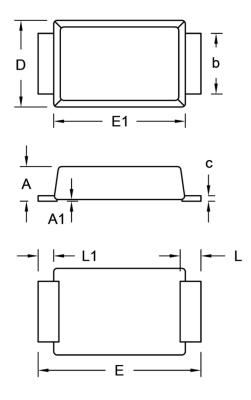




## **PACKAGE OUTLINE DIMENSIONS**

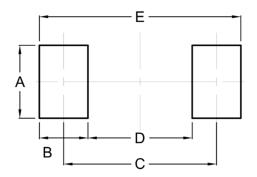
**5** TAIWAN SEMICONDUCTOR

SOD-128



DIM. Unit (r		(mm)	mm) Unit	
	Min.	Max.	Min.	Max.
A	0.90	1.10	0.035	0.043
A1	0.00	0.10	0.000	0.004
b	1.60	1.90	0.063	0.075
с	0.10	0.22	0.004	0.009
D	2.30	2.70	0.091	0.106
E	4.40	5.00	0.173	0.197
E1	3.60	4.00	0.142	0.157
L	0.40	0.80	0.016	0.031
L1	0.30	0.60	0.012	0.024

## SUGGESTED PAD LAYOUT



Symbol	Unit (mm)	Unit (inch)
A	2.10	0.083
В	1.40	0.055
С	4.40	0.173
D	3.00	0.118
E	5.80	0.228

# MARKING DIAGRAM



P/N	= Marking Code
YW	= Date Code
F	= Factory Code



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