

3A, 50V - 1000V High Efficient Surface Mount Rectifier

FEATURES

- Glass passivated junction chip
- Ideal for automated placement
- Low forward voltage drop
- Low profile package
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21

APPLICATIONS

- High frequency rectification
- Freewheeling application
- Switching mode converters and inverters in computer, automotive and telecommunication

MECHANICAL DATA

- Case: DO-214AB (SMC)
- Molding compound meets UL 94V-0 flammability rating
- Part no. with suffix "H" means AEC-Q101 qualified
- Packing code with suffix "G" means green compound (halogen-free)
- Moisture sensitivity level: level 1, per J-STD-020
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- Polarity: As marked
- Weight: 0.21 g (approximately)

KEY PARAMETERS						
PARAMETER	VALUE	UNIT				
I _{F(AV)}	3	А				
V _{RRM}	50 - 1000	V				
I _{FSM}	150	А				
T _{J MAX}	150	°C				
Package	DO-214AB (SMC)					
Configuration	Single die					





DO-214AB (SMC)

ABSOLUTE MAXIMUM RATINGS (T _A = 25°C unless otherwise noted)										
PARAMETER	SYMBOL	HS3A	HS3B	HS3D	HS3F	HS3G	HS3J	HS3K	HS3M	UNIT
Marking code on the device		HS3A	HS3B	HS3D	HS3F	HS3G	HS3J	HS3K	HS3M	
Repetitive peak reverse voltage	V _{RRM}	50	100	200	300	400	600	800	1000	V
Reverse voltage, total rms value	V _{R(RMS)}	35	70	140	210	280	420	560	700	V
Maximum DC blocking voltage	V_{DC}	50	100	200	300	400	600	800	1000	
Forward current	I _{F(AV)}				;	3				А
Surge peak forward current, 8.3 ms single half sine-wave uperimposed on rated load per diode	I _{FSM}				1:	50				A
Junction temperature	T_J	T _J - 55 to +150		°C						
Storage temperature	T _{STG}				- 55 to	o +150				°C



THERMAL PERFORMANCE			
PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-ambient thermal resistance	R _{eja}	60	°C/W

ELECTRICAL SPECIFICATIONS (T _A = 25°C unless otherwise noted)						
PARAMETER		CONDITIONS	SYMBOL	TYP.	MAX.	UNIT
	HS3A HS3B HS3D HS3F			-	1.0	V
Forward voltage per diode ⁽¹⁾	HS3G	I _F = 3A, T _J = 25°C	V _F	-	1.3	V
	HS3J HS3K HS3M			-	1.7	V
	(2)	$T_J = 25^{\circ}C$		-	10	μA
Reverse current @ rated V_R per	diode (=/	T _J = 125°C	- I _R	-	250	μA
Junction capacitance	HS3A HS3B HS3D HS3F HS3G	1 MHz, V _R =4.0V	CJ	80	-	pF
	HS3J HS3K HS3M			50	-	pF
Reverse recovery time	HS3A HS3B HS3D HS3F HS3G	I _F =0.5A , I _R =1.0A I _{RR} =0.25A	t _{rr}	-	50	ns
	HS3J HS3K HS3M			-	75	ns

Notes:

1. Pulse test with PW=0.3 ms

2. Pulse test with PW=30 ms



ORDERING INFORMATION						
PART NO.	PART NO. SUFFIX	PACKING CODE	PACKING CODE SUFFIX	PACKAGE	PACKING	
		R7		SMC	850 / 7" Plastic reel	
		R6	G	SMC	3,000 / 13" Paper reel	
HS3x (Note 1)	н	M6		SMC	3,000 / 13" Plastic reel	
		V7		Matrix SMC	850 / 7" Plastic reel	
		V6		Matrix SMC	3,000 / 13" Plastic reel	

Note :

1. "x" defines voltage from 50V (HS3A) to 1000V (HS3M)

EXAMPLE						
EXAMPLE P/N	PART NO.	PART NO. SUFFIX	PACKING CODE	PACKING CODE SUFFIX	DESCRIPTION	
HS3AHR7G	HS3A	Н	R7	G	AEC-Q101 qualified Green compound	



CHARACTERISTICS CURVES

(T_A = 25°C unless otherwise noted)

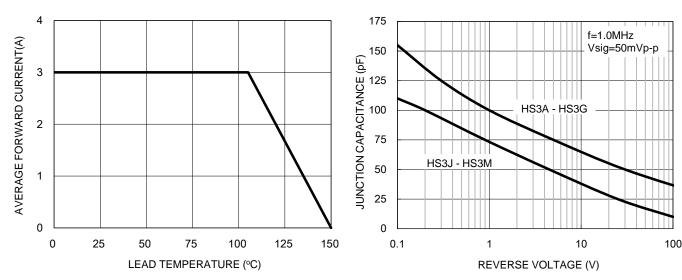


Fig.1 Forward Current Derating Curve

Fig.2 Typical Junction Capacitance

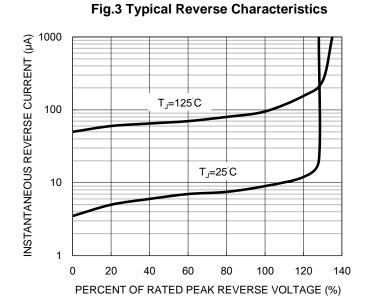
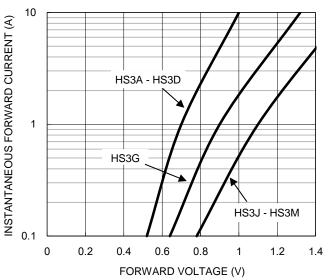


Fig.4 Typical Forward Characteristics





CHARACTERISTICS CURVES

(T_A = 25°C unless otherwise noted)

Fig.5 Maximum Non-repetitive Forward Surge Current

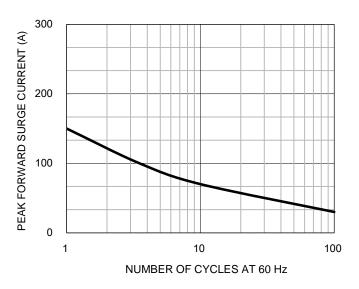
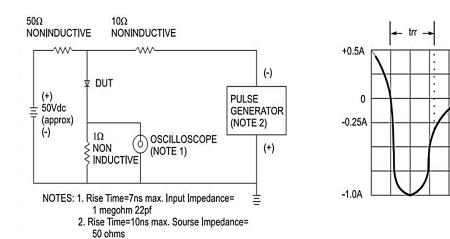


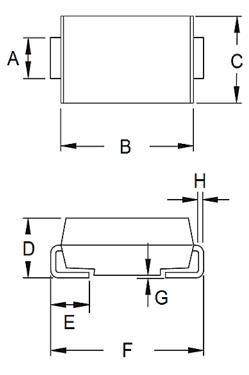
Fig.6 Reverse Recovery Time Characteristic And Test Circuit Diagram





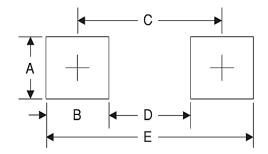
PACKAGE OUTLINE DIMENSIONS

DO-214AB (SMC)



	DIM. Unit (mm)			(inch)	
DIN.	Min.	Max.	Min.	Max.	
А	2.90	3.20	0.114	0.126	
В	6.60	7.11	0.260	0.280	
С	5.59	6.22	0.220	0.245	
D	2.00	2.62	0.079	0.103	
E	1.00	1.60	0.039	0.063	
F	7.75	8.13	0.305	0.320	
G	0.10	0.20	0.004	0.008	
Н	0.15	0.31	0.006	0.012	

SUGGESTED PAD LAYOUT



Symbol	Unit (mm)	Unit (inch)
А	3.30	0.130
В	2.50	0.098
С	6.80	0.268
D	4.40	0.173
E	9.40	0.370

MARKING DIAGRAM



- P/N =Marking Code
- G =Green Compound
- YW =Date Code
- F =Factory Code



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