



1A, 50V - 1000V Glass Passivated High Efficient Rectifier

FEATURES

- Glass passivated chip junction
- High current capability
- High reliability
- High surge current capability
- High efficiency, Low V_F
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21

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- High frequency rectification
- · Freewheeling application
- Switching mode converters and inverters in computer and telecommunication.

MECHANICAL DATA

- Case: TS-1
- Molding compound meets UL 94V-0 flammability rating
- Packing code with suffix "G" means green compound (halogen-free)
- Terminal: Pure tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 1A whisker test
- Polarity: As marked
- Weight: 0.2 g (approximately)

KEY PARAMETERS						
PARAMETER	VALUE	TINU				
I _{F(AV)}	1	Α				
V_{RRM}	50 - 1000	٧				
I _{FSM}	30	Α				
T_{JMAX}	150	°C				
Package	TS-1					
Configuration	Single Die)				





TS-1

PARAMETER	SYMBOL	HT	HT	HT	HT	HT	HT	HT	HT	UNIT
PARAMETER	STWBOL	11G-K	12G-K	13G-K	14G-K	15G-K	16G-K	17G-K	18G-K	UNII
Marking code on the device		HT11G	HT12G	HT13G	HT14G	HT15G	HT16G	HT17G	HT18G	
Repetitive peak reverse voltage	V_{RRM}	50	100	200	300	400	600	800	1000	V
Reverse voltage, total rms value	$V_{R(RMS)}$	35	70	140	210	280	420	560	700	V
Forward current	$I_{F(AV)}$		1				Α			
Surge peak forward current, 8.3 ms single half sine-wave superimposed on rated load per diode	I _{FSM}	30					А			
Junction temperature	T_J	- 55 to +150					°C			
Storage temperature	T _{STG}		- 55 to +150					°C		



THERMAL PERFORMANCE						
PARAMETER	SYMBOL	LIMIT	UNIT			
Junction-to-ambient thermal resistance	$R_{\Theta JA}$	95	°C/W			

PARAMETER		CONDITIONS	SYMBOL	TYP	MAX	UNIT
(1)	HT11G-K HT12G-K HT13G-K HT14G-K			-	1.0	V
Forward voltage per diode (1)	HT15G-K	$I_F = 1A, T_J = 25^{\circ}C$	V_{F}	-	1.30	V
	HT16G-K HT17G-K HT18G-K			-	1.70	V
(2)		T _J = 25°C		-	5	μΑ
Reverse current @ rated V_R per	diode (=)	T _J = 125°C	I _R	I _R -		μA
Junction capacitance	HT11G-K HT12G-K HT13G-K HT14G-K HT15G-K	1 MHz, V _R =4.0V	CJ	15	-	pF
	HT15G-K HT16G-K HT17G-K HT18G-K			10	-	pF
Reverse recovery time	HT11G-K HT12G-K HT13G-K HT14G-K HT15G-K	I _F =0.5A , I _R =1.0A I _{RR} =0.25A	t _{rr}	-	50	ns
	HT16G-K HT17G-K HT18G-K	1 100		-	75	ns

Notes:

- 1. Pulse test with PW=0.3 ms
- 2. Pulse test with PW=30 ms



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ORDERING	RDERING INFORMATION								
PART NO.	PACKING CODE	PACKING CODE SUFFIX	PACKAGE	PACKING					
	A0	G	TS-1	3,000 / Ammo box (52mm taping)					
HT1XG-K	A1		TS-1	3,000 / Ammo box (26mm taping)					
(Note 1, 2)	R0		TS-1	5,000 / 13" Paper reel					
	В0		TS-1	1,000 / Bulk packing					

Notes:

- 1. "x" defines voltage from 50V (HT11G-K) to 1000V (HT18G-K)
- 2. Whole series with green compound (halogen-free)

EXAMPLE P/N								
EXAMPLE P/N	PART NO.	PACKING CODE	PACKING CODE SUFFIX	DESCRIPTION				
HT11G-K A0G	HT11G-K	A0	G	Green compound				



CHARACTERISTICS CURVES

(T_A = 25°C unless otherwise noted)

Fig.1 Forward Current Derating Curve

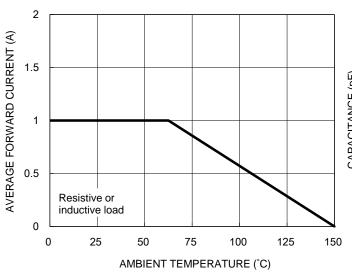


Fig.2 Typical Junction Capacitance

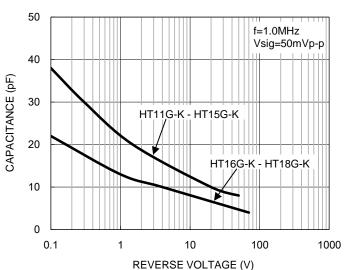


Fig.3 Typical Reverse Characteristics

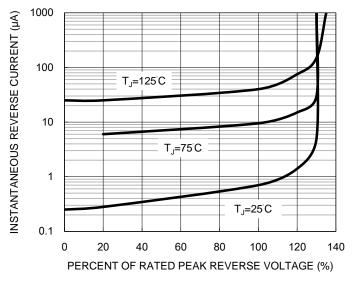
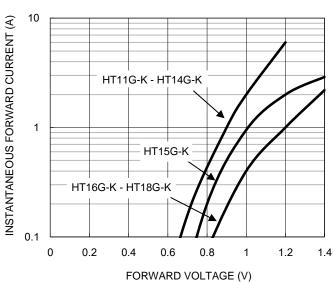


Fig.4 Typical Forward Characteristics





CHARACTERISTICS CURVES

(T_A = 25°C unless otherwise noted)

Fig.5 Maximum Non-repetitive Forward Surge Current

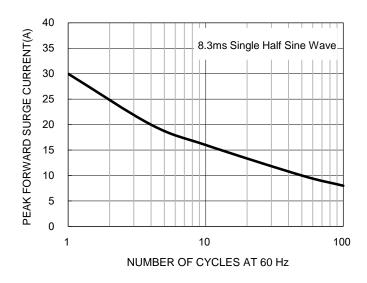
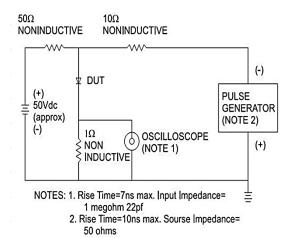
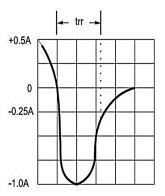


Fig.6 Reverse Recovery Time Characteristic And Test Circuit Diagram

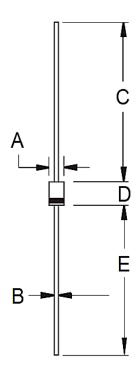






PACKAGE OUTLINE DIMENSIONS

TS-1



DIM.	Unit (ı	nm)	Unit ((inch)
DIIVI.	Min	Max	Min	Max
Α	2.00	2.70	0.079	0.106
В	0.53	0.64	0.021	0.025
С	25.40	-	1.000	-
D	3.00	3.30	0.118	0.130
E	25.40	-	1.000	-

MARKING DIAGRAM



P/N = Marking Code G = Green Compound YW = Date Code

YW = Date Code F = Factory Code

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