# MUR105S - MUR160S

Taiwan Semiconductor

# 1A, 50V - 600V Ultra Fast Surface Mount Rectifier

### **FEATURES**

TAIWAN

- Glass passivated chip junction
- Ideal for automated placement
- Ultra Fast recovery time for high efficiency
- Low forward voltage, low power loss

IICONDUCTOR

- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21 •

# **APPLICATIONS**

- Switching mode power supply (SMPS)
- Adapters •
- Lighting application •
- Converter •

# **MECHANICAL DATA**

- Case: DO-214AA (SMB)
- Molding compound meets UL 94V-0 flammability rating ٠
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- Polarity: Indicated by cathode band
- Weight: 0.090g (approximately)

KEY PARAMETERS				
PARAMETER	VALUE	UNIT		
I <sub>F</sub>	1	А		
V <sub>RRM</sub>	50 - 600	V		
I <sub>FSM</sub>	40, 35	А		
T <sub>J MAX</sub>	175	°C		
Package	DO-214AA (SMB)			
Configuration	Single die			

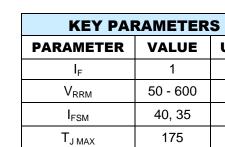




DO-214AA (SMB)



ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25°C unless otherwise noted)								
PARAMETER	SYMBOL	MUR 105S	MUR 110S	MUR 115S	MUR 120S	MUR 140S	MUR 160S	UNIT
Marking code on the device		MUR 105S	MUR 110S	MUR 115S	MUR 120S	MUR 140S	MUR 160S	
Repetitive peak reverse voltage	V <sub>RRM</sub>	50	100	150	200	400	600	V
Reverse voltage, total rms value	V <sub>R(RMS)</sub>	35	70	105	140	280	420	V
Forward current	I <sub>F</sub>	1		Α				
Surge peak forward current, 8.3ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	40		3	5	A		
Junction temperature	TJ	- 55 to +175			°C			
Storage temperature	T <sub>STG</sub>	- 55 to +175			°C			





THERMAL PERFORMANCE				
PARAMETER	SYMBOL	ТҮР	UNIT	
Junction-to-lead thermal resistance	$R_{\Theta JL}$	17	°C/W	

ELECTRICAL SPECIFICATIONS (TA = 25°C unless otherwise noted)							
PARAMETER		CONDITIONS	SYMBOL	ТҮР	MAX	UNIT	
	MUR105S MUR110S MUR115S MUR120S	I <sub>F</sub> = 1A, T <sub>J</sub> = 25°C		-	0.875	V	
Forward voltage <sup>(1)</sup>	MUR140S MUR160S		N	-	1.250	V	
Porward voltage	MUR105S MUR110S MUR115S MUR120S	I <sub>F</sub> = 1A, T <sub>J</sub> = 150°C	V <sub>F</sub>	-	0.710	V	
	MUR140S MUR160S			-	1.050	V	
	MUR105S MUR110S MUR115S MUR120S	T <sub>J</sub> = 25°C	- I <sub>R</sub>	2	μΑ		
	MUR140S MUR160S			-	5	μA	
Reverse current @ rated V <sub>R</sub> <sup>(2)</sup>	MUR105S MUR110S MUR115S MUR120S	T <sub>J</sub> = 150°C		IR	-	50	μΑ
	MUR140S MUR160S			-	150	μA	
Reverse recovery time	MUR105S MUR110S MUR115S MUR120S	$I_F = 0.5A, I_R = 1.0A$ $I_{rr} = 0.25A$	t <sub>rr</sub>	-	25	ns	
	MUR140S MUR160S			-	50	ns	

#### Notes:

Pulse test with PW = 0.3ms Pulse test with PW = 30ms



# ORDERING INFORMATION

ORDERING CODE <sup>(1)</sup>	PACKAGE	PACKING		
MUR1xS	DO-214AA (SMB)	3,000 / Tape & Reel		

Notes:

"x" defines voltage from 50V(MUR105S) to 600V(MUR160S)



# **CHARACTERISTICS CURVES**

(T<sub>A</sub> = 25°C unless otherwise noted)

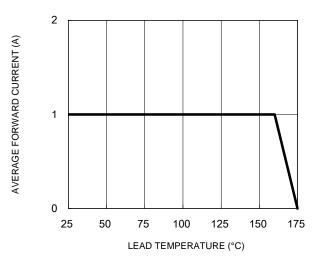
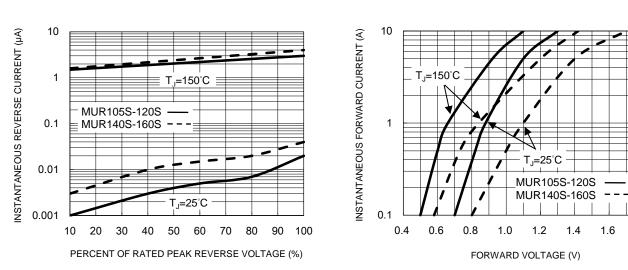


Fig.1 Forward Current Derating Curve

#### Fig.3 Typical Reverse Characteristics



#### Fig.5 Maximum Non-Repetitive Forward Surge Current

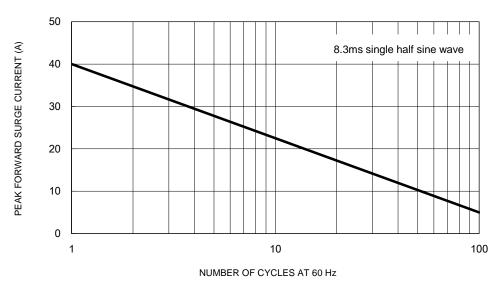


Fig.2 Typical Junction Capacitance

MUR105S-120S

12

REVERSE VOLTAGE (V)

**Fig.4 Typical Forward Characteristics** 

16

20

24

1.8

MUR140S-160S

8

4

70

60

50

40

30 20 10

0

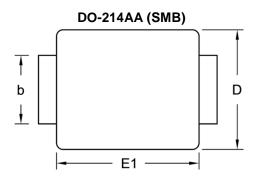
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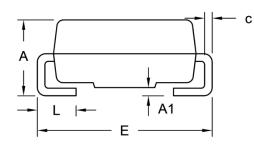
CAPACITANCE (pF)

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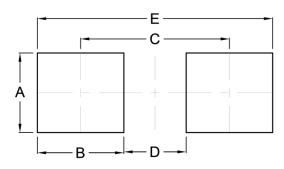
# PACKAGE OUTLINE DIMENSIONS





DIM.	Unit	Unit (mm)		(inch)
	Min.	Max.	Min.	Max.
A	1.95	2.65	0.077	0.104
A1	0.05	0.20	0.002	0.008
b	1.95	2.20	0.077	0.087
с	0.15	0.31	0.006	0.012
D	3.30	3.95	0.130	0.156
E	5.10	5.60	0.201	0.220
E1	4.05	4.60	0.159	0.181
L	0.75	1.60	0.030	0.063

# SUGGESTED PAD LAYOUT



Symbol	Unit (mm)	Unit (inch)
A	2.30	0.091
В	2.50	0.098
С	4.30	0.169
D	1.80	0.071
E	6.80	0.268

### MARKING DIAGRAM



- P/N = Marking Code
- G = Green Compound

YW = Date Code

F = Factory Code



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