

Taiwan Semiconductor

2A, 100-200V Ultrafast Surface Mount Rectifier

FEATURES

- AEC-Q101 gualified
- Planar technology
- Ideal for automated placement
- Low reverse leakage
- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21

APPLICATIONS

- High frequency switching
- DC/DC converter
- Snubber

MECHANICAL DATA

- Case: Micro SMA
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- Polarity: Indicated by cathode band
- Weight: 0.006g (approximately)

KEY PARAMETERS			
PARAMETER	VALUE	UNIT	
I _F	2	А	
V _{RRM}	100-200	V	
I _{FSM}	28	А	
T _{J MAX}	175	°C	
Package	Micro SMA		





Micro SMA



ABSOLUTE MAXIMUM RATINGS (T _A = 25°C unless otherwise noted)					
PARAMETER		SYMBOL	PU2BMH	PU2DMH	UNIT
Marking code on the device			P3	P4	
Repetitive peak reverse voltage		V _{RRM}	100	200	V
Reverse voltage, total rms value		V _{R(RMS)}	70	140	V
DC blocking voltage		V _{DC}	100	200	V
Forward current		I _F	2		Α
Surge peak forward current single	8.3ms at $T_A = 25^{\circ}C$	28		8	А
half sine-wave superimposed on rated load	1.0ms at $T_A = 25^{\circ}C$	I _{FSM}	52		Α
Junction temperature		TJ	-55 to +175		°C
Storage temperature		T _{STG}	-55 to +175		°C







THERMAL PERFORMANCE			
PARAMETER	SYMBOL	ТҮР	UNIT
Junction-to-lead thermal resistance	R _{θJL}	28	°C/W
Junction-to-ambient thermal resistance	R _{ejA}	60	°C/W
Junction-to-case thermal resistance	R _{eJC}	34	°C/W

Thermal Performance Note: Units mounted on PCB (5mm x 5mm Cu pad test board)

ELECTRICAL SPECIFICATIONS (T _A = 25°C unless otherwise noted)					
PARAMETER	CONDITIONS	SYMBOL	TYP	MAX	UNIT
Forward voltage ⁽¹⁾	$I_F = 1.0A, T_J = 25^{\circ}C$		0.90	-	V
	$I_F = 2.0A, T_J = 25^{\circ}C$	V _F	0.99	1.05	V
	$I_F = 1.0A, T_J = 125^{\circ}C$	VF	0.76	-	V
	$I_F = 2.0A, T_J = 125^{\circ}C$		0.84	0.90	V
Reverse current @ rated V _R ⁽²⁾	$T_J = 25^{\circ}C$		-	1	μA
	T _J = 125°C	I _R	-	15	μA
	$I_F = 0.5A, I_R = 1.0A, Irr = 0.25A$	+	-	25	ns
Reverse recovery time	I_{F} = 1.0A, di/dt = 50A/µs, V_{R} = 30V	t _{rr}	36	-	
Reverse recovery current		I _{RM}	3.8	-	Α
Reverse recovery charge	$I_F = 2.0A$, di/dt = 200A/µs, $V_R = 100V$	Q _{rr}	57	-	nC
Reverse recovery time		t _{rr}	28	-	ns
Junction capacitance	$1MHz, V_R = 4.0V$	CJ	18	-	pF

Notes:

(1) Pulse test with PW = 0.3ms

(2) Pulse test with PW = 30ms

ORDERING INFORMATION			
ORDERING CODE ⁽¹⁾	PACKAGE	PACKING	
PU2xMH M3G	Micro SMA	3000 / 7" reel	

Notes:

(1) "x" defines voltage from 100V(PU2BMH) to 200V(PU2DMH)



f=1.0MHz Vsig=50mVp-p

100

CHARACTERISTICS CURVES

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$

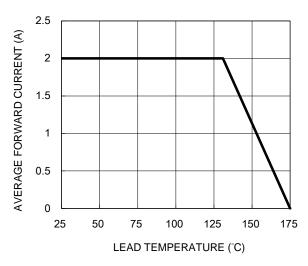
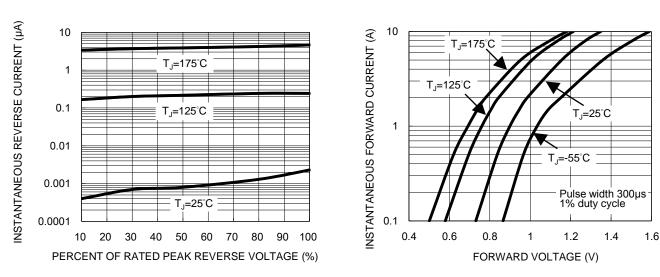


Fig.1 Forward Current Derating Curve

Fig.3 Typical Reverse Characteristics



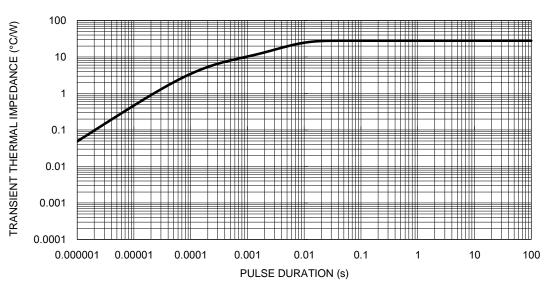


Fig.5 Typical Transient Thermal Impedance

Fig.2 Typical Junction Capacitance

10

REVERSE VOLTAGE (V)

Fig.4 Typical Forward Characteristics

100

10

1

1

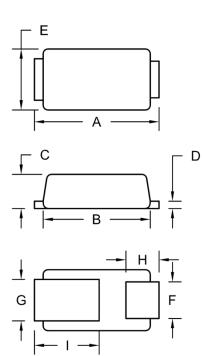
CAPACITANCE (pF)

PACKAGE OUTLINE DIMENSIONS (Unit: Millimeters)

Micro SMA

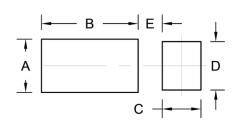
TAIWAN SEMICONDUCTOR

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DIM.	Unit (mm)		Unit	(inch)
DIN.	Min.	Max.	Min.	Max.
А	2.30	2.70	0.091	0.106
В	2.10	2.30	0.083	0.091
С	0.63	0.73	0.025	0.029
D	0.10	0.20	0.004	0.008
E	1.15	1.35	0.045	0.053
F	0.65	0.85	0.026	0.034
G	0.75	0.95	0.030	0.037
Н	0.55	0.75	0.022	0.030
I	1.10	1.50	0.043	0.059

SUGGESTED PAD LAYOUT



Symbol	Unit (mm)	Unit (inch)
A	1.10	0.043
В	2.00	0.079
С	0.80	0.031
D	1.00	0.039
E	0.50	0.020

MARKING DIAGRAM



P/N = Marking Code YW = Date Code



Taiwan Semiconductor

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