

20A, 50V - 600V Glass Passivated Super Fast Rectifiers

FEATURES

- Dual rectifier construction, positive center-tap
- Glass passivated chip junctions
- Superfast recovery time, high voltage
- Low forward voltage, high current capability
- Low thermal resistance
- Low power loss, high efficiency
- UL Recognized File # E-326243
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21



Case: TO-247AD (TO-3P)

Molding compound, UL flammability classification rating 94V-0

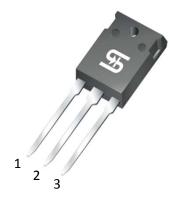
Part no. with suffix "H" means AEC-Q101 qualified

Packing code with suffix "G" means green compound (halogen-free) **Terminal:** Matte tin plated leads, solderable per JESD22-B102

Meet JESD 201 class 2 whisker test

Polarity: As marked

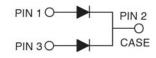
Mounting torque: 1.13 Nm max. **Weight:** 5.6g (approximately)







TO-247AD (TO-3P)



		SF	SF	SF	SF	SF	SF	SF	SF	
PARAMETER	SYMBOL	2001	2002	2003	2004	2005	2006	2007	2008	UNIT
		PT	PT	PT	PT	PT	PT	PT	PT	
Maximum repetitive peak reverse voltage	V_{RRM}	50	100	150	200	300	400	500	600	V
Maximum RMS voltage	V_{RMS}	35	70	105	140	210	280	350	420	V
Maximum DC blocking voltage	V_{DC}	50	100	150	200	300	400	500	600	V
Maximum average forward rectified current	I _{F(AV)}				2	20			Α	
Peak forward surge current, 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	180					А			
Maximum instantaneous forward voltage (Note 1) I_F = 10 A I_F = 20 A	V _F	•		0.975 1.100		1.3 1.5		1.7 1.9		V
Maximum reverse current @ rated V_R T_J =25°C T_J =125°C	I _R	10 400				μΑ				
Maximum reverse recovery time (Note 2)	t _{rr}	t _{rr} 35					ns			
Typical junction capacitance (Note 3)	CJ	175						pF		
Typical thermal resistance	$R_{\theta JC}$	2.5						°C/W		
Operating junction temperature range	TJ	- 55 to +150						°C		
Storage temperature range	T _{STG}	- 55 to +150					°C			

Note 1: Pulse test with PW=300 µs, 1% duty cycle

Note 2: Test conditions: I_F =0.5A, I_R =1.0A, recover to 0.25A.

Note 3: Measured at 1 MHz and applied reverse voltage of 4.0V DC.



ORDERING INFORMATION							
PART NO.	PART NO. SUFFIX	PACKING CODE	PACKING CODE SUFFIX ^(*)	PACKAGE	PACKING		
SF20xxPT (Note 1)	Н	C0	G	TO-3P	50 / Tube		

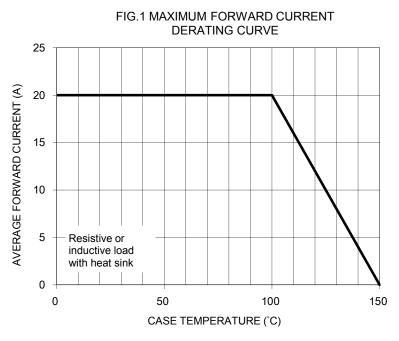
Note 1: "xx" defines voltage from 50V (SF2001PT) to 600V (SF2008PT)

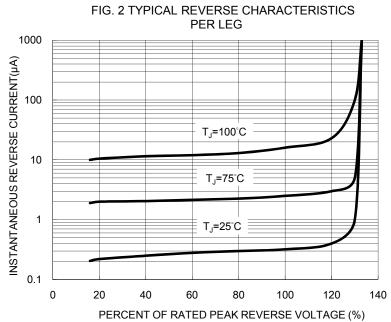
^{*:} Optional available

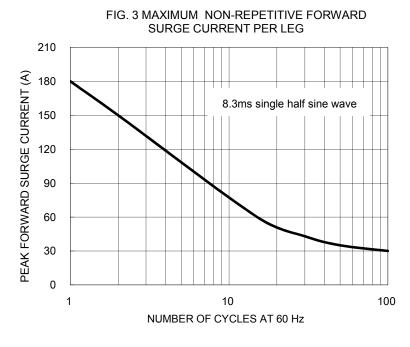
EXAMPLE							
EXAMPLE P/N PART NO.		PART NO. SUFFIX	PACKING CODE	PACKING CODE SUFFIX	DESCRIPTION		
SF2006PTHC0G	SF2006PT	Н	CO	G	AEC-Q101 qualified Green compound		

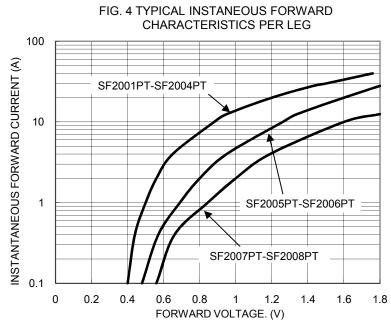
RATINGS AND CHARACTERISTICS CURVES

(T_A=25°C unless otherwise noted)









Version: H1601

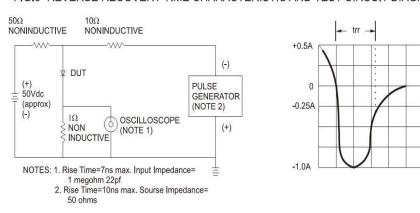




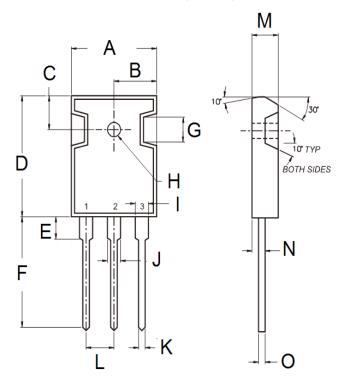
FIG. 5 TYPICAL JUNCTION CAPACITANCE PER LEG

1000 (dd) 1000 f=1MHz Vsig=50mVp-p 10 0.1 1 10 100 REVERSE VOLTAGE. (V)

FIG.6 REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM



PACKAGE OUTLINE DIMENSIONS TO-247AD (TO-3P)



DIM.	Unit	(mm)	Unit (inch)			
DIW.	Min Max		Min	Max		
Α	15.90	16.40	0.626	0.646		
В	7.90	8.20	0.311	0.323		
С	5.70	6.20	0.224	0.244		
D	20.80	21.30	0.819	0.839		
E	3.50	4.10	0.138	0.161		
F	19.70	20.20	0.776	0.795		
G	-	4.30	-	0.169		
Н	2.90	3.40	0.114	0.134		
I	1.93	2.18	0.076	0.086		
J	2.97	3.22	0.117	0.127		
K	1.12	1.22	0.044	0.048		
L	5.20	5.70	0.205	0.224		
М	4.90	5.16	0.193	0.203		
N	2.70	3.00	0.106	0.118		
0	0.51	0.76	0.020	0.030		

MARKING DIAGRAM



P/N = Marking Code
G = Green Compound
YWW = Date Code
F = Factory Code





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