

30V N-Channel Power MOSFET



SOP-8

Pin Definition:

8 1. Source 8. Drain
2. Source 7. Drain
3. Source 6. Drain
4. Gate 5. Drain

Key Parameter Performance

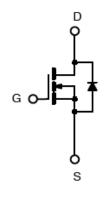
Parameter		Value	Unit	
V_{DS}		30	V	
R _{DS(on)} (max)	V _{GS} = 10V	18	mΩ	
	V _{GS} = 4.5V	28		
Q_g		4.1	nC	

Ordering Information

Part No.	Package	Packing		
TSM180N03CS RLG	SOP-8	2.5kpcs / 13" Reel		

Note: "G" denotes for Halogen- and Antimony-free as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds

Block Diagram



N-Channel MOSFET

Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V_{DS}	30	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Dunin Comment	Tc=25°C	l _D	9	Α
Continuous Drain Current	Tc=100°C		5.7	Α
Pulsed Drain Current (Note 1)		I _{DM}	36	Α
Single Pulse Avalanche Energy (Note 2)		E _{AS}	32	mJ
Power Dissipation @ T _C = 25°C		P _D	2.5	W
Operating Junction Temperature		TJ	150	°C
Storage Temperature Range		T _{STG}	-55 to +150	°C

Thermal Performance

Parameter	Symbol	Limit	Unit	
Thermal Resistance - Junction to Ambient	$R_{\Theta JA}$	50	°C/W	

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Electrical Specifications (T_J=25°C unless otherwise noted)

Parameter	Conditions	Symbol	Min	Тур	Max	Unit
Static						
Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250\mu A$	BV _{DSS}	30			V
	$V_{GS} = 10V, I_D = 8A$	R _{DS(ON)}		16	18	mΩ
Drain-Source On-State Resistance	$V_{GS} = 4.5V, I_D = 5A$			23	28	
Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	V _{GS(TH)}	1.2	1.6	2	V
	$V_{DS} = 30V, V_{GS} = 0V$	_			1	μΑ
Zero Gate Voltage Drain Current	V _{DS} = 24V, T _J = 125°C	I _{DSS}			10	
Gate Body Leakage	$V_{GS} = \pm 20V, V_{DS} = 0V$	I _{GSS}			±100	nA
Forward Transconductance (Note 3)	$V_{DS} = 10V, I_{D} = 5A$	g _{fs}		4		S
Dynamic						
Total Gate Charge (Note 3,4)		Q_g		4.1		nC
Gate-Source Charge (Note 3,4)	$V_{DS} = 15V, I_{D} = 8A,$	Q_{gs}		1		
Gate-Drain Charge (Note 3,4)	$V_{GS} = 4.5V$	Q_{gd}		2.1		
Input Capacitance		C _{iss}		345		pF
Output Capacitance	$V_{DS} = 25V, V_{GS} = 0V,$	C _{oss}		55		
Reverse Transfer Capacitance	f = 1.0MHz	C _{rss}		32		
Switching				l		
Turn-On Delay Time (Note 3,4)		t _{d(on)}		2.8		
Turn-On Rise Time (Note 3,4)	$V_{DD} = 15V, I_D = 1A,$	t _r		7.2		
Turn-Off Delay Time (Note 3,4)	$V_{GS} = 10V, R_G = 6\Omega$	t _{d(off)}		15.8		ns
Turn-Off Fall Time (Note 3,4)		t _f		4.6		
Source-Drain Diode Ratings and Ch	aracteristic					
Maximum Continuous Drain-Source	Integral reverse diode in the MOSFET	,			9	۸
Diode Forward Current		I _S			9	A
Maximum Pulse Drain-Source Diode		I _{SM}			36	Α
Forward Current		-				
Diode-Source Forward Voltage	$V_{GS} = 0V$, $I_S = 1A$	V_{SD}			1	V

Note:

- 1. Pulse width limited by safe operating area
- 2. L=1mH, I_{AS} =8A, V_{DD} =25V, R_{G} =25 Ω , Starting T_{J} =25 $^{\circ}$ C
- 3. Pulse test: pulse width ≤300µs, duty cycle ≤2%
- 4. Switching time is essentially independent of operating temperature.

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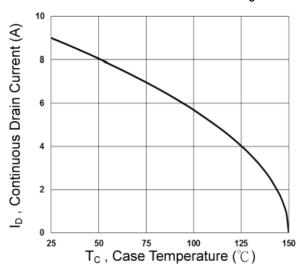


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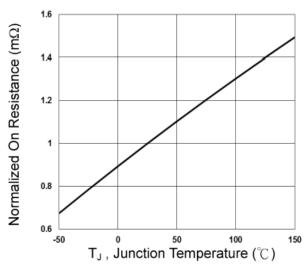


Electrical Characteristics Curve

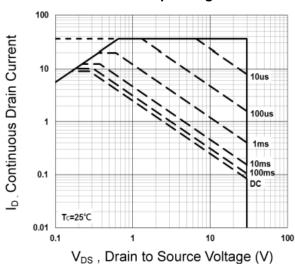
Continuous Drain Current vs. T_C



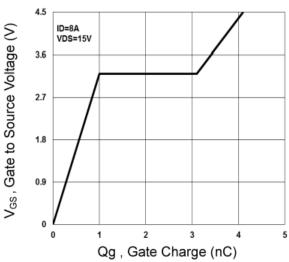
On-Resistance vs. Junction Temperature



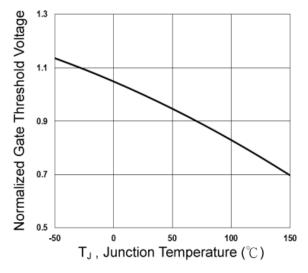
Maximum Safe Operating Area



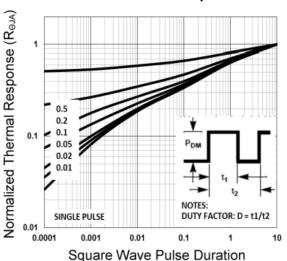
Gate Charge



Threshold Voltage vs. Junction Temperature



Normalized Thermal Transient Impedance Curve



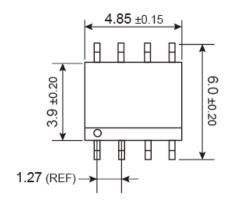
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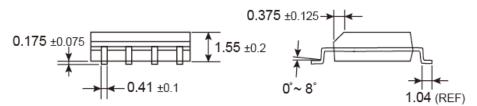


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SOP-8 Mechanical Drawing





Unit: Millimeters

Marking Diagram



Y = Year Code

M = Month Code for Halogen Free Product
 (O=Jan, P=Feb, Q=Mar, R=Apl, S=May, T=Jun, U=Jul, V=Aug, W=Sep,
 X=Oct, Y=Nov, Z=Dec)

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L = Lot Code

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Pb RóHS

TSM180N03CS 30V N-Channel Power MOSFET

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