

SOT-23		PRODUCT SUMMARY			
1 2	<b>Pin Definition:</b> 1. Gate 2. Source 3. Drain	V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (mΩ)	I <sub>D</sub> (A)	
		20	130 @ V <sub>GS</sub> = -4.5V	-2.8	
		-20	190 @ V <sub>GS</sub> = -2.5V	-2.0	

## **Features**

- Advance Trench Process Technology
- High Density Cell Design for Ultra Low On-resistance

#### **Application**

- Load Switch
- PA Switch

### Ordering Information

Part No.	Package	Packing		
TSM2301CX RFG	SOT-23	3Kpcs / 7" Reel		

Note: "G" denotes for Halogen Free

## Absolute Maximum Rating (Ta = 25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit		
Drain-Source Voltage		V <sub>DS</sub>	-20	V	
Gate-Source Voltage		V <sub>GS</sub>	±8	V	
Continuous Drain Current, V <sub>GS</sub> @4.5	Ι <sub>D</sub>	-2.8	А		
Pulsed Drain Current, V <sub>GS</sub> @4.5V	I <sub>DM</sub>	-8	А		
Continuous Source Current (Diode Co	I <sub>S</sub>	-0.72	А		
Maximum Dawar Discinction	Ta = 25°C		0.9	W	
Maximum Power Dissipation	Ta = 75°C	– P <sub>D</sub>	0.57		
Operating Junction Temperature		TJ	+150	°C	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	- 55 to +150	°C	

#### **Thermal Performance**

Parameter	Symbol	Limit	Unit	
Lead Temperature (1/8" from case)	TL	5	S	
Junction to Ambient Thermal Resistance (PCB mounted)	RƏ <sub>JA</sub>	120	°C/W	

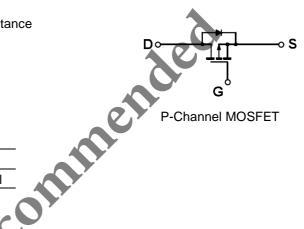
Notes:

a. Pulse width limited by the Maximum junction temperature

b. Surface Mounted on FR4 Board, t  $\leq$  5 sec.

c. Surface Mounted on FR4 Board,

### **Block Diagram**





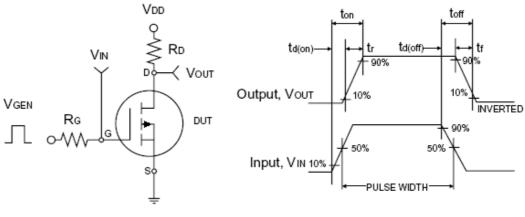
#### Electrical Specifications (Ta = 25°C unless otherwise noted)

Parameter	Conditions	Symbol	Min	Тур	Мах	Unit
Static		1				
Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_{D} = -250uA$	$BV_{DSS}$	-20			V
Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	V <sub>GS(TH)</sub>	-0.45		-0.95	V
Gate Body Leakage	$V_{GS} = \pm 8V, V_{DS} = 0V$	I <sub>GSS</sub>			±100	nA
Zero Gate Voltage Drain Current	$V_{DS}$ = -9.6V, $V_{GS}$ = 0V	I <sub>DSS</sub>			-1.0	μA
On-State Drain Current <sup>a</sup>	$V_{DS} = -10V, V_{GS} = -5V$	I <sub>D(ON)</sub>	-6			А
Ducia Course On State Decistence <sup>a</sup>	$V_{GS}$ = -4.5V, $I_{D}$ = -2.8A	D		85	130	mΩ
Drain-Source On-State Resistance <sup>a</sup>	$V_{GS}$ = -2.5V, $I_{D}$ = -2.0A	R <sub>DS(ON)</sub>		122	190	
Forward Transconductance <sup>a</sup>	$V_{DS} = -5V, I_{D} = -4A$	<b>g</b> <sub>fs</sub>		6.5		S
Diode Forward Voltage	$I_{S}$ = -0.75A, $V_{GS}$ = 0V	V <sub>SD</sub>		- 0.8	-1.2	V
Dynamic <sup>ь</sup>					_	-
Total Gate Charge	$V_{DS} = -6V, I_D = -2.8A,$ $V_{GS} = -4.5V$	Q <sub>9</sub>		5.4	10	nC
Gate-Source Charge		Q <sub>gs</sub>		0.8		
Gate-Drain Charge		Q <sub>gd</sub>		1.1		
Input Capacitance	$V_{DS} = -6V, V_{GS} = 0V$ f = 1.0MHz	C <sub>iss</sub>		447		pF
Output Capacitance		C <sub>oss</sub>		127		
Reverse Transfer Capacitance		C <sub>rss</sub>		80		
Switching <sup>c</sup>		1	L			
Turn-On Delay Time	$V_{DD} = -6V, R_L = 6\Omega,$ $V_D = -1A, V_{GEN} = -4.5V,$ $R_S = 6\Omega$	t <sub>d(on)</sub>		5	25	nS
Turn-On Rise Time		t <sub>r</sub>		19	60	
Turn-Off Delay Time		t <sub>d(off)</sub>		95	110	
Turn-Off Fall Time	1vg - 012	t <sub>f</sub>		65	80	

Notes:

a. pulse test:  $PW = 300\mu$ S, duty cycle = 2% b. For DESIGN AID ONLY, not subject to production testing.

b. Switching time is essentially independent of operating temperature.

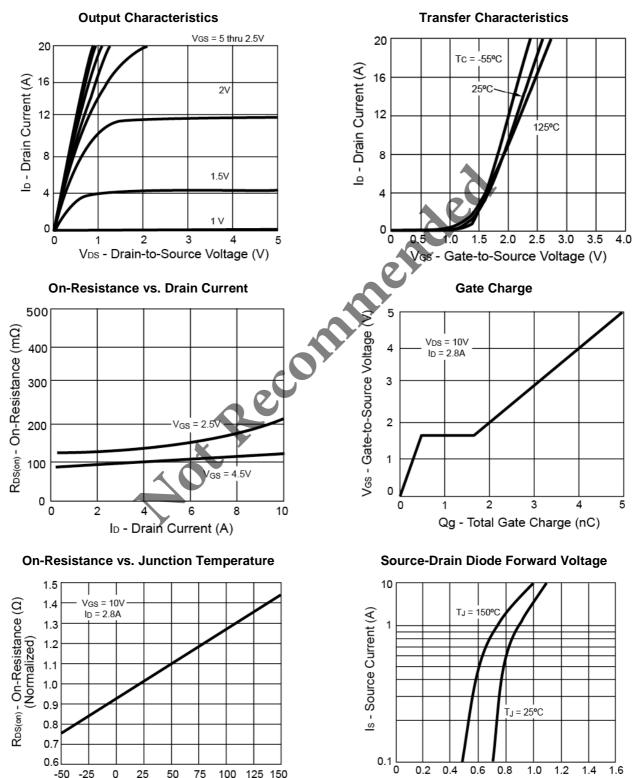


Switching Test Circuit

Switchin Waveforms



#### Electrical Characteristics Curve (Ta = 25°C, unless otherwise noted)



Document Number: DS\_P0000258

25

0

75

50

Tj - Junction Temperature (°C)

100

125 150

0.6

-50 -25 0.6

0.8

Vsp - Source-to-Drain Voltage (V)

1.0

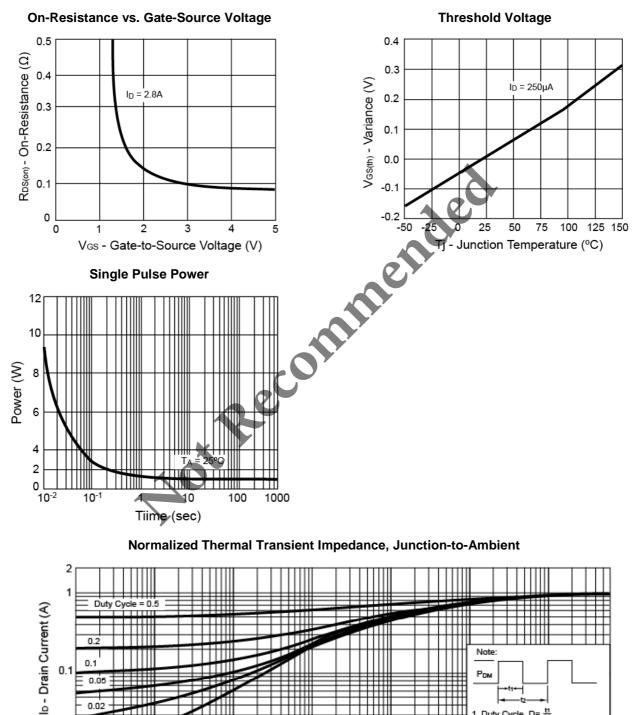
1.2

1.4 1.6

0.2 0.4



### Electrical Characteristics Curve (Ta = 25°C, unless otherwise noted)



0.1

0.05 0.02

0.1

0.01

10-4

Ш

10-2

Single Pul

10-3

4

Square Wave Pulse Duration (sec)

1

Ш

10-1

Ром

4

10

1. Duty Cycle, D= #

2. Per Unit Base = Rhu

3. TJM - TA = PDMZtHJA

Surface Mounted

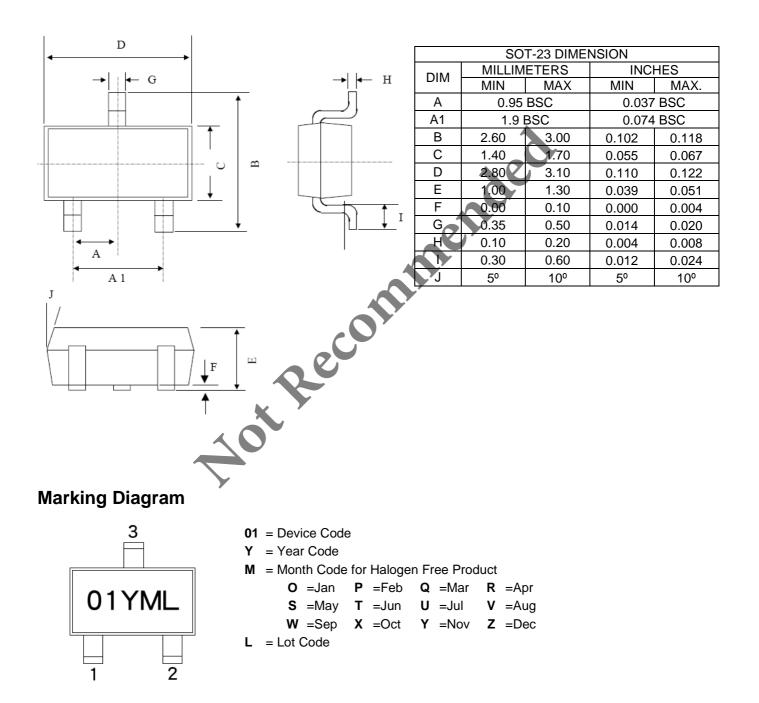
100

120°C/W

600



# SOT-23 Mechanical Drawing







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