

SOT-23

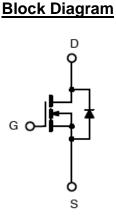
Pin Definition: 1. Gate

1. Gate 2. Source	Parameter		Value	Unit	
3. Drain	V _{DS}		30	V	
Note: MSL 1 (Moisture Sensitivity Level) per J-STD-020	R _{DS(on)} (max)	$V_{GS} = 10V$	24		
		$V_{GS} = 4.5V$	34	mΩ	
Qg			4.1	nC	

Ordering Information

Ordering code	Package	Packing
TSM240N03CX RFG	SOT-23	3kpcs / 7" Reel

Note: Halogen-free according to IEC 61249-2-21 definition •



N-Channel MOSFET

Absolute Maximum Ratings (T_c = 25°C unless otherwise noted)

Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V _{DS}	30	V
Gate-Source Voltage		V _{GS}	±20	V
Continuous Drain Current	$T_{C} = 25^{\circ}C$	- I _D	6.5	А
	$T_{c} = 100^{\circ}C$		4.1	А
Pulsed Drain Current (Note 1)		I _{DM}	26	А
Single Pulse Avalanche Energy (Note 2)		E _{AS}	32	mJ
Power Dissipation @ $T_c = 25^{\circ}C$		P _D	1.56	W
Operating Junction Temperature		TJ	150	°C
Storage Temperature Range		T _{STG}	-55 to +150	°C

Thermal Performance

Parameter	Symbol	Limit	Unit	
Thermal Resistance - Junction to Ambient	$R_{\Theta JA}$	80	°C/W	

Key Parameter Performance

TSM240N03CX 30V N-Channel Power MOSFET





Electrical Specifications (T_c = 25°C unless otherwise noted)

Parameter	Conditions	Symbol	Min	Тур	Max	Unit
Static						
Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250 \mu A$	BV _{DSS}	30			V
	$V_{GS} = 10V, I_D = 6A$	R _{DS(on)}		17	24	mΩ
Drain-Source On-State Resistance	$V_{GS} = 4.5V, I_D = 4A$			22	34	
Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	V _{GS(TH)}	1.2	1.4	2.5	V
	$V_{DS} = 30V, V_{GS} = 0V$				1	μA
Zero Gate Voltage Drain Current	V _{DS} = 24V, T _J = 125°C	I _{DSS}			10	
Gate Body Leakage	$V_{GS} = \pm 20V, V_{DS} = 0V$	I _{GSS}			±100	μA
Forward Transconductance (Note 3)	$V_{DS} = 10V, I_{D} = 4A$	g _{fs}		6.5		S
Dynamic				1	•	
Total Gate Charge (Note 3,4)		Qg		4.1		nC
Gate-Source Charge (Note 3,4)	$V_{DS} = 15V, I_D = 6A,$	Q _{gs}		1		
Gate-Drain Charge (Note 3,4)	$V_{GS} = 4.5V$	Q _{gd}		2.1		
Input Capacitance	$V_{DS} = 25V, V_{GS} = 0V,$	C _{iss}		345		
Output Capacitance		C _{oss}		55		pF
Reverse Transfer Capacitance	f = 1.0MHz	C _{rss}		32		
Switching	1				•	
Turn-On Delay Time (Note 3,4)		t _{d(on)}		2.8		
Turn-On Rise Time (Note 3,4)	V _{DD} = 15V, I _D = 1A,	t _r		7.2		
Turn-Off Delay Time (Note 3,4)	$V_{GS} = 10V, R_G = 6\Omega$	t _{d(off)}		15.8		ns
Turn-Off Fall Time (Note 3,4)		t _f		4.6		
Source-Drain Diode Ratings and Ch	aracteristic			1		
Maximum Continuous Drain-Source	Integral reverse diode in the MOSFET	I			6.5	٨
Diode Forward Current		۱ _s			0.5	A
Maximum Pulse Drain-Source Diode		I _{SM}			26	А
Forward Current		_				
Diode-Source Forward Voltage	$V_{GS} = 0V, I_{S} = 1A$	V_{SD}			1	V

Note:

1. Pulse width limited by safe operating area

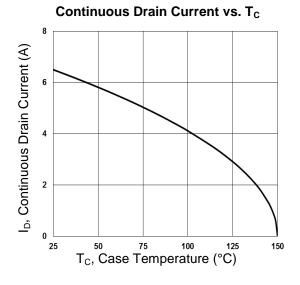
2. L = 1mH, I_{AS} = 8A, V_{DD} = 25V, R_G = 25 Ω , Starting T_J = 25°C

3. Pulse test: pulse width \leq 300µs, duty cycle \leq 2%

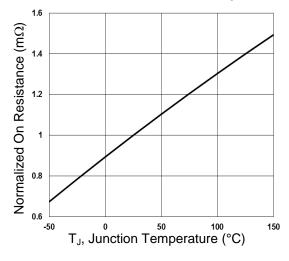
4. Switching time is essentially independent of operating temperature.



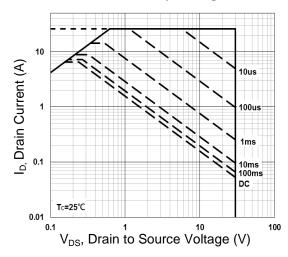
Electrical Characteristics Curve

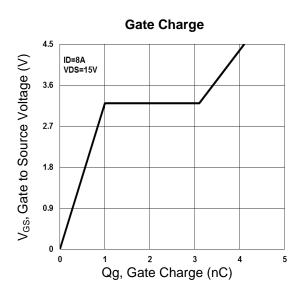


On-Resistance vs. Junction Temperature

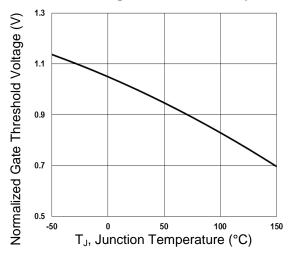


Maximum Safe Operating Area

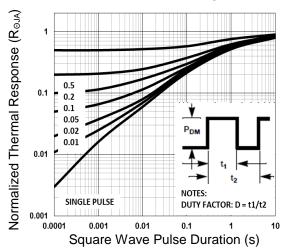




Threshold Voltage vs. Junction Temperature

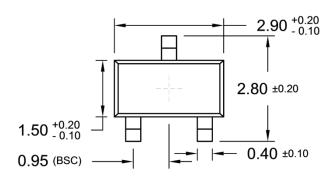


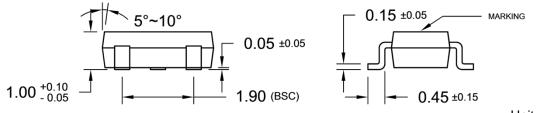
Normalized Thermal Transient Impedance Curve





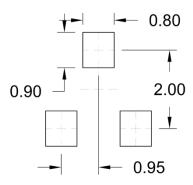
SOT-23 Mechanical Drawing





Unit: Millimeters

SUGGESTED PAD LAYOUT (Unit: Millimeters)



Marking Diagram



- 24 = Device Code
- Y = Year Code
- M = Month Code for Halogen Free Product
 (O=Jan, P=Feb, Q=Mar, R=Apl, S=May, T=Jun, U=Jul, V=Aug, W=Sep, X=Oct, Y=Nov, Z=Dec)
- L = Lot Code





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