

# **Trench Schottky Rectifier**

### **FEATURES**

- Patented Trench Schottky technology
- Excellent high temperature stability
- Low forward voltage
- Lower power loss/ high efficiency
- High forward surge capability
- Halogen-free according to IEC 61249-2-21 definition
- Moisture sensitivity level: level 1, per J-STD-020
- Compliant to RoHS directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- AEC-Q101 qualified







## SOD123HE

## HALOGEN FREE

## **TYPICAL APPLICATIONS**

Trench Schottky barrier rectifier are designed for high frequency miniature switched mode power supplies such as adapters, lighting and on-board DC/DC converters.

## **MECHANICAL DATA**

Case: SOD123HE

Molding compound, UL flammability classification rating 94V-0 Packing code with suffix "G" means green compound (halogen-free)

Terminal: Matte tin plated leads, solderable per JESD22-B102

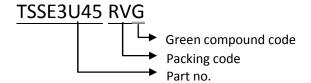
Meet JESD 201 class 2 whisker test **Polarity:** Indicated by cathode band **Weight:** 0.022 g (approximately)

MAXIMUM RATINGS AND ELE	CTRICAL CHARAC	CTERISTIC	<b>S</b> (T <sub>A</sub> =25	°C unless	otherwise	noted)	
PARAMETER		SYMBOL	TSSE3U45		TSSE3U60		UNIT
Marking code		E3U45 E3U60					
Maximum repetitive peak reverse voltag	$V_{RRM}$	45 60					
Working Peak Reverse Voltage	$V_{RWM}$			V			
DC Blocking Voltage	$V_{RM}$						
Maximum RMS voltage	$V_{RMS}$	32 42		2	V		
Maximum average forward rectified curre	I <sub>F(AV)</sub>	3		Α			
Peak forward surge current, 8.3 ms single half sine-wave superimposed on rated load		I <sub>FSM</sub>	80			А	
			Тур	Max	Тур	Max	
	I <sub>F</sub> = 1A	V <sub>F</sub>	0.33	-	0.39	-	V
	$I_F = 3A$ $T_J = 25^{\circ}C$		0.40	0.47	0.49	0.58	
Instantaneous forward voltage (Note 1)	I <sub>F</sub> = 1A		0.24	-	0.28	-	
	$I_F = 3A$ $T_J = 125^{\circ}C$		0.34	0.44	0.43	0.52	
Maximum Instantaneous reverse current at rated reverse voltage $T_J = 25^{\circ}C$ $T_J = 125^{\circ}C$		. I <sub>R</sub>	1000			μA	
			50			mA	
Typical thermal resistance	$R_{ heta JL}$	23			°C/W		
Operating junction temperature range	$T_J$	- 55 to +150			°C		
Storage temperature range		$T_{STG}$	- 55 to +150			°C	

Note 1: Pulse Test with Pulse Width=300µs, 1% Duty Cycle



## **ORDER INFORMATION (EXAMPLE)**

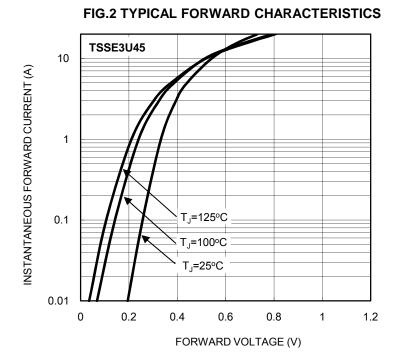


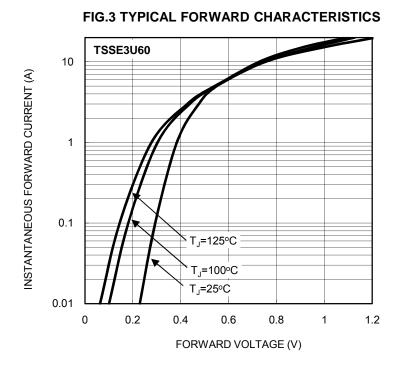
## **RATINGS AND CHARACTERISTICS CURVES**

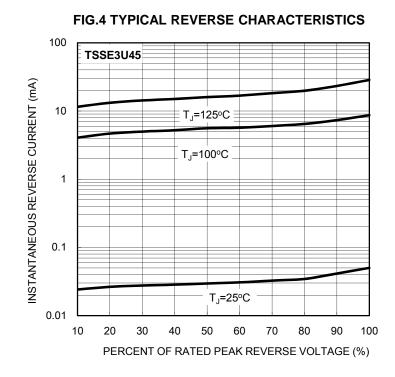
(T<sub>A</sub>=25°C unless otherwise noted)

# FIG.1 FORWARD CURRENT DERATING CURVE 4 3 WITH HEATSINK 5mm x 5mm PAD PCB 50 60 70 80 90 100 110 120 130 140 150

LEAD TEMPERATURE (°C)

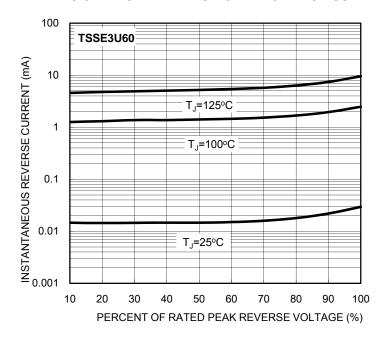




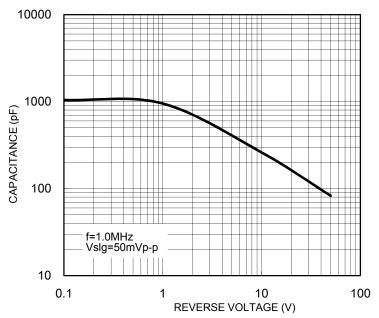




## FIG.5 TYPICAL REVERSE CHARACTERISTICS



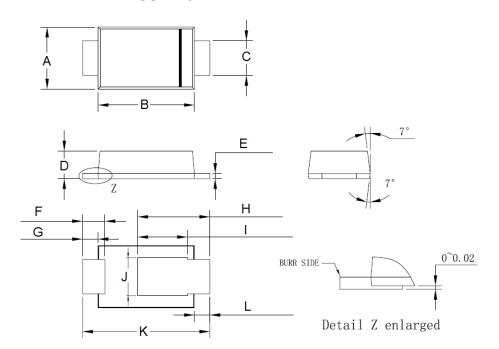
## FIG.6 TYPICAL JUNCTION CAPACITANCE





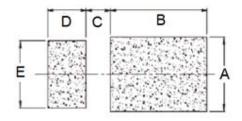


# PACKAGE OUTLINE DIMENSIONS SOD123HE



DIM.	Unit	(mm)	Unit (inch)		
DIIVI.	Min	Max	Min	Max	
Α	1.65	1.95	0.06	0.08	
В	2.60	3.00	0.10	0.12	
С	0.85	1.15	0.03	0.05	
D	0.75	0.85	0.03	0.03	
Е	0.10	0.20	0.00	0.01	
F	0.55	0.75	0.02	0.03	
G	0.35	0.55	0.01	0.02	
Н	1.90	2.30	0.07	0.09	
I	1.35	1.55	0.05	0.06	
J	0.95	1.25	0.04	0.05	
K	3.50	3.90	0.14	0.15	
L	0.35	0.55	0.01	0.02	

# **SUGGESTED PAD LAYOUT**



Symbol	Unit (mm)	Unit (inch)
Α	1.40	0.055
В	2.40	0.094
С	0.70	0.028
D	0.90	0.035
E	1.40	0.055

# **MARKING DIAGRAM**

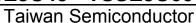


YW

= Marking Code

= Date Code

= Factory Code





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