

# 20A, 600V High Efficient Surface Mount Rectifier

#### **FEATURES**

- AEC-Q101 qualified
- · Lead for automated placement
- Low switching loss
- High surge current capability
- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21

#### **APPLICATIONS**

• On Board Charger

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Case: D<sup>2</sup>PAK-D

Molding compound meets UL 94V-0 flammability rating

• Terminal: Matte tin plated leads, solderable per J-STD-002

• Meet JESD 201 class 2 whisker test

Polarity: As marked
 Weight: 4,405

• Weight: 1.40g

KEY PARAMETERS				
PARAMETER	VALUE	UNIT		
I <sub>F</sub>	20	Α		
$V_{RRM}$	600	V		
I <sub>FSM</sub>	150	Α		
T <sub>J MAX</sub>	150	°C		
Package	D <sup>2</sup> PAK-D			

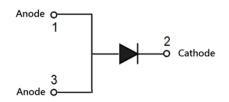








D<sup>2</sup>PAK-D



ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25°C unless otherwise noted)				
PARAMETER	SYMBOL	UGS20JH	UNIT	
Marking code on the device		UGS20JH		
Repetitive peak reverse voltage	$V_{RRM}$	600	V	
Reverse voltage, total rms value	V <sub>R(RMS)</sub>	420	V	
Forward current	I <sub>F</sub>	20	А	
Surge peak forward current, 1.0 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	270	А	
Surge peak forward current, 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	150	А	
Junction temperature	T <sub>J</sub>	-55 to +150	°C	
Storage temperature	T <sub>STG</sub>	-55 to +150	°C	





THERMAL PERFORMANCE				
PARAMETER	SYMBOL	TYP	UNIT	
Junction-to-lead thermal resistance	$R_{\Theta JL}$	4	°C/W	
Junction-to-ambient thermal resistance	R <sub>ÐJA</sub>	11	°C/W	
Junction-to-case thermal resistance	R <sub>eJC</sub>	3	°C/W	

Thermal Performance Note: Units mounted on heatsink 4"x 6"x 0.25" Al-plate

ELECTRICAL SPECIFICATIONS (T <sub>A</sub> = 25°C unless otherwise noted)					
PARAMETER	CONDITIONS	SYMBOL	TYP	MAX	UNIT
	$I_F = 10A, T_J = 25^{\circ}C$	.,	1.37	-	V
Forward voltage <sup>(1)</sup>	I <sub>F</sub> = 20A, T <sub>J</sub> = 25°C		1.57	2.00	V
Forward voltage	I <sub>F</sub> = 10A, T <sub>J</sub> = 125°C	V <sub>F</sub>	1.07	-	V
	I <sub>F</sub> = 20A, T <sub>J</sub> = 125°C		1.30	1.80	V
D	T <sub>J</sub> = 25°C		-	1	μΑ
Reverse current @ rated V <sub>R</sub> <sup>(2)</sup>	T <sub>J</sub> = 125°C	- I <sub>R</sub>	-	100	μΑ
Junction capacitance	1MHz, V <sub>R</sub> = 4.0V	CJ	94.5	-	pF
Reverse recovery time	$I_F = 0.5A$ , $I_R = 1.0A$ $I_{rr} = 0.25A$	t <sub>rr</sub>	-	50	ns
Reverse recovery current		I <sub>RM(REC)</sub>	4.6	-	Α
Reverse recovery time	$V_R = 400V, I_F = 6A$	t <sub>rr</sub>	69	-	ns
Reverse recovery charge	$dI_F/dt = 200A/\mu s$	Q <sub>rr</sub>	185	-	nC

### Notes:

- 1. Pulse test with PW = 0.3ms
- 2. Pulse test with PW = 30ms

ORDERING INFORMATION				
ORDERING CODE	PACKAGE	PACKING		
UGS20JH	D <sup>2</sup> PAK-D	800 / Tape & Reel		



#### **CHARACTERISTICS CURVES**

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$ 

**Fig.1 Forward Current Derating Curve** 

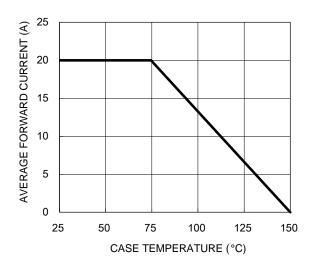


Fig.3 Typical Reverse Characteristics

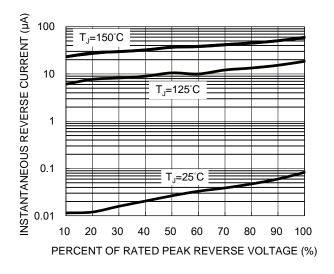


Fig.2 Typical Junction Capacitance

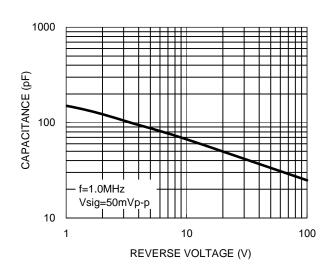


Fig.4 Typical Forward Characteristics

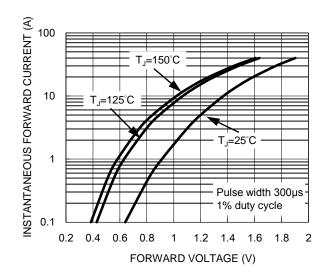
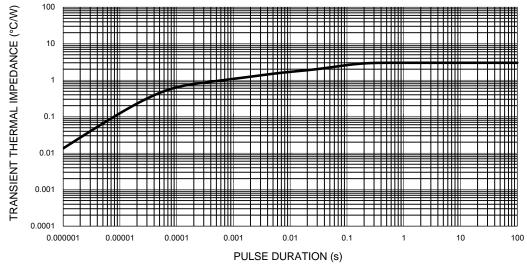


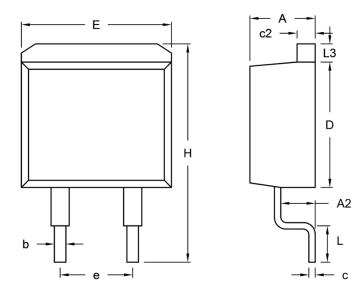
Fig.5 Typical Transient Thermal Impedance





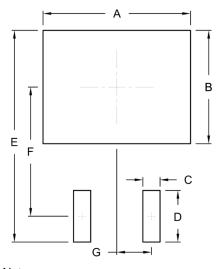
### **PACKAGE OUTLINE DIMENSIONS**

D<sup>2</sup>PAK-D



DIM.	Unit	Unit (mm)		Unit (inch)	
DIWI.	Min.	Max.	Min.	Max.	
Α	4.44	4.70	0.175	0.185	
A2	2.03	2.79	0.080	0.110	
b	0.68	0.94	0.027	0.037	
С	0.36	0.53	0.014	0.021	
c2	1.14	1.40	0.045	0.055	
D	8.25	9.25	0.325	0.364	
E	-	10.50	-	0.413	
е	4.82	5.34	0.190	0.210	
Н	14.60	15.88	0.575	0.625	
L	2.29	2.79	0.090	0.110	
L3	1.14	1.40	0.045	0.055	

### **SUGGESTED PAD LAYOUT**



Symbol	Unit (mm)	Unit (inch)
Α	10.80	0.425
В	8.30	0.327
С	1.26	0.050
D	3.78	0.149
E	15.50	0.610
F	9.46	0.372
G	2.54	0.100

### Notes:

This recommended land pattern is for reference purposes only. Please consult your manufacturing group to ensure your PCB design guidelines are met.

### **MARKING DIAGRAM**



P/N = Marking Code G = Green Compound

YWW = Date Code F = Factory Code



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