SPECIFICATION

SPEC. No. C-General-e

D A T E: 2017 Aug.

To

Non-Controlled Copy

CUSTOMER'S PRODUCT NAME

TDK PRODUCT NAME

MULTILAYER CERAMIC CHIP CAPACITORS

C Series / Commercial grade

General (Up to 50V)

Mid voltage (100 to 630V)

Please return this specification to TDK representatives.

If orders are placed without returned specification, please allow us to judge that specification is accepted by your side.

RECEIPT CONFIRMATION

DATE: YEAR MONTH DAY

TDK Corporation

Sales

Electronic Components Sales & Marketing Group Engineering

Electronic Components Business Company Ceramic Capacitors Business Group

APPROVED	Person in charge

APPROVED	CHECKED	Person in charge

1. SCOPE

This specification is applicable to chip type multilayer ceramic capacitors with a priority over the other relevant specifications.

Production places defined in this specification shall be TDK Corporation Japan,

TDK (Suzhou) Co., Ltd and TDK Components U.S.A. Inc.

EXPLANATORY NOTE:

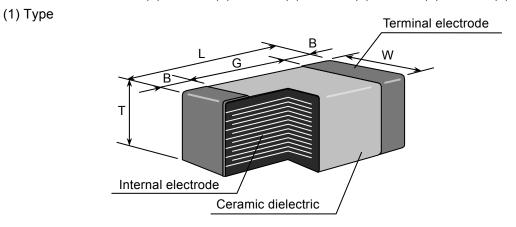
This specification warrants the quality of the ceramic chip capacitors. The chips should be evaluated or confirmed a state of mounted on your product.

If the use of the chips goes beyond the bounds of the specification, we can not afford to guarantee.

2. CODE CONSTRUCTION

(Example)

Catalog Number : (Web)	<u>C2012</u> (1)	(2)	1E (3)	<u>105</u> (4)	<u>K</u> (5)	<u>125</u> (6)	$\frac{A}{(7)}$ $\frac{A}{(8)}$
Item Description :	<u>C2012</u> (1)	X7R (2)	<u>1E</u> (3)	105 (4)	<u>K</u> (5)	<u>T</u> (9)	<u>xxxx</u> (10)



^{*}As for dimensions of each product, please refer to detailed information on TDK web.

(2) Temperature Characteristics (Details are shown in table 1 No.7 and No.8 at page 5)

(3) Rated Voltage

Symbol	Rated Voltage			
2 J	DC 630 V			
2 W	DC 450 V			
2 V	DC 350 V			
2 E	DC 250 V			
2 A	DC 100 V			
1 H	DC 50 V			
1 V	DC 35 V			
1 E	DC 25 V			
1 C	DC 16 V			
1 A	DC 10 V			
0 J	DC 6.3 V			
0 G	DC 4V			

(4) Rated Capacitance

Stated in three digits and in units of pico farads (pF).

The first and Second digits identify the first and second significant figures of the capacitance, the third digit identifies the multiplier.

R is designated for a decimal point.

$$105 \rightarrow 1,000,000pF$$

(5) Capacitance tolerance

Symbol	Tolerance	Capacitance
В	± 0.1 pF	
С	± 0.25 pF	10pF and under
D	± 0.5 pF	
J	± 5%	
K	± 10 %	Over 10pF
М	± 20 %	

- (6) Thickness code (Only Catalog Number)
- (7) Package code (Only Catalog Number)
- (8) Special code (Only Catalog Number)
- (9) Packaging (Only Item Description)

Symbol	Packaging
В	Bulk
Т	Taping

(10) Internal code (Only Item Description)

3. RATED CAPACITANCE AND CAPACITANCE TOLERANCE

3.1 Standard combination of rated capacitance and tolerances

Class	Temperature Characteristics	Capacitanc	e tolerance	Rated capacitance
		10pF and	C (±0.25pF)	0.5, 1, 1.5, 2, 2.2, 3, 3.3, 4, 4.7, 5
1	СН	under	D (±0.5pF)	6, 6.8, 7, 8, 9, 10
'	COG	12pF to 10,000pF	J (±5 %)	E – 12 series
		Over 10,000pF		E – 6 series
	J B X5R	10uF and	K (±10 %)	
2	X6S X7R X7S X7T	under	M (±20 %)	E – 6 series
		Over 10uF	M (±20 %)	E - 0 Selles

3.2 Capacitance Step in E series

E series	Capacitance Step											
E- 3	1.0			2.2			4.7					
E- 6	1.	.0	1.	.5	2.	.2	3	.3	4	.7	6.	.8
E-12	1.0	1.2	1.5	1.8	2.2	2.7	3.3	3.9	4.7	5.6	6.8	8.2

4. OPERATING TEMPERATURE RANGE

T.C.	Min. operating Temperature	Max. operating Temperature	Reference Temperature
C H J B	-25°C	85°C	20°C
X5R	-55°C	85°C	25°C
X6S	-55°C	105°C	25°C
X7R X7S X7T C0G	-55°C	125°C	25°C

5. STORING CONDITION AND TERM

5 to 40°C at 20 to 70%RH

6 months Max.

6. P.C. BOARD

When mounting on an aluminum substrate, large case sizes such as C3225, C4532 and C5750 types are more likely to be affected by heat stress from the substrate.

Please inquire separate specification for the large case sizes when mounted on the substrate.

7. INDUSTRIAL WASTE DISPOSAL

Dispose this product as industrial waste in accordance with the Industrial Waste Law.

8. PERFORMANCE

table 1

No.	Item	Performance		Test or inspection method				
1	External Appearance	No defects which may affect performance.	Inspect with magnifying glass (3×), in case of C0402 and C0603 type, with magnifying glass (10×)					
2	Insulation Resistance	10,000MΩ or 500MΩ·μF min. (As for the capacitors of rated voltage 16, 10V DC and lower, 10,000 MΩ or 100MΩ·μF min.,) whichever smaller.	Apply ra	ted voltage fo	r 60s.			
3	Voltage Proof	Withstand test voltage without insulation breakdown or other damage.		Rated voltage RV≦100V 100V <rv≦50 100v<rv≦50="" 500v<rv="" c="" cu<="" discharge="" rv≦100v="" shard="" td="" voltage=""><td>3 × rat 0V 1.5 × ra 1.3 × ra 2.5 × ra 0V 1.5 × ra 1.3 × ra 1.3 × ra all be applie</td><td></td></rv≦50>	3 × rat 0V 1.5 × ra 1.3 × ra 2.5 × ra 0V 1.5 × ra 1.3 × ra 1.3 × ra all be applie			
4	Capacitance	Within the specified tolerance.	Class 1 2	Rated Capacitance 1000pF and under Over 1000pF 10uF and under Over 10uF	Measuring frequency 1MHz±10% 1kHz±10% 1kHz±20%	Measuring voltage 0.5-5 Vrms. 0.5±0.2 Vrms. 1.0±0.2 Vrms. 0.5±0.2 Vrms.		
5	Q (Class1)	Rated Capacitance Q 30pF and over 1,000 min. Under 30pF 400+20×C min. C : Rated capacitance (pF)	See No.4	4 in this table	for measu	ring		
6	Dissipation Factor (Class2)	0.025 max. 0.03 max. 0.05 max. 0.075 max. 0.1 max.	condition For infor Dissipati	4 in this table n. mation which on Factor, ple each product	product ha	as which ne detail		

No.	Item	Performance		Test or inspection method		
7	Temperature	perature		Temperature coefficient shall be		
•	Characteristics	T.C. Temp	erature Coefficient (ppm/°C)	calculated based on values at 25°C		
	of Capacitance	СН	0 ± 60	(CH : 20°C) and 85°C temperature.		
	(Class1)	C0G	0 ± 30	Measuring temperature below 20°C shal		
		Capacitance dr	ift	be -10°C and -25°C.		
		Within ± 0.2%	•	50 - 10 0 dilu - 25 0.		
		whichever large	er.			
8	Temperature Characteristics	Capacitano	ce Change (%)	Capacitance shall be measured by the steps shown in the following table after		
	of Capacitance	No voltage applied	With voltage applied	thermal equilibrium is obtained for each		
	(Class2)	J B: ±10	J B: +10,-30 +10,-50	step. ΔC be calculated ref. STEP3 reading		
			+10,-60	Step Temperature(°C)		
		X5R: ±15		1 Reference temp. ± 2		
		X6S: ±22		2 Min. operating temp. ± 2		
		X7R: ±15 X7S: ±22		3 Reference temp. ± 2		
		X7T: +22,		4 Max. operating temp. ± 2		
9	Robustness of	No sign of term	ination coming off,	Measuring voltage: 0.1, 0.2, 0.5, 1.0Vrms. For information which product has which applied voltage, please contact with our sales representative. Reflow solder the capacitors on a		
•	Terminations	breakage of ce	_	P.C.Board shown in Appendix 1a or		
		abnormal signs		Appendix 1b and apply a pushing force		
		J		of 2N (C0603, C1005) or 5N (C1608,		
				C2012, C3216, C3225, C4532, C5750)		
				with 10±1s.		
				(Not applicable to C0402.)		
				Pushing force P.C.Board		
10	Bending	No mechanical	damage.	Reflow solder the capacitors on a		
				P.C.Board shown in Appendix 2a or		
				Appendix 2b and bend it for 1mm.		
				50 F R230		

No.	Item	Performance	Test or inspection method
No. 11	Solderability	Performance (C0402) Both end faces and the contact areas shall be covered with a smooth and bright solder coating with no more than a small amount of scattered imperfections such as pinholes or un-wetted or de-wetted areas. These imperfections shall not be concentrated in one area. (Others) New solder to cover over 75% of termination. 25% may have pin holes or rough spots but not concentrated in one spot. Ceramic surface of A sections shall not be exposed due to melting or shifting of termination material.	Test or inspection method Completely soak both terminations in solder at the following conditions. Solder: Sn-3.0Ag-0.5Cu or Sn-37Pb Temperature:245±5°C(Sn-3.0Ag-0.5Cu)
		A section	Time : 30 to 60s.

No.	Item		Performance			Test or inspection method	
12	Resistance to solder heat	External appearance	terminati	ions sh	allowed and all be covered at new solder.	Completely soak both terminations in solder at the following conditions.	
	пеас	Capacitance	least 60	/o WILII I	new solder.	260±5°C for 10±1s.	
		Capacitarios	Charact	teristics	Change from the value before test	Preheating condition	
			Class1	C H C0G	Capacitance drift within ±2.5% or ±0.25pF, whichever larger.	Temp. : 110 to 140°C Time : 30 to 60s.	
			Class2	J B X5R X6S X7R X7S X7T	± 7.5 %	Solder: Sn-3.0Ag-0.5Cu or Sn-37Pb Flux: Isopropyl alcohol (JIS K 8839) Rosin (JIS K 5902) 25% solid solution.	
		Q (Class1)	Meet the	initial	spec.	Leave the capacitors in ambient condition for 6 to 24h (Class1) or 24±2 (Class2) before measurement.	
		D.F. (Class2)	Meet the initial spec. Meet the initial spec.			Only reflow soldering applicable to C0402. Peak condition	
		Insulation Resistance					
		No insulation breakdown or other damage.			Temp.: 260°C Time: 5±0.5s. Preheating condition Temp.: 110 to 140°C Time: 30 to 60s.		
13	Vibration	External appearance	No mech	nanical	damage.	Reflow solder the capacitors on a P.C.Board shown in Appendix 1a or	
		Q (Class1)	Charact Class1 Class2 Meet the	C H COG J B X5R X6S X7R X7S X7T		Appendix 1b before testing. Vibrate the capacitors with amplitude of 1.5mm P-P changing the frequencies from 10Hz to 55Hz and back to 10Hz in about 1min. Repeat this for 2h each in 3 perpendicular directions.	
		D.F. (Class2)	Meet the initial spec.				

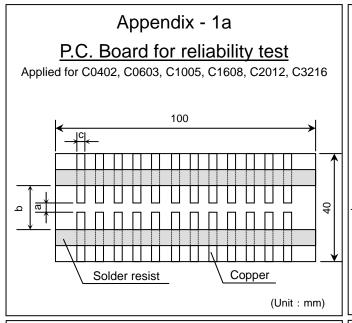
No.	Item		Performance			Test or inspection m	ethod		
14	Temperature cycle	External appearance	No mecha	No mechanical damage.			Reflow solder the capacitors on a P.C.Board shown in Appendix1a or		
		Capacitance	Change from the value before test				dix1b before testing. e the capacitors in th		
			Class1	C H C0G	±2.5% or ±0.25pF, whichever larger.		step1 through step 4 and repeat 5 consecutively.		
			Class2	JB X5R X6S X7R X7S	± 7.5 % ± 10 % ± 12.5 %	Leave the capacitors in ambient condition for 6 to 24h (Class 1) or 24±2h (Class 2) before measurement.	s 1) or		
			X7T			Step	Temperature(°C)	Time (min.)	
		Q (01===4)	Meet the initia	initial spec.		1	Min. operating temp. ±3	30 ± 3	
		(Class1)					Reference Temp.	2 - 5	
		D.F. (Class2)	Meet the initial spec	pec.	3	Max. operating temp. ±2	30 ± 2		
		Insulation Resistance	Meet the	Meet the initial spec.			Reference Temp.	2 - 5	
		Voltage No insul proof damage		ulation breakdown or other e.					

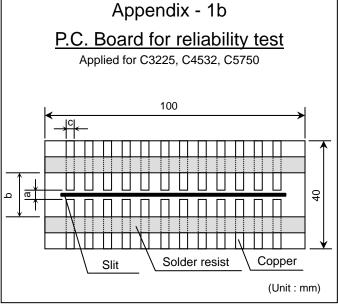
No.	Ito	em		Perfor	mance	Test or inspection method	
15	Moisture Resistance	External appearance	No mecha	nical da	amage.	Reflow solder the capacitors on a P.C.Board shown in Appendix 1a or	
	(Steady	Capacitance				Appendix 1b before testing.	
	State)	·	Characte	eristics	Change from the value before test	Leave at temperature 40 ± 2°C, 90 to	
			Class1		±5% or ±0.5pF, whichever larger.	95%RH for 500 +24,0h.	
			Class2	JB X5R X6S X7R X7S X7T	± 10 % ± 12.5 % ± 25 %	Leave the capacitors in ambient condition for 6 to 24h (Class1) or 24 ± 2h (Class2) before measurement.	
		Q					
		(Class1)	Rated Capacitance		e Q		
			30pF a	nd over	350 min.		
			•	nd over 30pF	275+5/2×C min.		
				Under	10pF	200+10×C min.	
				C : Rated	capacit	ance (pF)	
		D.F. (Class2)	200% of in	itial spe	ec. max.		
		Insulation	1,000ΜΩ ο	r 50MΩ·	μF min.		
	Resistance		(As for the	capaci	tors of rated		
			voltage 16	, 10V D	C and lower,		
			1,000 ΜΩ	or 10M	Ω·μF min.,)		
			whichever	smalle	r.		

No.	It	em		Perfo	rmance	Test or inspection method
	Moisture Resistance	External appearance	No mecha	nical da	amage.	Reflow solder the capacitors on a P.C.Board shown in Appendix1a or
		Capacitance				Appendix 1b before testing.
			Characte	eristics	Change from the value before test	Apply the rated voltage at temperature 40±2°C and 90 to
			Class1	C H C0G	±7.5% or ±0.75pF, whichever larger.	95%RH for 500 +24,0h.
				J B X5R X6S	± 10 %	Charge/discharge current shall not exceed 50mA.
			Class2	X7R X7S X7T	± 12.5 % ± 25 %	Leave the capacitors in ambient condition for 6 to 24h (Class1) or 24±2h (Class2) before measurement.
						Voltage conditioning (only for class 2)
		Q				
		(Class1)	Rated Ca	Rated Capacitance Q		Voltage treat the capacitors under
			30pF a	nd over	200 min.	testing temperature and voltage for 1
			Unde	r 30pF	100+10/3×C min.	hour.
			C : Rated	capacit	ance (pF)	Leave the capacitors in ambient condition for 24±2h before
		D.F. (Class2)	200% of ir	nitial spe	ec. max.	measurement. Use this measurement for initial
		Insulation	500MΩ or	25ΜΩ·	μF min.	value.
		Resistance			itors of rated	
			,		OC and lower, 500	
					in.,) whichever	

No.	It	tem		Perfo	rmance	Test or inspection method	
17	Life	fe External appearance		nical da	amage.	Reflow solder the capacitors on a P.C.Board shown in Appendix1a or Appendix 1b before testing.	
		Capacitance	Characte	eristics	Change from the value before test	Below the voltage shall be applied at maximum operating temperature ±2°C	
				Class1	C H C0G	±3% or ±0.3pF, whichever larger.	for 1,000 +48, 0h.
				JB		Applied voltage	
				X5R X6S	± 10 %	Rated voltage x2	
			Class2	X7R X7S	± 12.5 % ± 25 %	Rated voltage x1.5	
				X75 X7T	£ 25 %	Rated voltage x1.2	
						Rated voltage x1	
		Q					
		(Class1)	Rated Capacitance		- 	For information which product has which applied voltage, please contact	
				and over over unde	350 min.	with our sales representative.	
				OpF	275+5/2×C min.	Charge/discharge current shall not	
			Unde	er 10pF	200+10×C min.	exceed 50mA.	
			C : Rated	capacit	ance (pF)	Leave the capacitors in ambient	
		D.F.	200% of in	nitial spe	ec. max.	condition for 6 to 24h (Class1) or	
		(Class2)				24±2h (Class2) before measurement.	
		Insulation	1,000ΜΩ ο	r 50MΩ·	μF min.	Voltage conditioning (only for class 2)	
		Resistance	`		itors of rated	Voltage treat the capacitors under	
			_		O us min	testing temperature and voltage for 1	
			1,000 MΩ or 10MΩ· μ F min.,) whichever smaller.			hour.	
			wnichever smaller.			Leave the capacitors in ambient	
						condition for 24±2h before measurement.	
						Use this measurement for initial value.	

^{*}As for the initial measurement of capacitors (Class2) on number 8,12,13,14 and 15, leave capacitors at 150 -10,0°C for 1 hour and measure the value after leaving capacitors for $24 \pm 2h$ in ambient condition.





Appendix - 2b

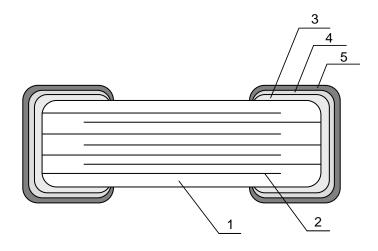
Appendix - 2a P.C. Board for bending test Applied for C0402, C0603, C1005

P.C. Board for bending test

Applied for C1608, C2012, C3216, C3225, C4532, C5750

Dimensions (mm)			
а	b	С	
0.2	0.8	0.2	
0.3	0.8	0.3	
0.4	1.5	0.5	
1.0	3.0	1.2	
1.2	4.0	1.65	
2.2	5.0	2.0	
2.2	5.0	2.9	
3.5	7.0	3.7	
4.5	8.0	5.6	
	a 0.2 0.3 0.4 1.0 1.2 2.2 2.2 3.5	a b 0.2 0.8 0.3 0.8 0.4 1.5 1.0 3.0 1.2 4.0 2.2 5.0 2.2 5.0 3.5 7.0	

9. INSIDE STRUCTURE AND MATERIAL



No	NAME	MATERIAL				
No.	INAIVIE	Class1	Class2			
1	Dielectric	CaZrO ₃	BaTiO ₃			
2	Electrode	Nickel (Ni)				
3		Copper (Cu)				
4	Termination	Nickel (Ni)				
5		Tin (Sn)				

10. RECOMMENDATION

As for C3225, C4532 and C5750 types, It is recommended to provide a slit (about 1mm wide) in the board under the components to improve washing Flux. And please make sure to dry detergent up completely before.

11. SOLDERING CONDITION

As for C0402, C0603, C1005, C3225, C4532 and C5750 types, reflow soldering only.

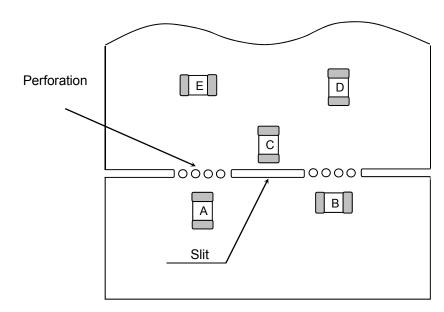
12. CAUTION

No.	Process	Condition					
1	Operating Condition (Storage, Transportation)	 1-1. Storage 1) The capacitors must be stored in an ambient temperature of 5 to 40°C with a relative humidity of 20 to 70%RH. The products should be used within 6 months upon receipt. 					
		2) The capacitors must be operated and stored in an environment free of dew condensation and these gases such as Hydrogen Sulphide, Hydrogen Sulphate, Chlorine, Ammonia and sulfur.3) Avoid storing in sun light and falling of dew.					
		4) Do not use capacitors under high humidity and high and low atmospheric pressure which may affect capacitors reliability.					
		5) Capacitors should be tested for the solderability when they are stored for long time.					
		1-2. Handling in transportation					
		In case of the transportation of the capacitors, the performance of the capacitors					
		may be deteriorated depending on the transportation condition. (Refer to JEITA RCR-2335C 9.2 Handling in transportation)					
2	Circuit design Li Caution	2-1. Operating temperature Operating temperature should be followed strictly within this specification, especially be careful with maximum temperature.					
		Do not use capacitors above the maximum allowable operating temperature.					
		2) Surface temperature including self heating should be below maximum operating temperature.					
		(Due to dielectric loss, capacitors will heat itself when AC is applied. Especially at high frequencies around its SRF, the heat might be so extreme that it may damage itself or the product mounted on. Please design the circuit so that the maximum temperature of the capacitors including the self heating to be below the maximum allowable operating temperature. Temperature rise at capacitor surface shall be below 20°C)					
		 3) The electrical characteristics of the capacitors will vary depending on the temperature. The capacitors should be selected and designed in taking the temperature into consideration. 2-2 Operating voltage 1) Operating voltage across the terminals should be below the rated voltage. 					
		When AC and DC are super imposed, V_{0-P} must be below the rated voltage. AC or pulse with overshooting, V_{P-P} must be below the rated voltage. AC or pulse with overshooting, V_{P-P} must be below the rated voltage.					
		——— (3), (4) and (5)					
		When the voltage is started to apply to the circuit or it is stopped applying, the irregular voltage may be generated for a transit period because of resonance or switching. Be sure to use the capacitors within rated voltage containing these Irregular voltage.					
		Voltage (1) DC voltage (2) DC+AC voltage (3) AC voltage					
		Positional Measurement (Rated voltage) Vo-P 0					
		Voltage (4) Pulse voltage (A) (5) Pulse voltage (B)					
		Positional Measurement (Rated voltage)					

No.	Process	Condition								
2	Circuit design Caution	,	 Even below the rated voltage, if repetitive high frequency AC or pulse is applied, the reliability of the capacitors may be reduced. 							
		 3) The effective capacitance will vary depending on applied DC and AC voltages. The capacitors should be selected and designed in taking the voltages into consideration. 2-3. Frequency When the capacitors (Class 2) are used in AC and/or pulse voltages, the capacitors may vibrate themselves and generate audible sound. 								
3	Designing P.C. board	The amount of solo capacitors.	ler at the ter	minations	has a d	direct ef	fect on the re	eliability of the		
		The greater the the more likely shape and size terminations.	that it will bre of the solder	ak. Whe	n desigi ave pro	ning a P	.C. board, de ount of solder	termine the on the		
		Avoid using cor solder land for a			ultiple te	erminati	ons and prov	ide individual		
		3) Size and recom	nmended land	d dimensio	ns.					
				Chip capa	citor s	Solder lar	nd			
			c B	A			Sold	er resist		
		• Flow solde	ering				(mm)			
		Type Symbol	C1608 (CC0603)		012 0805)		3216 1206)			
		A	0.7 - 1.0		- 1.3		- 2.5			
		B C	0.8 - 1.0 0.6 - 0.8		- 1.2 - 1.1		- 1.3 - 1.3			
			0.0 0.0	0.0		1.0				
		· Reflow sol		Т	<u> </u>		T	(mm)		
		Type Symbol	C0402 (CC01005)	C0603 (CC020 ⁻		1005 C0402)	C1608 (CC0603)	C2012 (CC0805)		
		A								
		B								
		C	0.15 - 0.25	0.25 - 0.3	5 0.4	1 - 0.6	0.6 - 0.8	0.9 - 1.2		
		Type Symbol	C3216 (CC1206)	C3225 (CC1210		4532 C1812)	C5750 (CC2220)			
		A	2.0 - 2.4	2.0 - 2.4		1 - 3.7	4.1 - 4.8			
		B	1.0 - 1.2	1.0 - 1.2	_	2 - 1.4	1.2 - 1.4			
		C	1.1 - 1.6	1.9 - 2.5	2.4	1 - 3.2	4.0 - 5.0			

No.	Process			Condition	
3	Designing P.C.board	4)	Recommende	ed chip capacitor layout is as follo	wing.
				Disadvantage against bending stress	Advantage against bending stress
				Perforation or slit	Perforation or slit
			Mounting face		
				Break P.C.board with mounted side up.	Break P.C.board with mounted side down.
				Mount perpendicularly to perforation or slit	Mount in parallel with perforation or slit
			Chip	Perforation or slit	Perforation or slit
			arrangement (Direction)		
				Closer to slit is higher stress	Away from slit is less stress
			Distance from slit	$\begin{array}{c c} & \ell_1 \\ \hline & \vdots \\ & \ell_2 \end{array}$	$\begin{array}{c c} & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & \\ & & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & &$
				(21 - 22)	(~1 -~2)

No. Process Condition 3 Designing P.C.board 5) Mechanical stress varies according to location of chip capacitors on the P.C.board.



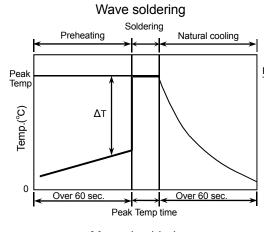
The stress in capacitors is in the following order. A > B = C > D > E

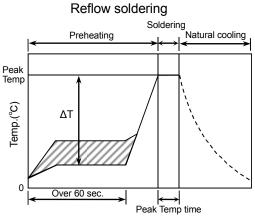
6) Layout recommendation

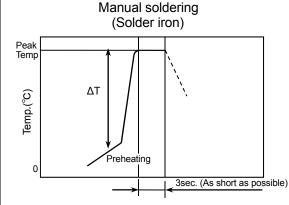
Example	Use of common solder land	Soldering with chassis	Use of common solder land with other SMD
Need to avoid	Lead wire Chip Solder PCB Adhesive Solder land	Chassis Excessive solder	Excessive solder Missing Solder land
Recommendation	Solder resist Lead wire	Solder resist ℓ^2 $\ell^2 > \ell^1$	Solder resist

No.	Process	Condition							
4	Mounting	 4-1. Stress from mounting head If the mounting head is adjusted too low, it may induce excessive stress in the chip capacitor to result in cracking. Please take following precautions. 1) Adjust the bottom dead center of the mounting head to reach on the P.C.board surface and not press it. 2) Adjust the mounting head pressure to be 1 to 3N of static weight. 3) To minimize the impact energy from mounting head, it is important to provide support from the bottom side of the P.C.board. See following examples. 							
		<u> </u>		commended	Recommended				
		Single sided mounting		Crack	Support pin				
		Double-si des mounting	Solder peeling	Crack	Support pin				
		to cause crack.	Please contro		nechanical impact on the capacitors assion of the centering jaw and placement of it.				
		4-2. Amount of ad	lhesive	→ a ← → a ←					
				c c					
			Example : 0	C2012 (CC0805), C	3216 (CC1206)				
			а	0.2mm r	min.				
			b	70 - 100	<u>·</u>				
			C	Do not touch the	solder land				

No.	Process	Condition
5	Soldering	5-1. Flux selection Although highly-activated flux gives better solderability, substances which increase activity may also degrade the insulation of the chip capacitors. To avoid such degradation, it is recommended following.
		 It is recommended to use a mildly activated rosin flux (less than 0.1wt% chlorine). Strong flux is not recommended. Excessive flux must be avoided. Please provide proper amount of flux. When water-soluble flux is used, enough washing is necessary. Recommended soldering profile by various methods
		2. Recommended condening prome by various methods







APPLICATION

As for C1608, C2012 and C3216 applied to wave soldering and reflow soldering. As for C0402, C0603, C1005, C3225, C4532, C5750 applied only to reflow soldering.

*As for peak temperature of manual soldering, please refer"5-6. Solder repair by solder iron"

5-3. Recommended soldering peak temp and peak temp duration

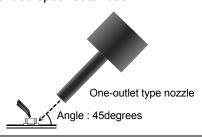
	Wave soldering		Reflow soldering		
	Peak temp(°C) Duration(sec.)		Peak temp(°C)	Duration(sec.)	
Sn-Pb Solder	250 max.	3 max.	230 max.	20 max.	
Lead Free Solder	260 max.	5 max.	260 max.	10 max.	

Recommended solder compositions Sn-37Pb (Sn-Pb solder) Sn-3.0Ag-0.5Cu (Lead Free Solder)

No.	Process		Condi	tion	
5	Soldering	5-4. Avoiding thermal shock			
Ū	goldoning	1) Preheating condition			
		Soldering		Туре	Temp. (°C)
		Wave soldering	C1608, C201	2, C3216	ΔT ≤150
		Reflow soldering	C0402, C060 C2012, C321	3, C1005, C160 6	8, ΔT ≤150
			C3225, C453	2, C5750	ΔT ≤130
		Manual soldering	C0402, C060 C2012, C321	3, C1005, C160 6	8, ΔT ≤150
			C3225, C453	2, C5750	ΔT ≤130
		Cooling condition Natural cooling using air cleaning, the temperature 5-5. Amount of solder Excessive solder will induce changes and it may resul capacitors from the P.C.boa	e difference (Δ٦ e higher tensile t in chip crack	「) must be less t force in chip cap	pacitors when temperature
		Excessive solder			her tensile force in chip pacitor to cause crack
		Adequate			n amount n amount
		Insufficient solder		con	v robustness may cause stact failure or chip sacitor comes off the s.board.
		5-6. Solder repair by solder in	on		
		may cause a crack in the	iron varies by its mperature, the q chip capacitors. temp. before sol recommended	uicker the operat dering and keep condition. (Please	the peak temp and time in e preheat the chip
		Recommended solder iro	on condition (Sr	n-Pb Solder and	Lead Free Solder)
		Туре	Temp. (°C)	Wattage (W)	Shape (mm)
		C0603(CC0201) C1005(CC0402) C1608(CC0603) C2012(CC0805) C3216(CC1206)	350 max.	20 max.	φ 3.0 max.
		C3225(CC1210) C4520(CC1808) C4532(CC1812) C5750(CC2220)	280 max.		

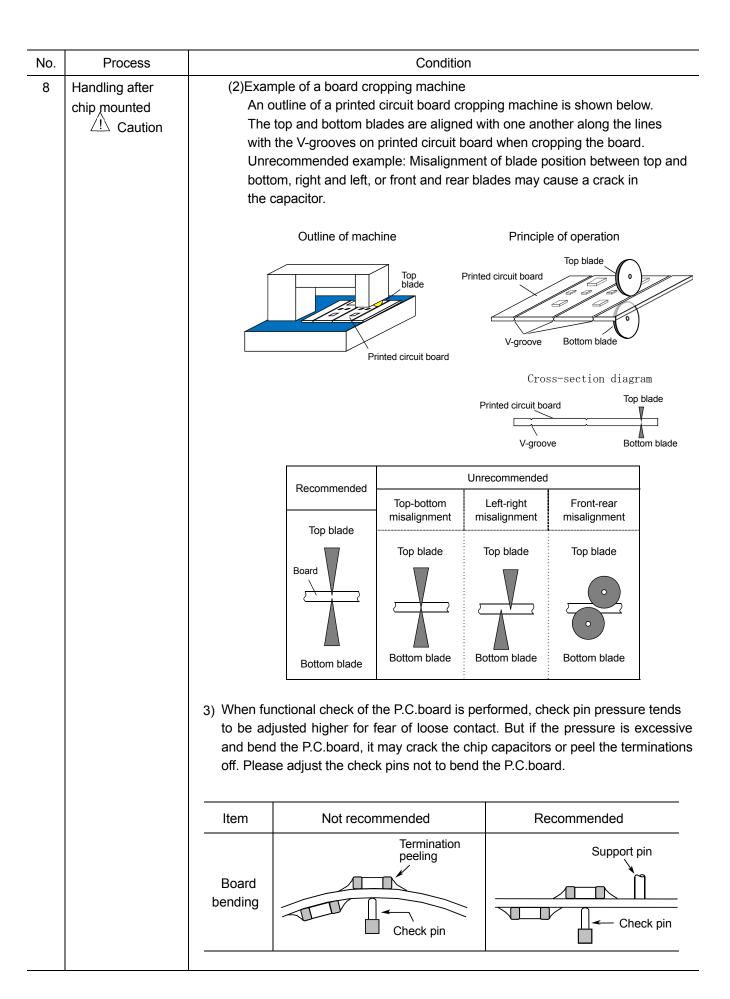
No.	Process		Condition
5	Soldering		ing iron with ceramic dielectric of chip capacitors may the ceramic dielectric and the terminations by solder
		3) It is not recommended to re	use dismounted capacitors. (For soft electrode)
		(also called a "blower") rathe	ay possibly be reduced by using a spot heater
		capacitor compared to usin uniformly with a small heat stress caused by quick hea Moreover, where ultra-sma	ater may suppress the occurrence of cracks in the ig a soldering iron. A spot heater can heat up a capacitor gradient which leads to lower thermal sting and cooling or localized heating. Il capacitors are mounted close together on a printed circuit of heater can eliminate the risk of direct contact between and a capacitor.
		may occur due to heat stress occurrence. Keep more than 5mm betw. The blower temperature of The airflow shall be set as a The diameter of the nozzle standard and common. Duration of blowing hot air C2012(CC0805), C3216(C C4520(CC1808), C4532(C the capacitor and melting to The angle between the nozorder to work easily and to	is recommended to be 2mm(one-outlet type). The size is is recommended to be 10s or less C1608(CC0603), C1206) and 30s or less for C3225(CC1210), C1812) and C5750(CC2220), considering surface area of emperature of solder. It is recommended to be 45degrees in avoid partial area heating. a soldering iron, preheating reduces thermal stress on
		• Recommended rework co	andition (Consult the component manufactures for details.)
		Distance from nozzle	5mm and over
		Nozzle angle	45degrees
		Nozzle temp.	400°C and less
		Airflow	Set as weak as possible (The airflow shall be the minimum value necessary for solder to melt in the Conditions mentioned above.)
		Nozzle diameter	φ2mm (one-outlet type)
		Blowing duration	10s and less (C1608[CC0603], C2012[CC0805], C3216[CC1206]) 30s and less (C3225[CC1210], C4532[CC1812], C5750[CC2220])

• Example of recommended spot heater use



No.	Process	Condition
5	Soldering	 Amount of solder should be suitable to from a proper fillet shape. Excess solder causes mechanical and thermal stress on a capacitor and results in cracks. Insufficient solder causes weak adherence of the capacitor to the substrate and may result in detachment of a capacitor and deteriorate reliability of the printed wiring board. See the example of appropriate solder fillet shape for 5-5. Amount of solder. 5-8. Sn-Zn solder
		Sn-Zn solder affects product reliability.
		Please contact TDK in advance when utilize Sn-Zn solder.
		5-9. Countermeasure for tombstone
		The misalignment between the mounted positions of the capacitors and the land
		patterns should be minimized. The tombstone phenomenon may occur especially
		the capacitors are mounted (in longitudinal direction) in the same direction of the reflow soldering.
		(Refer to JEITA RCR-2335C Annex A (Informative) Recommendations to prevent the
		tombstone phenomenon)
6	Cleaning	 If an unsuitable cleaning fluid is used, flux residue or some foreign articles may stick to chip capacitors surface to deteriorate especially the insulation resistance. If cleaning condition is not suitable, it may damage the chip capacitors.
		2)-1. Insufficient washing (1) Terminal electrodes may corrode by Halogen in the flux.
		(2) Halogen in the flux may adhere on the surface of capacitors, and lower the insulation resistance.
		(3) Water soluble flux has higher tendency to have above mentioned problems (1) and (2).
		2)-2. Excessive washing
		When ultrasonic cleaning is used, excessively high ultrasonic energy output can affect the connection between the ceramic chip capacitor's body and the terminal electrode. To avoid this, following is the recommended condition.
		Power : 20W/
		2)-3. If the cleaning fluid is contaminated, density of Halogen increases, and it may bring the same result as insufficient cleaning.

No.	Process	Condition
7	Coating and molding of the P.C. board	 When the P.C. board is coated, please verify the quality influence on the product. Please verify carefully that there is no harmful decomposing or reaction gas emission during curing which may damage the chip capacitors. Please verify the curing temperature.
8	Handling after chip mounted Caution	1) Please pay attention not to bend or distort the P.C.board after soldering in handling otherwise the chip capacitors may crack. Bend Twist Twist 2) Printed circuit board cropping should not be carried out by hand, but by using the proper tooling. Printed circuit board cropping should be carried out using a board cropping jig as shown in the following figure or a board cropping apparatus to prevent inducing mechanical stress on the board. (1)Example of a board cropping jig Recommended example: The board should be pushed from the back side, close to the cropping jig so that the board is not bent and the stress applied to the capacitor is compressive. Unrecommended example: If the pushing point is far from the cropping jig and the pushing direction is from the front side of the board, large tensile stress is applied to the capacitor, which may cause cracks.
		Outline of jig Recommended Unrecommended Printed circuit board Printed circuit board Slot Slot Slot Slot Slot Slot Unrecommended Unrecommended Direction of load or load



No.	Process	Condition
9	Handling of loose chip capacitors	1) If dropped the chip capacitors may crack. Once dropped do not use it. Especially, the large case sized chip capacitors are tendency to have cracks easily, so please handle with care. Floor 2) Piling the P.C.board after mounting for storage or handling, the corner of the P.C. board may hit the chip capacitors of another board to cause crack. P.C.board Crack
10	Capacitance aging	The capacitors (Class 2) have aging in the capacitance. They may not be used in precision time constant circuit. In case of the time constant circuit, the evaluation should be done well.
11	Estimated life and estimated failure rate of capacitors	As per the estimated life and the estimated failure rate depend on the temperature and the voltage. This can be calculated by the equation described in JEITA RCR-2335C Annex F(Informative) Calculation of the estimated lifetime and the estimated failure rate (Voltage acceleration coefficient : 3 multiplication rule, Temperature acceleration coefficient : 10°C rule) The failure rate can be decreased by reducing the temperature and the voltage but they will not be guaranteed.

No.	Process	Condition
12	Caution during operation of equipment	A capacitor shall not be touched directly with bare hands during operation in order to avoid electric shock. Electric energy held by the capacitor may be discharged through the human body when touched with a bare hand. Even when the equipment is off, a capacitor may stay charged. The capacitor should be handled after being completely discharged using a resistor.
		2) The terminals of a capacitor shall not be short-circuited by any accidental contact with a conductive object. A capacitor shall not be exposed to a conductive liquid such as an acid or alkali solution. A conductive object or liquid, such as acid and alkali, between the terminals may lead to the breakdown of a capacitor due to short circuit
		 Confirm that the environment to which the equipment will be exposed during transportation and operation meets the specified conditions. Do not to use the equipment in the following environments. Environment where a capacitor is spattered with water or oil Environment where a capacitor is exposed to direct sunlight Environment where a capacitor is exposed to Ozone, ultraviolet rays or radiation Environment where a capacitor exposed to corrosive gas(e.g. hydrogen sulfide, sulfur dioxide, chlorine. ammonia gas etc.) Environment where a capacitor exposed to vibration or mechanical shock exceeding the specified limits. Atmosphere change with causes condensation
13	Others Caution	The products listed on this specification sheet are intended for use in general electronic equipment (AV equipment, telecommunications equipment, home appliances, amusement equipment, computer equipment, personal equipment, office equipment, measurement equipment, industrial robots) under a normal operation and use condition. The products are not designed or warranted to meet the requirements of the applications listed below, whose performance and/or quality require a more stringent level of safety or reliability, or whose failure, malfunction or trouble could cause serious damage to society, person or property. Please understand that we are not responsible for any damage or liability caused by use of the products in any of the applications below or for any other use exceeding the range or conditions set forth in this specification sheet. If you intend to use the products in the applications listed
		below or if you have special requirements exceeding the range or conditions set forth in this specification, please contact us. (1) Aerospace/Aviation equipment (2) Transportation equipment (cars, electric trains, ships, etc.) (3) Medical equipment (Excepting Pharmaceutical Affairs Law classification Class1, 2) (4) Power-generation control equipment (5) Atomic energy-related equipment (6) Seabed equipment (7) Transportation control equipment (8) Public information-processing equipment (9) Military equipment (10) Electric heating apparatus, burning equipment (11) Disaster prevention/crime prevention equipment (12) Safety equipment (13) Other applications that are not considered general-purpose applications When designing your equipment even for general-purpose applications, you are kindly requested to take into consideration securing protection circuit/device or providing backup circuits in your equipment.

13. PACKAGING LABELI

Packaging shall be done to protect the components from the damage during transportation and storing, and a label which has the following information shall be attached.

- 1) Inspection No.
- 2) TDK P/N
- 3) Customer's P/N
- 4) Quantity

*Composition of Inspection No.

Example
$$\underline{F}$$
 $\underline{7}$ \underline{A} - \underline{OO} - \underline{OOO} (a) (b) (c) (d) (e)

- a) Line code
- b) Last digit of the year
- c) Month and A for January and B for February and so on. (Skip I)
- d) Inspection Date of the month.
- e) Serial No. of the day

14. BULK PACKAGING QUANTITY

Total number of components in a plastic bag for bulk packaging: 1,000pcs. As for C0402, C0603 and C1005 types, not available for bulk packaging.

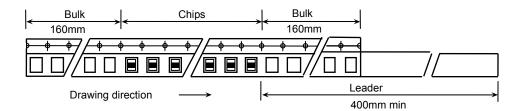
15. TAPE PACKAGING SPECIFICATION

1. CONSTRUCTION AND DIMENSION OF TAPING

1-1. Dimensions of carrier tape

Dimensions of paper tape shall be according to Appendix 3, 4. Dimensions of plastic tape shall be according to Appendix 5, 6.

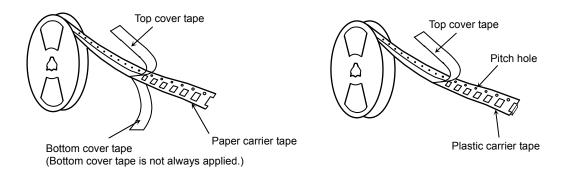
1-2. Bulk part and leader of taping



1-3. Dimensions of reel

Dimensions of Ø178 reel shall be according to Appendix 7, 8. Dimensions of Ø330 reel shall be according to Appendix 9, 10.

1-4. Structure of taping

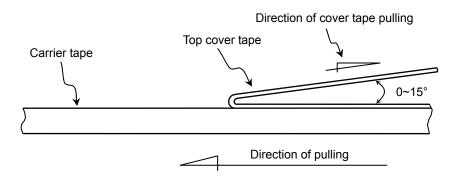


2. CHIP QUANTITY

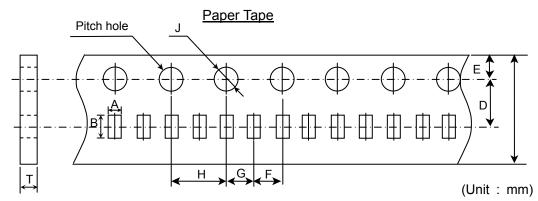
Typo	Thickness	Taping	Chip quar	ntity (pcs.)	
Type	of chip	Material	φ178mm reel	φ330mm reel	
C0402	0.20 mm	Paper	20,000	-	
C0603	0.30 mm	Paper	15,000	-	
C1005	0.50 mm	Paper	10,000	50,000	
C1608	0.80 mm	Paper	4,000	10,000	
	0.60 mm	Paper	4 000		
C2012	0.85 mm	Paper or Plastic	4,000	10,000	
	1.25 mm	Plastic	2,000		
	0.60 mm	Paper	4 000		
	0.85 mm	Paper or Plastic	4,000	10,000	
C3216	1.15 mm			10,000	
	1.30 mm	Plastic	2,000		
	1.60 mm			8,000	
	1.15 mm		2,000	10,000	
	1.25 mm		2,000	8,000	
	1.30 mm				
C3225	1.60 mm	Plastic			
	2.00 mm		1,000	5,000	
	2.30 mm				
	2.50 mm				
	1.60 mm		1,000	- 3,000	
	2.00 mm		1,000		
C4532	2.30 mm	Plastic			
04002	2.50 mm	1 lastic	500		
	2.80 mm		300	2,000	
	3.20 mm			2,000	
	2.00 mm				
C5750	2.30 mm	Plastic	500	3,000	
00700	2.50 mm	i idolio	300		
	2.80 mm			2,000	

3. PERFORMANCE SPECIFICATIONS

3-1. Fixing peeling strength (top tape) 0.05-0.7N. (See the following figure.)



- 3-2. Carrier tape shall be flexible enough to be wound around a minimum radius of 30mm with components in tape.
- 3-3. The missing of components shall be less than 0.1%
- 3-4. Components shall not stick to fixing tape.
- 3-5. The fixing tapes shall not protrude beyond the edges of the carrier tape not shall cover the sprocket holes.



Symbol Type	А	В	С	D	E	F
C0402 (C01005)	(0.25)	(0.45)				
C0603 (CC0201)	(0.38)	(0.68)	8.00 ± 0.30	3.50 ± 0.05	1.75 ± 0.10	2.00 ± 0.05
C1005 (CC0402)	(0.65) *1(0.73) *2(0.80)	(1.15) *1(1.23) *2(1.30)				

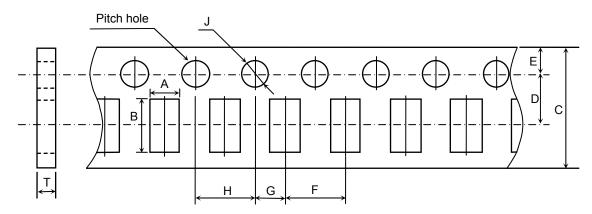
Symbol Type	G	Н	J	Т
C0402 (C01005)	2.00 ± 0.05	4.00 ± 0.10		0.29 min.
C0603 (CC0201)			Ø 1.5 +0.10	0.40 min.
C1005				0.60± 0.05 *1 0.68± 0.05
(CC0402)				*2 0.75± 0.05

^{*} The values in the parentheses () are for reference.

^{*1} Applied to thickness, 0.5 ± 0.10 mm and 0.50+0.15/-0.10mm products.

^{*2} Applied to thickness, 0.50+0.20/-0.10mm products.

Paper Tape

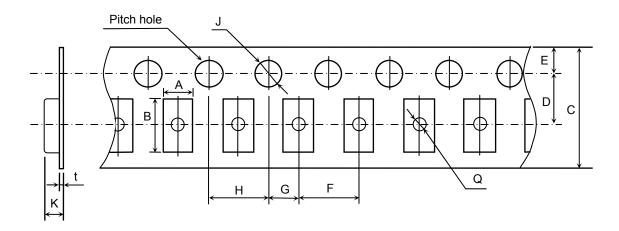


(Unit: mm)

Symbol					_	_
Туре	Α	В	С	D	E	F
C1608 (CC0603)	(1.10)	(1.90)				
C2012 (CC0805)	(1.50)	(2.30)	8.00 ± 0.30	3.50 ± 0.05	1.75 ± 0.10	4.00 ± 0.10
C3216 (CC1206)	(1.90)	(3.50)				
Symbol Type	G	Н	J	Т		
C1608 (CC0603)						
C2012 (CC0805)	2.00 ± 0.05	4.00 ± 0.10	Ø 1.5 +0.10	1.20max.		
C3216 (CC1206)						

^{*} The values in the parentheses () are for reference.

Plastic Tape



(Unit:mm)

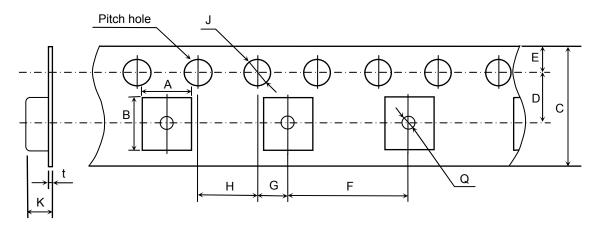
Symbol Type	А	В	С	D	E	F
C2012 (CC0805)	(1.50)	(2.30)	0.00 . 0.00	2.50 + 0.05		
C3216 (CC1206)	(1.90)	(3.50)	8.00 ± 0.30 [12.0 ± 0.30]	3.50 ± 0.05 $[5.50 \pm 0.05]$	1.75 ± 0.10	4.00 ± 0.10
C3225 (CC1210)	(2.90)	(3.60)	[12.0 ± 0.00]	[0.00 ± 0.00]		
Symbol Type	G	Н	J	К	t	Q
C2012 (CC0805)						
C3216 (CC1206)	2.00 ± 0.05	4.00 ± 0.10	Ø 1.5 +0.10	3.20max.	0.60max.	Ø 0.50 min.
C3225 (CC1210)						

^{*} The values in the parentheses () are for reference.

^{*} As for 2.5mm thickness products, apply values in the brackets [].

^{*} Exceptionally no hole in the cavity is applied. Please inquire if hole in cavity is mandatory.

Plastic Tape

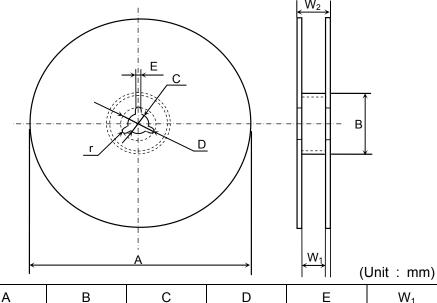


(Unit:mm)

Symbol Type	А	В	С	D	E	F
C4532 (CC1812)	(3.60)	(4.90)	12.0 ± 0.30	5 50 + 0 05	1.75 ± 0.10	8.00 ± 0.10
C5750 (CC2220)	(5.40)	(6.10)	12.0 \pm 0.30 5.50 \pm 0.05		1.75 ± 0.10	6.00 ± 0.10
Symbol Type	G	Н	J	К	t	Q
C4532 (CC1812)	2.00 ± 0.05	4.00 ± 0.10	Ø 1.5 +0.10	6.50 max.	0.60 max.	Ø 1.50 min.
C5750	2.00 F 0.05	4.00 £ 0.10	0 1.5	0.50 IIIax.	0.00 Illax.	Ø 1.50 IIIII.

^{*} The values in the parentheses () are for reference.

C0402, C0603, C1005, C1608, C2012, C3216, C3225 (As for C3225 type, any thickness of the item except 2.5mm) (Material : Polystyrene)

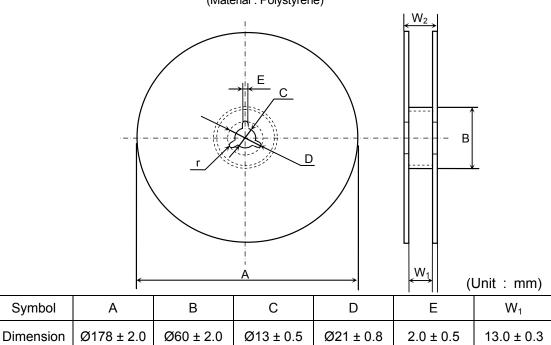


Symbol	Α	В	С	D	E	W_1
Dimension	Ø178 ± 2.0	Ø60 ± 2.0	Ø13 ± 0.5	Ø21 ± 0.8	2.0 ± 0.5	9.0 ± 0.3

Symbol	W_2	r
Dimension	13.0 ± 1.4	1.0

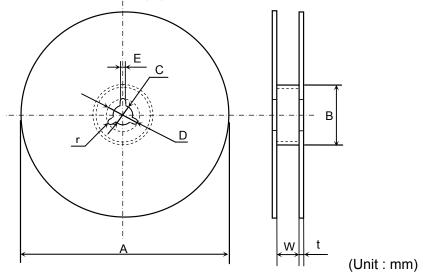
Appendix 8

C3225, C4532, C5750 (As for C3225 type, applied to 2.5mm thickness products) (Material : Polystyrene)



Symbol	W ₂	r
Dimension	17.0 ± 1.4	1.0

C1005, C1608, C2012, C3216, C3225 (As for C3225 type, any thickness of the item except 2.5mm) (Material : Polystyrene)

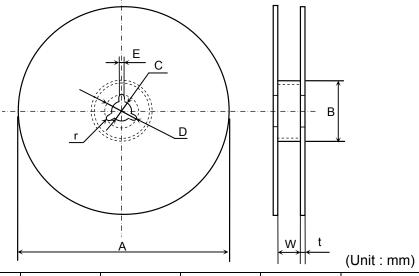


Symbol	А	В	С	D	E	W
Dimension	Ø382 max. (Nominal Ø330)	Ø50 min.	Ø13 ± 0.5	Ø21 ± 0.8	2.0 ± 0.5	10.0 ± 1.5

Symbol	t	r
Dimension	2.0 ± 0.5	1.0

Appendix 10

C3225, C4532, C5750 (As for C3225 type, applied to 2.5mm thickness products) (Material : Polystyrene)



Symbol	А	В	С	D	E	W
Dimension	Ø382 max. (Nominal Ø330)	Ø50 min.	Ø13 ± 0.5	Ø21 ± 0.8	2.0 ± 0.5	14.0 ± 1.5

Symbol	t	r
Dimension	2.0 ± 0.5	1.0

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2225J2500824KXT CCR07CG103KM CGA2B2C0G1H010C CGA2B2C0G1H040C CGA2B2C0G1H050C CGA2B2C0G1H060D

CGA2B2C0G1H070D CGA2B2C0G1H151J CGA2B2C0G1H1R5C CGA2B2C0G1H2R2C CGA2B2C0G1H3R3C CGA2B2C0G1H680J

CGA4J2X7R2A104K