

SPECIFICATION APPROVAL

SPEC. No. E600NAA00843

DATE: May.12.2017

MESSRS: BEIJING FOXTAR ELECTRONICS CO. , LTD.

CUSTOMER'S PRODUCT NAME:	
TDK PRODUCT NAME: NTCG20 series	
THIS SPECIFICATION IS:	
<input type="checkbox"/> FULLY APPROVED <input type="checkbox"/> DENIED <input type="checkbox"/> APPROVED UNDER THE FOLLOWING CONDITIONS	
SIGNATURE: _____	DATE: _____
NAME(PRINTED): _____	_____
TITLE: _____	_____

TDK Corporation				
<u>SALES</u> Electronic components Sales & Marketing Group		<u>ENGINEERING</u> TDK-EPC Corporation Piezo & Protection Devices Business Group		
APPROVED	Person in Charge	APPROVED	CHECKED	Person in Charge
		<i>S. Hakeinuma</i>	<i>S. sasaki</i>	<i>M. Sato</i>

1. Scope

This specification shall be applied to chip type NTC thermistors delivered to BEIJING FOXTAR ELECTRONICS CO. , LTD..

2. Description

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Revision records

Rev.	Date.	Revised by	Revision
Business Group		Date	Spec No.
Piezo & Protection Device Business Group		May.12.2017	E600NAA00843

INSTRUCTIONS FOR CHIP NTC THERMISTOR

Please read these instructions before using the chip NTC thermistors.

SAFETY WARNING

Pay careful attention to all warnings and operate only in accordance with safety specifications. Improper use may cause smoke emissions or fire.



WARNING

- Confirm your environment of usage and mounting for the NTC thermistors, and do not operate the NTC thermistors beyond the rating and performance specified in this specification and catalog.
 - Do not operate the NTC thermistors beyond the temperature range specified in this specification.
 - Do not operate the NTC thermistors beyond the maximum rated power specified in this specification.
 - Do not apply instantly and directly the loads of 5mW and over by the constant voltage power supply, since NTC thermistors may burn by thermal runaway mode.
 - Pay careful attention to the power applied to NTC thermistors, because the thermistor that decreased the resistance by self heating may damage to the electric machines.
 - In case of the electrical machine that consumers can directly touch the NTC thermistors by bare hands, be thorough about warning that customers do not let touch to the NTC thermistors.
 - Store the NTC thermistors in the packages that are used for shipment by TDK and keep the storage facility in the following environmental conditions:
 - Temperature : -10 ~ +40°C
 - Relative humidity : 75% or below
 - To avoid direct sunshine, rapid temperature change, corrosive gas and dusty place.
 - Not to apply the load stress.
 - Do not use NTC thermistors if stored over 6 months.
 - Confirm reliability of the NTC thermistors when adding sealant which uses molding materials, adhesive materials, resin, etc. Furthermore, do not exceed temperature limits specified in this specification when curing these materials.
 - Do not apply the vibration, shock (dropping etc.) and force that are beyond the specified limits in this specification.
 - Do not use or store in a wet or humid environment over 85%RH, because it damages to the NTC thermistors.
 - Do not use or store in the following environmental conditions:
 - Corrosive gas(Cl₂, NH₃, SO_x, NO_x, H₂S etc.)
 - Electrolyte, water, salt water, etc.
 - Acidic, alkaline and solvent
 - Dusty place
 - To prevent failure or damage to the NTC thermistors, pay careful attention for mounting as follows;
 - Do not warp or twist the board before and after mounting and soldering the NTC thermistors.
 - Design bilaterally symmetric land size.
 - Do not use the NTC thermistors that dropped or that detached from the board.
 - Do not use excessive amount of solder.
 - Use resin or molding materials that hydrogen(H₂) doesn't occur, if these are covered on the NTC thermistors.
 - The products listed on this specification sheet are intended for use in general electronic equipment (AV equipment, telecommunications equipment, home appliances, amusement equipment, computer equipment, personal equipment, office equipment, measurement equipment, industrial robots) under a normal operation and use condition.
- The products are not designed or warranted to meet the requirements of the applications listed below, whose performance and/or quality require a more stringent level of safety or reliability, or whose failure, malfunction or trouble could cause serious damage to society, person or property. If you intend to use the products in the applications listed below or if you have special requirements exceeding the range or conditions set forth in this catalog, please contact us.
- (1) Aerospace/Aviation equipment
 - (2) Transportation equipment (cars, electric trains, ships, etc.)
 - (3) Medical equipment
 - (4) Power-generation control equipment
 - (5) Atomic energy-related equipment
 - (6) Seabed equipment
 - (7) Transportation control equipment
 - (8) Public information-processing equipment
 - (9) Military equipment
 - (10) Electric heating apparatus, burning equipment
 - (11) Disaster prevention/crime prevention equipment
 - (12) Safety equipment
 - (13) Other applications that are not considered ge etc.

When designing your equipment even for general-purpose applications, you are kindly requested to take into consideration securing protection circuit/device or providing backup circuits in your equipment.

PLP Spec. No.

NTC095G01

3. Part code designation

(Example)

NTC G 20 3N H 103 J I □□
 (1) (2) (3) (4) (5) (6) (7) (8) (9)

- | | |
|--|---|
| (1) Product category | NTC : NTC thermistor |
| (2) Structure | G : Chip type and lead free series |
| (3) Dimensions | 20 : 2.0×1.25×0.7mm |
| (4) Resistance temperature characteristic (B constant) | 3N : 3650K |
| (5) Resistance temperature characteristic tolerance | J : ±5%
H : ± 3%,
G : ± 2%,
F : ± 1% |
| (6) Nominal resistance | 103 : 10×10 ³ [Ω] |
| (7) Nominal resistance tolerance | J : ±5%
H : ±3%
F : ±1% |
| (8) Types of packaging | T : Taping
B : Bulk |
| (9) TDK special code | |
- The TDK special code does not announce by the circumstances of our company managerially and may be changed.

4. Operating temperature range
-40 ~ 125°C

5. Test condition

Unless otherwise specified, tests and measurements shall be made at the standard condition; temperature 15 ~ 35°C , humidity 25 ~ 85%RH. and atmospheric pressure 86 ~ 106kPa

Regarding the tests shown section 6.3 and 6.4, the samples shall be mounted glass-epoxy board by reflow soldering (not to be applicable for solderability nor soldering to heat).

The measurement shall be performed after the tested samples are left under room temperature and normal humidity for 12 ~ 24hours.

Soldering condition

Solder paste :	Sn-3Ag-0.5Cu
Peak temperature of reflow :	250°C

6. Performance

6.1 Electrical performance

	Items	Specifications	Test methods
1	Nominal resistance (R25) and tolerance	To meet the specifications shown on section 6.2.	Measurement shall be made applying 0.25mW max. at ambient temperature 25±0.2°C (#1). This test must be not influenced by self-heating.
2	Resistance temperature characteristic (B constant) and tolerance	To meet the specifications shown on section 6.2.	B constant calculated from the resistance between ambient temperature 25±0.2°C and 50±0.2°C (#2) or between ambient temperature 25±0.2°C and 85±0.2°C (#3) according to JIS-C-2570. $B = \frac{\ln R1 - \ln R2}{\frac{1}{T1} - \frac{1}{T2}}$ T1,T2 : Temperature[K] R1:Thermistor resistance at temperature T1 R2:Thermistor resistance at temperature T2
3	Maximum rated power	To meet the specifications shown on section 6.2.	Maximum rated power loaded consecutively in still air (25±0.2°C).
4	Permissive operating current	To meet the specifications shown on section 6.2.	Maximum current to rise 1°C in still air (25±0.2°C).

6.2 Electrical characteristic specifications

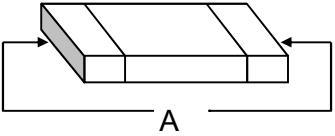
No.	TDK product name	R25 (#1) [Ω]	B constant		Maximum rated power (25°C) [mW]	Permissive operating current (25°C) [mA]
			B25/50 (#2) [K]	B25/85 (#3) [K]		
1	NTCG203EH471JTDS	470 ± 5%	-	3250 ± 3%	200	2.00
2	NTCG203EH681JTDS	680 ± 5%	-	3250 ± 3%	200	1.70
3	NTCG203BH102JTDS	1.0k ± 5%	-	3100 ± 3%	200	1.40
4	NTCG203BH152JTDS	1.5k ± 5%	-	3100 ± 3%	200	1.10
5	NTCG203FH222JTDS	2.2K ± 5%	-	3300 ± 3%	200	0.95
6	NTCG203FH332JTDS	3.3k ± 5%	-	3300 ± 3%	200	0.77
7	NTCG203JH472JTDS	4.7k ± 5%	-	3450 ± 3%	200	0.65
8	NTCG203JH682JTDS	6.8k ± 5%	-	3450 ± 3%	200	0.54
9	NTCG203NH103JTDS	10k ± 5%	-	3650 ± 3%	200	0.44
10	NTCG203NH153JTDS	15k ± 5%	-	3650 ± 3%	200	0.36
11	NTCG203SH223JTDS	22k ± 5%	-	3850 ± 3%	200	0.30
12	NTCG203SH333JTDS	33k ± 5%	-	3850 ± 3%	200	0.24
13	NTCG204AH473JTDS	47k ± 5%	-	4000 ± 3%	200	0.20
14	NTCG204AH683JTDS	68k ± 5%	-	4000 ± 3%	200	0.17
15	NTCG204CH104JTDS	100k ± 5%	-	4150 ± 3%	200	0.14
16	NTCG204CH154JTDS	150k ± 5%	-	4150 ± 3%	200	0.11
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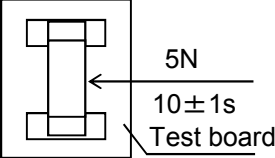
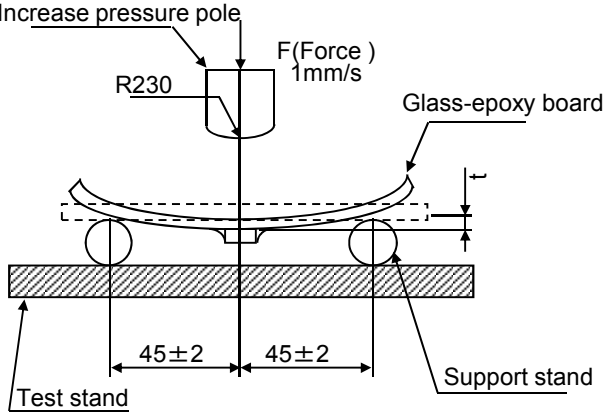
#1) R25 : Nominal resistance

#2) B25/50 : B constant between 25 and 50°C

#3) B25/85 : B constant between 25 and 85°C

6.3 Mechanical performance

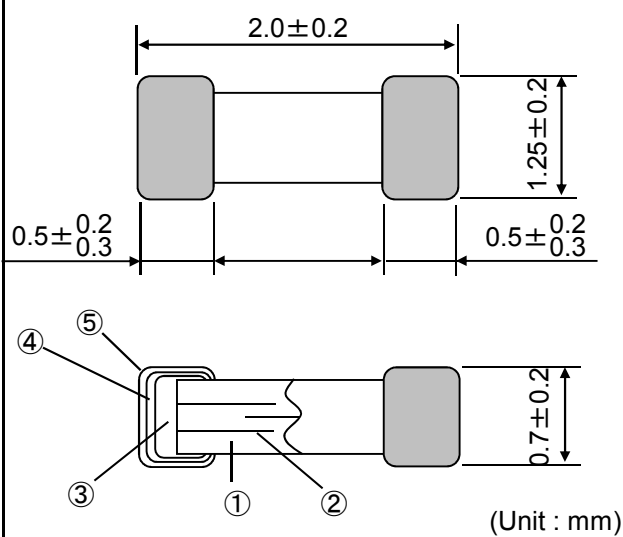
	Items	Specifications	Test methods
1	Vibration	1) External appearance shall have no mechanical damage 2) Resistance change: within $\pm 3\%$ 3) B constant change: within $\pm 1\%$	Test shall be performed according to MIL-STD-202 method 204, Condition F. Test cycle: 12 cyc.
2	Shock test	1) External appearance shall have no mechanical damage at the thermistor. 2) Resistance change: within $\pm 3\%$ 3) B constant change: within $\pm 1\%$	Test shall be performed according to MIL-STD-202 method 213, Condition F.
3	Solderability	More than 75% of terminal electrode sections shall be covered with new solder. 25% may have pinholes or rough spots but they shall not be collected in one position. Ceramic surface of A-sections shall not be exposed due to melting or shifting of electrode sections. 	Completely soak both terminal electrodes in solder of $245 \pm 3^\circ\text{C}$ for 3 ± 0.5 seconds. Solder : Sn-3Ag-0.5Cu Flux : Ethanol(JIS K 1501) solution of rosin (JIS K 5902) concentration with rosin approximately 20%(weight ratio) Test shall be performed according to JIS-C-2570.
4	Resistance to solder heat	1) External appearance shall have no abnormal sign at the thermistor. 2) External appearance shall have no mechanical damage at the thermistor. 3) Resistance change: within $\pm 3\%$ 4) B constant change: within $\pm 1\%$	Completely dip both terminal electrodes in solder of $260 \pm 3^\circ\text{C}$ for 10 ± 0.5 seconds. Solder : Sn-3Ag-0.5Cu Flux : Ethanol(JIS K 1501) solution of rosin (JIS K 5902) concentration with rosin approximately 20%(weight ratio) Preheat Temp. : $100 \pm 10^\circ\text{C}$ Time : $30 \pm 5\text{s}$ Test shall be performed according to JIS-C-2570. Or, passed through the reflow oven under the condition shown on §8.

	Items	Specifications	Test methods
5	Robustness of terminations	There shall be no sign of coming off terminal electrodes, breakage of ceramic, or other abnormal sign.	<p>Test samples shall be mounted test board (1.2mm thickness glass-epoxy board) with solder.</p> <p>The soldering methods shall be soldering iron or reflow soldering without any thermal shock nor cause of failure.</p> 
6	Bending strength test	No mechanical damage shall be caused on the thermistor.	<p>Test shall be performed according to JIS-C-2570.</p> <p>To be soldered on the glass-epoxy board (thickness 1.6mm), the load shall be put on the until the board bends 1mm.</p> 
7	Electrostatic discharge test	<p>1) No mechanical damage shall be caused on the thermistor.</p> <p>2) Resistance change: within $\pm 5\%$</p> <p>3) B constant change: within $\pm 2\%$</p>	<p>HBM(Human Body Model)</p> <p>Discharge resistor : 330Ω</p> <p>Energy storage capacitance : 150pF</p> <p>Output voltage : $\pm 2\text{kV}$</p> <p>Number of discharge : 10 times</p> <p>Contact discharge</p> <p>Test shall be performed according to IEC61000-4-2.</p>

6.4 Life test

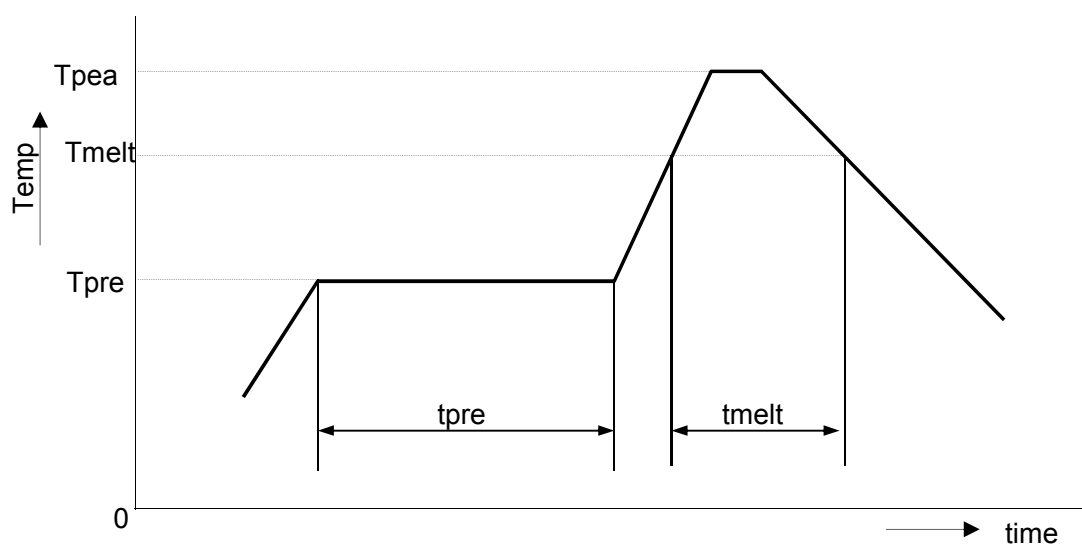
	Items	Specifications	Test methods									
1	High temperature storage	1)Resistance change: within $\pm 5\%$ 2)B constant change: within $\pm 2\%$	Leave at temperature $125\pm 5^{\circ}\text{C}$ for 1000 ± 12 hours.									
2	Temperature cycle	1)Shall have no mechanical damage at the thermistor. 2)Resistance change: within $\pm 5\%$ 3)B constant change: within $\pm 2\%$	Leave at temperature of the two steps of 1 to 2 sequentially, shown in the following table. With the above procedure as one cycle, repeat 1000 cycles consecutively. <table border="1" data-bbox="928 631 1417 741"> <thead> <tr> <th>Step</th> <th>Temp. [$^{\circ}\text{C}$]</th> <th>Time[minutes]</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>-40 ± 5</td> <td>30</td> </tr> <tr> <td>2</td> <td>125 ± 5</td> <td>30</td> </tr> </tbody> </table> <p>The transfer time at each temperature is within a minute.</p>	Step	Temp. [$^{\circ}\text{C}$]	Time[minutes]	1	-40 ± 5	30	2	125 ± 5	30
Step	Temp. [$^{\circ}\text{C}$]	Time[minutes]										
1	-40 ± 5	30										
2	125 ± 5	30										
3	Moisture resistance	1)Shall have no mechanical damage at the thermistor. 2)Resistance change: within $\pm 5\%$ 3)B constant change: within $\pm 2\%$	Test shall be performed according to MIL-STD-202 method 106 Test cycle: 10 cyc. (24 hours/cycle). Note: Steps 7a & 7b not required. Unpowered.									
4	Biased Humidity	1)Shall have no mechanical damage at the thermistor. 2)Resistance change: within $\pm 5\%$ 3)B constant change: within $\pm 2\%$	At temperature $85\pm 5^{\circ}\text{C}$ and humidity 82 to 87%RH, apply the 10mW (DC voltage) for 1000 ± 12 hours.									
5	Operational life	1)Shall have no mechanical damage at the thermistor. 2)Resistance change: within $\pm 5\%$ 3)B constant change: within $\pm 2\%$	At temperature $125\pm 5^{\circ}\text{C}$, apply the 0.1mW (DC voltage) for 1000 ± 12 hours, and then taken out to room temperature for 2 hours before measurement.									

7. Shapes and dimension (Construction)



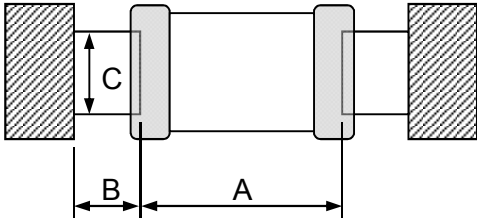
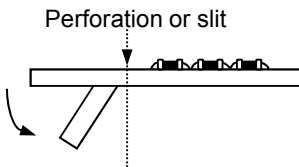
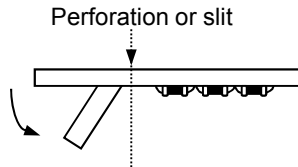
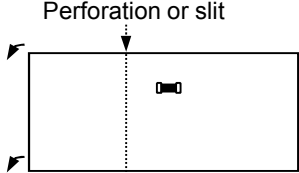
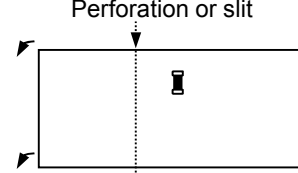
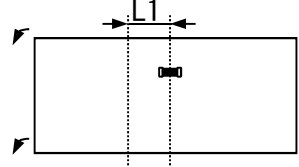
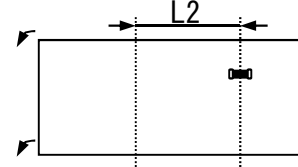
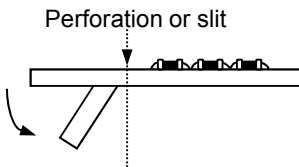
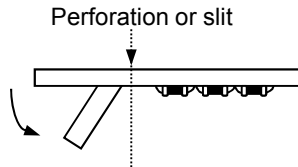
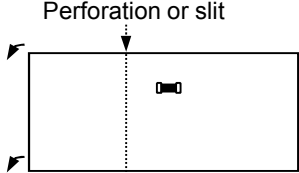
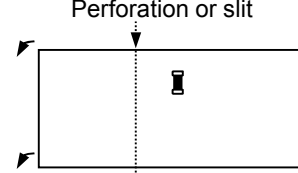
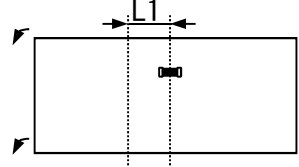
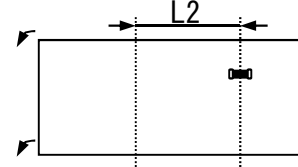
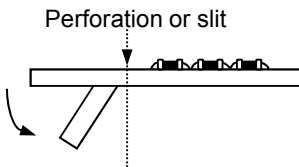
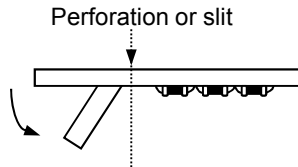
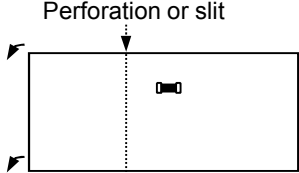
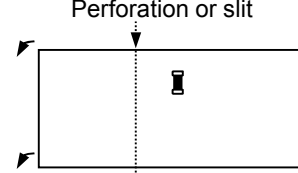
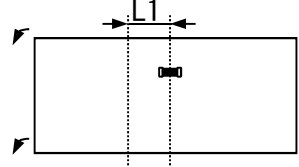
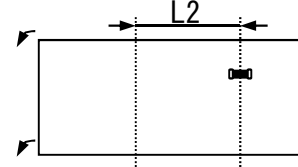
No.	Construction	Materials, treatment
①	NTC Thermistor element	Mn, Ni, Co-oxide base ceramics materials
②	Inner electrodes	Palladium ※The number of inner electrodes may not be according to this figure.
③		Silver base
④	Terminals	Nickel-plating
⑤		Tin-plating

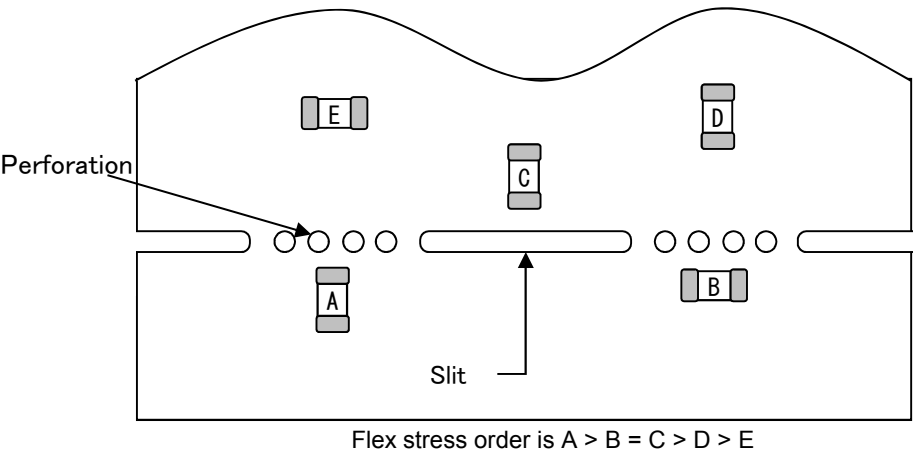
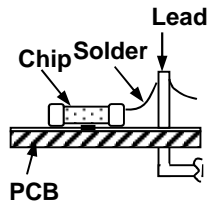
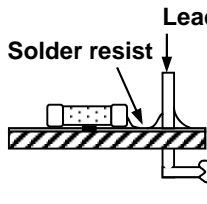
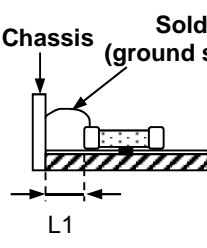
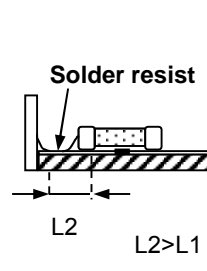
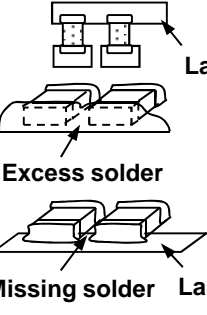
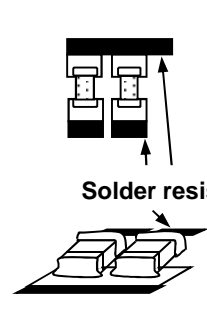
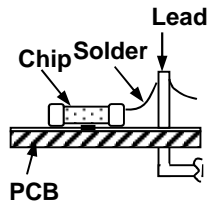
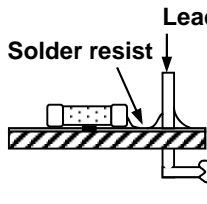
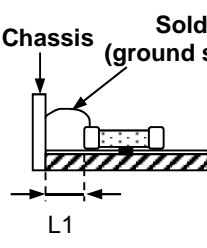
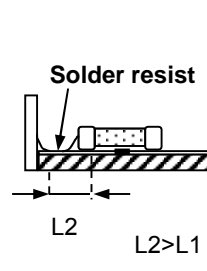
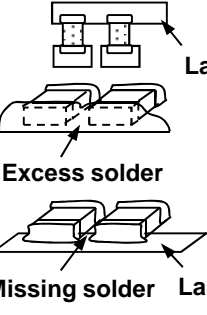
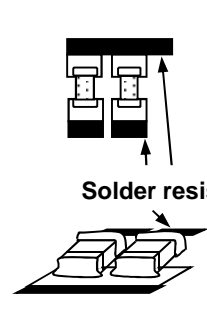
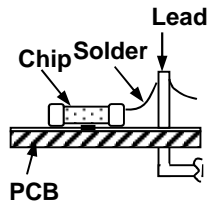
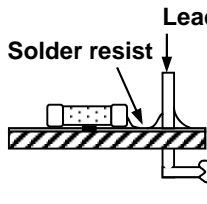
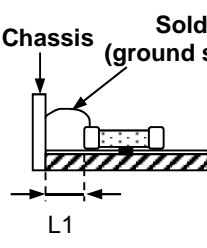
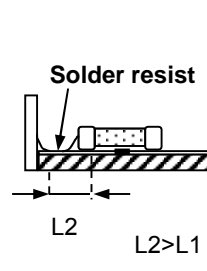
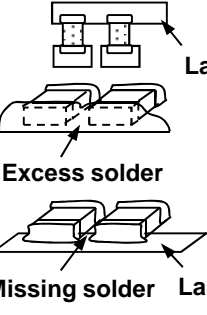
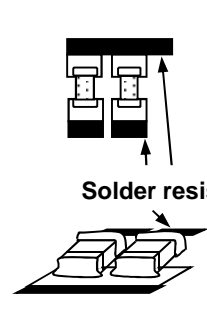
8. Reflow temperature profile

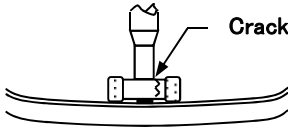
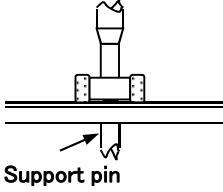
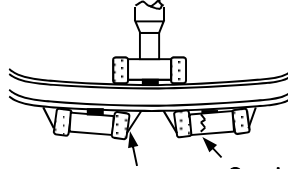
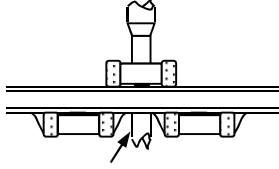
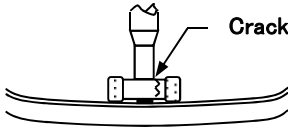
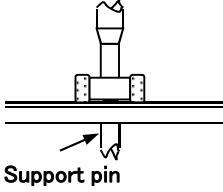
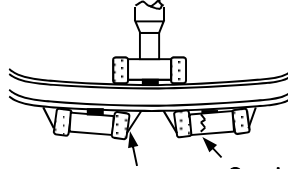
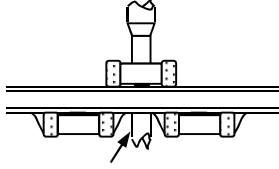
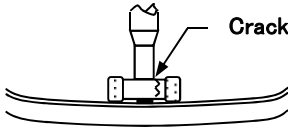
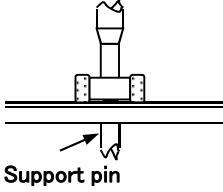
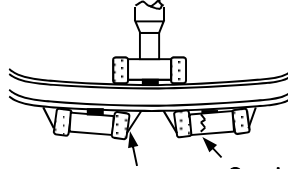
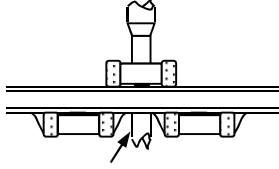


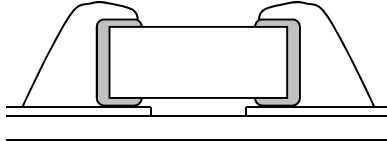
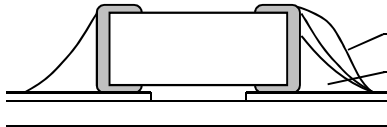
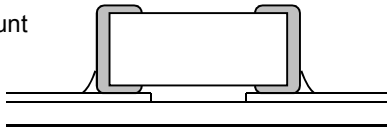
Item	Conditions	Conditions	
		For Sn-Pb solder	For lead-free solder
T_{pre}	Preheat temp.	160 ~ 180°C	150 ~ 180°C
T_{melt}	Solder melting temp.	200°C	230°C
T_{peak}	Peak temp.	240°C max.	260°C max.
t_{pre}	Preheat time	100s max.	120s max.
t_{melt}	Solder melting time	30s max.	40s max.
	Reflow possibility times	2 max.	2 max.

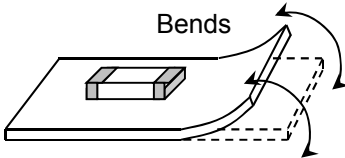
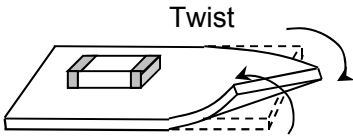
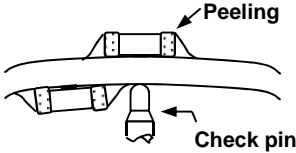
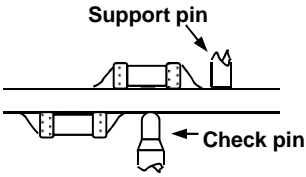
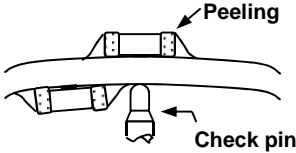
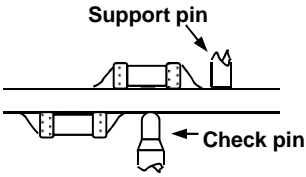
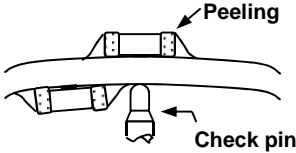
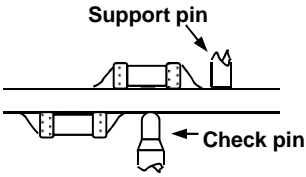
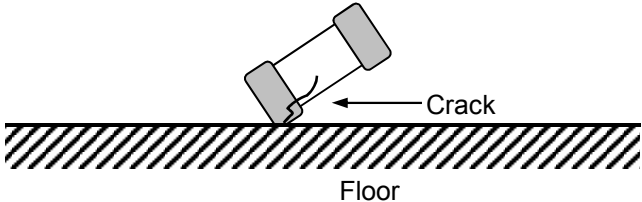
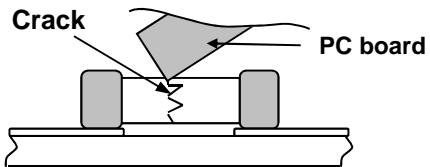
9. Application manual

	Process	Condition																								
1	PC Board design	<p>The solder fillet volume will directly affect the reliability of the NTC thermistors.</p> <p>1)The greater the amount of solder, the higher the stress on the NTC thermistors, and the more likely that it will break. When designing a P.C.board, determine the shape and size of the solder lands to have proper amount of solder on the terminations.</p> <p>2)Avoid using common solder land for multiple terminations and provide individual solder land for each termination.</p> <p>3)Recommended PC Board Pattern</p> <div style="text-align: center;">  <p>(Unit : mm)</p> </div> <p>· Recommend land pattern for reflow use</p> <table border="1" data-bbox="493 831 1302 931"> <thead> <tr> <th>Symbol</th> <th>A</th> <th>B</th> <th>C</th> </tr> </thead> <tbody> <tr> <td>Dimension</td> <td></td> <td></td> <td></td> </tr> <tr> <td>2012</td> <td>0.9 ~ 1.2</td> <td>0.7 ~ 0.9</td> <td>0.9 ~ 1.2</td> </tr> </tbody> </table> <p>4)Relationship between NTC thermistors position and flex stress during board separation</p> <table border="1" data-bbox="493 1025 1410 1973"> <thead> <tr> <th></th> <th>Disadvantage against bending stress</th> <th>Advantage against bending stress</th> </tr> </thead> <tbody> <tr> <td>Mounting face</td> <td> <p>Perforation or slit</p>  <p>Hold chip mounting surface upward and bend upward</p> </td> <td> <p>Perforation or slit</p>  <p>Hold chip mounting surface downward and bend downward.</p> </td> </tr> <tr> <td>Chip arrangement (Direction)</td> <td> <p>Mount perpendicularly to perforation or slit</p> <p>Perforation or slit</p>  </td> <td> <p>Mount in parallel with perforation or slit</p> <p>Perforation or slit</p>  </td> </tr> <tr> <td>Distance from slit</td> <td> <p>Closer to slit is higher stress</p>  <p>(L1<L2)</p> </td> <td> <p>Away from slit is less stress</p>  <p>(L1<L2)</p> </td> </tr> </tbody> </table>	Symbol	A	B	C	Dimension				2012	0.9 ~ 1.2	0.7 ~ 0.9	0.9 ~ 1.2		Disadvantage against bending stress	Advantage against bending stress	Mounting face	<p>Perforation or slit</p>  <p>Hold chip mounting surface upward and bend upward</p>	<p>Perforation or slit</p>  <p>Hold chip mounting surface downward and bend downward.</p>	Chip arrangement (Direction)	<p>Mount perpendicularly to perforation or slit</p> <p>Perforation or slit</p> 	<p>Mount in parallel with perforation or slit</p> <p>Perforation or slit</p> 	Distance from slit	<p>Closer to slit is higher stress</p>  <p>(L1<L2)</p>	<p>Away from slit is less stress</p>  <p>(L1<L2)</p>
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Process	Condition												
	<p>5) Mechanical stress varies according to location of NTC thermistors on the P.C.board.</p>  <p>Flex stress order is A > B = C > D > E</p> <p>6) Example of placement to be avoided and layout recommendations (processing of land divisions)</p> <table border="1"> <thead> <tr> <th data-bbox="494 918 762 952">Example</th> <th data-bbox="762 918 1085 952">Need to avoid</th> <th data-bbox="1085 918 1407 952">Recommendation</th> </tr> </thead> <tbody> <tr> <td data-bbox="494 952 762 1272">Use of common solder land with PTH components</td> <td data-bbox="762 952 1085 1272">  </td> <td data-bbox="1085 952 1407 1272">  </td> </tr> <tr> <td data-bbox="494 1272 762 1601">Soldering with chassis</td> <td data-bbox="762 1272 1085 1601">  </td> <td data-bbox="1085 1272 1407 1601">  </td> </tr> <tr> <td data-bbox="494 1601 762 1960">Use of common solder land with other SMD</td> <td data-bbox="762 1601 1085 1960">  </td> <td data-bbox="1085 1601 1407 1960">  </td> </tr> </tbody> </table>	Example	Need to avoid	Recommendation	Use of common solder land with PTH components			Soldering with chassis			Use of common solder land with other SMD		
Example	Need to avoid	Recommendation											
Use of common solder land with PTH components													
Soldering with chassis													
Use of common solder land with other SMD													

	Process	Condition									
2	Mounting	<p>Pressure on installation head</p> <p>If the drop point of the suction nozzle is too low, excessive forces will be applied to the NTC thermistors during installation and that may cause breaks.</p> <ol style="list-style-type: none"> 1) Realign the circuit board slide and adjust the bottom dead center over the circuit board. 2) The usual amount of mounting force is 0.1N to 0.3N (dead weight). 3) To prevent pressure from the installation head causes the circuit board to bend, we recommend the installation of support pins for the back surface of the circuit board. (See the model diagram as below.) <table border="1" data-bbox="493 528 1410 1016"> <thead> <tr> <th data-bbox="493 528 655 562"></th> <th data-bbox="655 528 1034 562">Locations to avoid</th> <th data-bbox="1034 528 1410 562">Recommended locations</th> </tr> </thead> <tbody> <tr> <td data-bbox="493 562 655 786">single-sided circuit board</td> <td data-bbox="655 562 1034 786">  <p>Crack</p> </td> <td data-bbox="1034 562 1410 786">  <p>Support pin</p> </td> </tr> <tr> <td data-bbox="493 786 655 1016">dual-sided circuit board</td> <td data-bbox="655 786 1034 1016">  <p>Solder peeling Crack</p> </td> <td data-bbox="1034 786 1410 1016">  <p>Support pin</p> </td> </tr> </tbody> </table> <p>When the centering jaw is worn, excessive localized shock may be loaded on the NTC thermistors during positioning and cracking may occur. To prevent problems like this from occurring, carefully monitor the centering jaw and ensure that periodic maintenance and inspection is performed.</p>		Locations to avoid	Recommended locations	single-sided circuit board	 <p>Crack</p>	 <p>Support pin</p>	dual-sided circuit board	 <p>Solder peeling Crack</p>	 <p>Support pin</p>
	Locations to avoid	Recommended locations									
single-sided circuit board	 <p>Crack</p>	 <p>Support pin</p>									
dual-sided circuit board	 <p>Solder peeling Crack</p>	 <p>Support pin</p>									
3	Soldering	<p>3-1 Recommended flux</p> <p>Flux is one of the more important elements affecting the performance of NTC thermistors. Before selecting and using flux, check the items shown below for the best flux.</p> <ol style="list-style-type: none"> 1) Use flux that has a chlorine content of 0.1wt% or less. Do not use strong acid flux. 2) Excessive flux must be avoided. Keep the amount of flux coating minimized. 3) When water-soluble flux is used, enough washing is necessary. <p>3-2 Solder amount</p> <p>Too much or too little solder may cause serious consequences for circuit reliability such as cracks from solder stress and parts falling off the circuit board. Check the diagram and make sure to provide the right amount of solder.</p>									

	Process	Condition										
		<div style="display: flex; justify-content: space-between;"> <div data-bbox="496 304 667 360">Excess amount of solder</div> <div data-bbox="679 282 1067 421">  </div> <div data-bbox="1086 304 1358 360">Increased occurrence of solder stress and cracks</div> </div> <hr/> <div style="display: flex; justify-content: space-between;"> <div data-bbox="496 465 647 521">Right amount of solder</div> <div data-bbox="679 456 1067 582">  </div> <div data-bbox="1086 456 1366 521">Maximum layer quantity Minimum layer quantity</div> </div> <hr/> <div style="display: flex; justify-content: space-between;"> <div data-bbox="496 629 707 685">Insufficient amount of solder</div> <div data-bbox="679 620 1067 745">  </div> <div data-bbox="1086 595 1414 719">Adhesive strength is weak producing danger of defective contacts and parts coming off of board.</div> </div>										
		<p>3-3 Condition of re-work by a soldering iron Basically TDK don't recommend re-work by a soldering iron. In case of re-work, the condition is shown below.</p> <p>1)The selection of a soldering iron. The temperature of a soldering iron varies with the kinds, the size of printed circuit board and land patterns. In case of high temperature soldering, the time of the soldering work may be shortened. However thermal shock may cause cracks in some cases, so please solder condition below.</p> <table border="1" data-bbox="493 1115 1461 1279"> <thead> <tr> <th>Tip temp. (°C)</th> <th>Wattage (W)</th> <th>Tip shape (mm)</th> <th>Soldering time (s)</th> <th>Number of soldering</th> </tr> </thead> <tbody> <tr> <td>350max.</td> <td>20max.</td> <td>φ3.0max.</td> <td>5 max.</td> <td>1 time or less for each terminals. (Total 2 times or less)</td> </tr> </tbody> </table> <p>2)If the soldering iron may touch the body of NTC thermistors, the strain will be caused by thermal shock, in some case thermal crack may be caused. Don't contact a soldering iron to NTC thermistors body except terminals.</p>	Tip temp. (°C)	Wattage (W)	Tip shape (mm)	Soldering time (s)	Number of soldering	350max.	20max.	φ3.0max.	5 max.	1 time or less for each terminals. (Total 2 times or less)
Tip temp. (°C)	Wattage (W)	Tip shape (mm)	Soldering time (s)	Number of soldering								
350max.	20max.	φ3.0max.	5 max.	1 time or less for each terminals. (Total 2 times or less)								
4	Cleaning	<p>1)In the case that cleaning fluid is inappropriate, residues of flux and other foreign substance adhere on the surface of the NTC thermistors and the case may causes deterioration the performance (especially insulation resistance) of the NTC thermistors.</p> <p>2)In the case that cleaning condition is inappropriate(cleaning insufficient, cleaning excessive), it may deteriorate the performance of the NTC thermistors.</p> <p>2-1).Cleaning insufficient</p> <p>(1)The terminal electrode may corrode by the substance of the halogenate in residues of flux.</p> <p>(2)The halogenate in residues of flux adheres on the surface of the NTC thermistors and may deteriorate insulation resistance.</p> <p>(3)Water soluble flux has higher tendency to have above mentioned problems (1) and (2).</p>										

	Process	Condition						
5	Handling of PC board after mounting	<p>2-2).Cleaning excessive (1)It may deteriorate the performance of the NTC thermistors by cleaning fluid. (2)If the ultrasonic energy output is too high during ultrasonic cleaning that will affect the adhesion of terminal electrodes so we recommend the following conditions for ultrasonic cleaning: Output : 20W/liter or less Frequency : 40kHz or less Cleaning time : 5minutes or less</p> <p>2-3).Pollution of cleaning fluid may resulted the such as cleaning insufficient.</p> <p>1)As separate the board, please do not load the stress shown in the blow as much as possible, because crack may occur to the NTC thermistors.</p> <div style="display: flex; justify-content: space-around; align-items: center;"> <div style="text-align: center;">  <p>Bends</p> </div> <div style="text-align: center;">  <p>Twist</p> </div> </div> <p>2)To prevent pressure from the check pin causes the circuit board to bend, we recommend the installation of support pins for the back surface of the circuit board. (see the model diagram on the below.)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th data-bbox="493 1016 710 1048">Item</th> <th data-bbox="710 1016 1085 1048">Locations to avoid</th> <th data-bbox="1085 1016 1461 1048">Recommended locations</th> </tr> </thead> <tbody> <tr> <td data-bbox="493 1048 710 1308" style="text-align: center; vertical-align: middle;">Board bends</td> <td data-bbox="710 1048 1085 1308" style="text-align: center;">  </td> <td data-bbox="1085 1048 1461 1308" style="text-align: center;">  </td> </tr> </tbody> </table>	Item	Locations to avoid	Recommended locations	Board bends		
Item	Locations to avoid	Recommended locations						
Board bends								
6	Handling of chip	<p>1)Please do not use the NTC thermistors that fell, because a damage and crack may occur by the fall impact.</p> <div style="text-align: center;">  </div> <p>2)Please pay attention handling of the PC board after soldering, because damage and crack might occur to the NTC thermistors with the impact by the accumulation storage and the corner of the board hits to the NTC thermistors.</p> <div style="text-align: center;">  </div>						

10. Taping

10.1 Scope

This specification sheet is applicable to the tape packaging of chip type NTC Thermistor. When questionable matters are found regarding the specification of the products, this specification sheet shall be applicable with a priority, and the matters shall be settled with written documents upon conferring and confirming between the design groups of both companies.

10.2 Structure

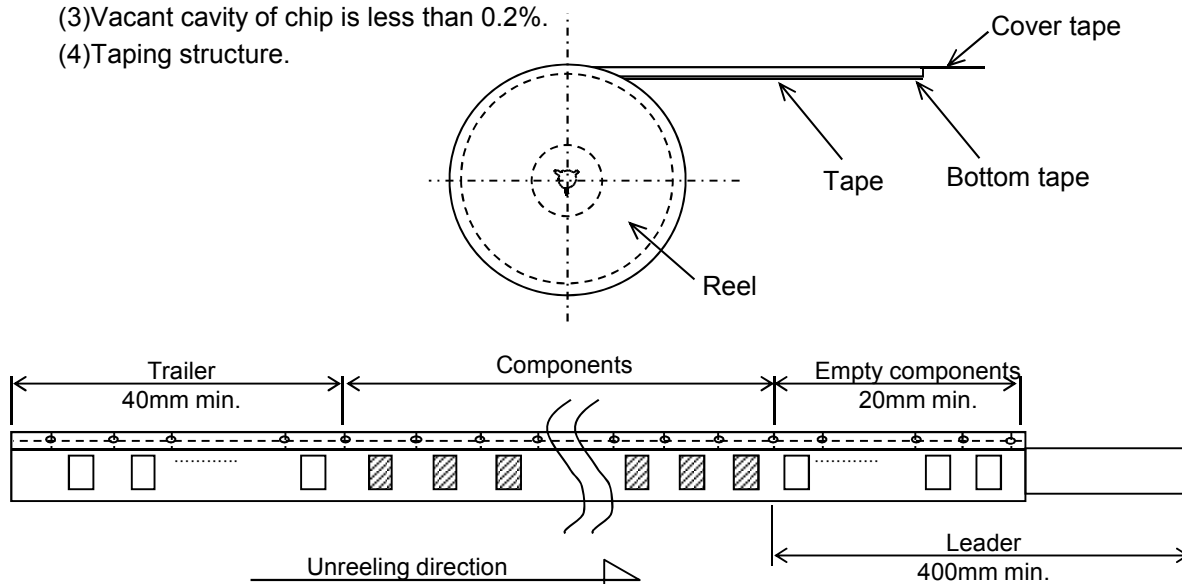
(1) Taping consist of :

- ① Tape (Dimensions are shown in section 10.5)
- ② Reel (Dimensions are shown in section 10.6)
- ③ Cover tape
- ④ Bottom tape

(2) 2,000 chips is in each reel.

(3) Vacant cavity of chip is less than 0.2%.

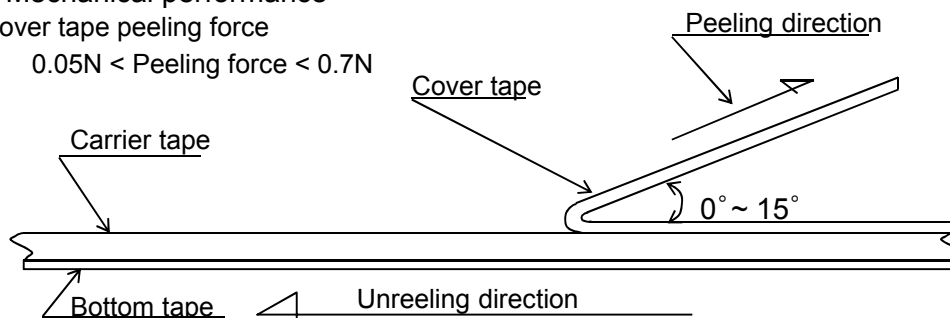
(4) Taping structure.



10.3 Mechanical performance

Cover tape peeling force

$$0.05\text{N} < \text{Peeling force} < 0.7\text{N}$$



(When cover tape is peeled, paper tape shall not adhere to the cover tape by broken.)

10.4 Tape packaging specification

(1) Tape bending

Taping shall show no mechanical damage nor with bending diameter of 30mm min.

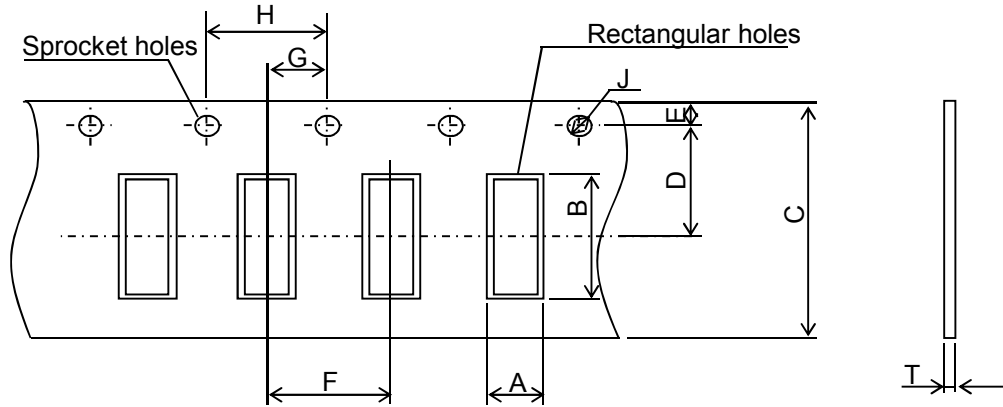
(2) Adhesion of the chip to the cover tape

Chip shall not be stuck to cover tape and exist in rectangular hole.

(3) Burr etc. of the rectangular hole

When cover tape is peeled, NTC thermistor shall be easily picked up and pick-up nozzle may not be stopped up by paper filings.

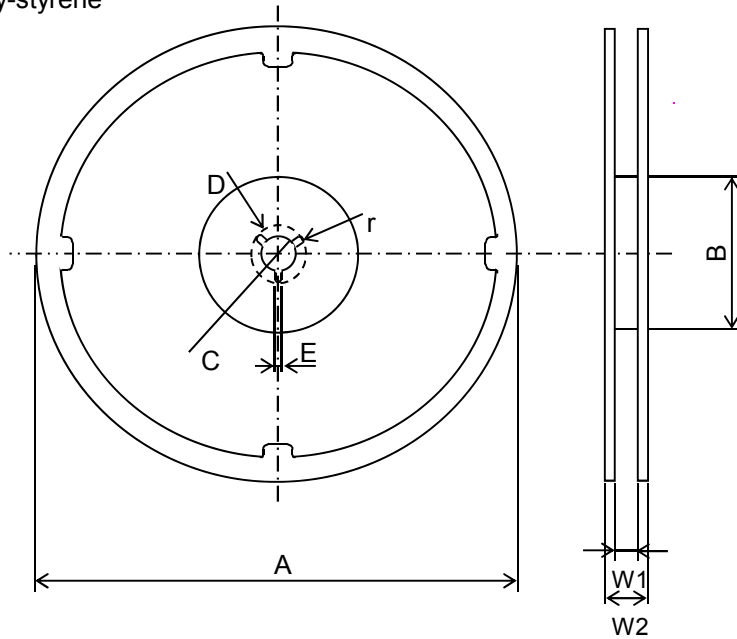
10.5 Paper tape dimension



Dimension [mm]	A	B	C	D	E	F
	1.5±0.2	2.3±0.2	8.0±0.3	3.5±0.05	1.75±0.1	4.0±0.1
	G	H	J	T		
	2.0±0.05	4.0±0.1	\varnothing 1.5 ^{+0.1} ₋₀	1.1MAX		

10.6 Reel dimension

Material : Poly-styrene



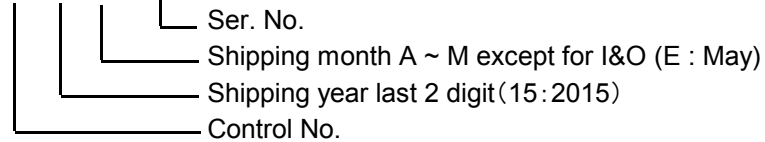
Dimension [mm]	A	B	C	D	E
	\varnothing 180±2.0	\varnothing 60min	\varnothing 13±0.2	\varnothing 21±0.8	2.0±0.5
	W1	W2	r		
	9.0min	14max	0.8		

11. Package & transport

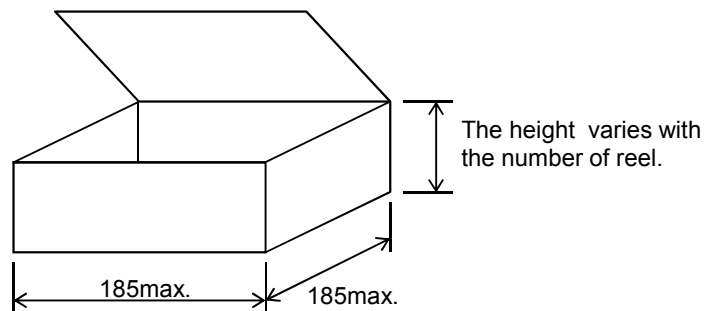
11.1 Package shall be secure from damage during transportation and storage, shipping label shall be attached.

- (1) Shipping No.
- (2) TDK's parts No.
- (3) Customer's parts No.
- (4) Quantity
- (5) Production Country

Example) AE 15 E 0406701



11.2 Package is shown as figure below. (unit : mm)



12. Production Country

Japan

X-ON Electronics

Largest Supplier of Electrical and Electronic Components

Click to view similar products for [NTC \(Negative Temperature Coefficient\) Thermistors category:](#)

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