

### **74AHC1G08**

2-INPUT AND GATE

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#### **Features**

- Operation Voltage Range:2~5.5V
- Low Power Current: I<sub>CC</sub>=10μA(Max)
- High speed: t PD=4.3ns(Typ) @ VCC =5V
- ESD Protection Exceeds JESD 22
- 2000-V Human-Body Model (A114-A)
- 1000-V Charged-Device Model (C101)
- SOT23-5 Package Available
- SOT353 Package Available

#### **General Descrition**

The 74AHC1G08 is a high-speed si-gate CMOS device which provides the 2-input AND function.

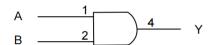
## **Applications**

- Voltage Level Shifting
- General Purpose Logic
- Power Down Signal Isolation
- Wide array of products such as:
  - PCs, Networking, Notebooks, Netbooks, PDAs
  - Tablet Computers, E-readers
  - Computer Peripherals, Hard Drives, CD/DVD ROM
  - TV, DVD, DVR, Set-Top Box
  - Cell Phones, Personal Navigation / GPS
  - MP3 Players, Cameras, Video Recorders

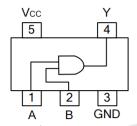
## **Ordering Information**

ORDER NUMBER	PACKAGE DESCRIPTION	PACKAGE OPTION
74AHC1G08GV	SOT23-5	Tape and Reel,3000
74AHC1G08GW	SOT353	Tape and Reel,3000

## **Logic Diagram**



## **Pin Configuration**



SOT23-5/SOT353

## **Marking**

74AHC1G08GV Marking:A08 74AHC1G08GW Marking:AE

#### **Function Table**

INP	INPUTS		
Α	В	Υ	
L	L	L	
L	Н	L	
Н	L	L	
Н	Н	Н	





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#### **Absolute Maximum Ratings**

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	Vcc	-0.5~7	V
Input Voltage	V <sub>IN</sub>	-0.5~7	V
Output Voltage	V <sub>OUT</sub>	-0.5~V <sub>CC</sub> +0.5	V
Input Clamp Current	I <sub>IK</sub>	-20	mA
Output Clamp Current	I <sub>OK</sub>	±20	mA
Output Current	l <sub>OUT</sub>	±25	mA
V <sub>CC</sub> or GND Current	Icc	±50	mA
Power Dissipation	P <sub>D</sub>	250	mW
Storage Temperature	T <sub>STG</sub>	-65 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

## **Recommended Operating Conditions**

<b>I</b>						
PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Supply Voltage	V <sub>CC</sub>		2		5.5	<b>V</b>
Input Voltage	V <sub>IN</sub>		0		5.5	<b>V</b>
Output Voltage	$V_{OUT}$		0		$V_{CC}$	<b>V</b>
Innut Transition Disc or Fall Times	,	V <sub>CC</sub> =3.3V±0.3V			100	ns/V
Input Transition Rise or Fall Times	$t_R, t_F$	V <sub>CC</sub> =5.0V±0.5V			20	ns/v
Operating Temperature	T <sub>OPR</sub>		-40		125	°C





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#### **Electrical Characteristics**

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
		V <sub>CC</sub> =2.0V	1.5			V
High-Level Input Voltage	V <sub>IH</sub>	V <sub>CC</sub> =3.0V	2.1			V
		V <sub>CC</sub> =5.5V	3.85			V
		V <sub>CC</sub> =2.0V			0.5	V
Low-Level Input Voltage	VIL	V <sub>CC</sub> =3.0V			0.9	V
		V <sub>CC</sub> =5.5V			1.65	<b>V</b>
		V <sub>CC</sub> =2.0V, I <sub>OH</sub> =-50μA	1.9	2.0		V
		V <sub>CC</sub> =3.0V, I <sub>OH</sub> =-50μA	2.9	3.0		V
High-Level Output Voltage	V <sub>OH</sub>	V <sub>CC</sub> =4.5V, I <sub>OH</sub> =-50μA	4.4	4.5		V
		V <sub>CC</sub> =3.0V, I <sub>OH</sub> =-4mA	2.58			V
		V <sub>CC</sub> =4.5V, I <sub>OH</sub> =-8mA	3.94			V
		$V_{CC}$ =2.0V, $I_{OL}$ =50 $\mu$ A			0.1	V
		V <sub>CC</sub> =3.0V, I <sub>OL</sub> =50μA			0.1	V
Low-Level Output Voltage	V <sub>OL</sub>	V <sub>CC</sub> =4.5V, I <sub>OL</sub> =50μA			0.1	V
		V <sub>CC</sub> =3.0V, I <sub>OL</sub> =4mA			0.36	V
		V <sub>CC</sub> =4.5V, I <sub>OL</sub> =8mA			0.36	V
Input Leakage Current	I <sub>I(LEAK)</sub>	V <sub>CC</sub> =0~5.5V, V <sub>IN</sub> =V <sub>CC</sub> or GND			±0.1	μΑ
Quiescent Supply Current	ΙQ	$V_{CC}$ =5.5V, $V_{IN}$ = $V_{CC}$ or GND, $I_{OUT}$ =0			1	μΑ
Input Capacitance	C <sub>IN</sub>	V <sub>CC</sub> =5V, V <sub>IN</sub> =V <sub>CC</sub> or GND		4	10	pF

## **Dynamic Characteristics** (Input: t<sub>R</sub>, t<sub>F</sub>≤3ns; P<sub>RR</sub>≤1MHz)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
	t <sub>PLH</sub>	\\ -3.3\\\\0.3\\\0.7\		6.2	8.8	ns
Propagation delay time	t <sub>PHL</sub>	V <sub>CC</sub> =3.3V±0.3V, C <sub>L</sub> =15pF		6.2	8.8	ns
Input (A or B) to output(Y)	t <sub>PLH</sub>	V2 2)/+0 2)/ C =50pE		8.7	12.3	ns
	t <sub>PHL</sub>	V <sub>CC</sub> =3.3V±0.3V, C <sub>L</sub> =50pF		8.7	12.3	ns
	<b>t</b> PLH			4.3	5.9	ns
Propagation delay time	t <sub>PHL</sub>			4.3	5.9	ns
Input (A or B) to output(Y)	t <sub>PLH</sub>			5.8	7.9	ns
	tphL			5.8	7.9	ns



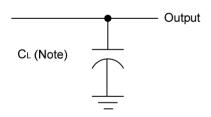


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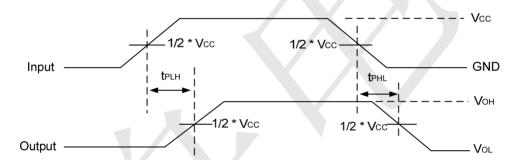
# **Operating Characteristics**

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Power Dissipation Capacitance	$C_PD$	No load, V <sub>CC</sub> =5V, f=1MHz		18		pF

#### **Test Circuit And Waveforms**



Note: CL includes probe and jig capacitance.



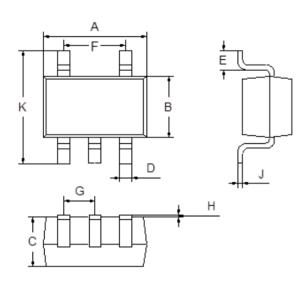




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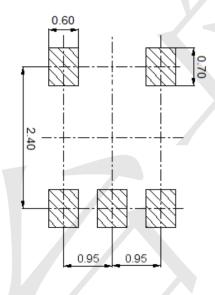
## Package Outline Dimensions (Unit: mm)

SOT23-5



Dimension	Min.	Max.
Α	2.80	3.00
В	1.50	1.70
С	1.00	1.20
D	0.35	0.45
E	0.35	0.55
F	1.80	2.00
G	0.90	1.00
Н	0.02	0.10
J	0.10	0.20
K	2.60	3.00

## Mounting Pad Layout (Unit: mm)



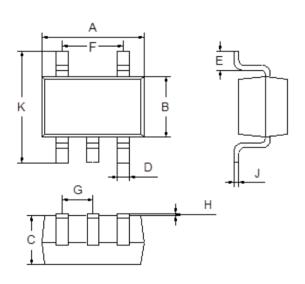




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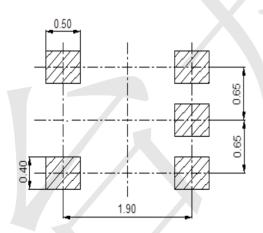
## Package Outline Dimensions (Unit: mm)

**SOT353** 



Dimension	Min.	Max.
Α	2.00	2.20
В	1.15	1.35
С	0.85	1.05
D	0.15	0.35
E	0.25	0.40
F	1.20	1.40
G	0.60	0.70
Н	0.02	0.10
J	0.05	0.15
K	2.20	2.40

# Mounting Pad Layout (Unit: mm)



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74LVC1G86Z-7 NLV74HC14ADR2G NLV74HC20ADR2G NLVVHC1G09DFT1G NLX2G86MUTCG 74LVC2G32RA3-7
74LVC2G00HD4-7 NL17SG02P5T5G 74LVC2G86HK3-7 NLVVHC1G14DFT2G NLX1G99DMUTWG NLVVHC1G00DFT2G
NLV7SZ57DFT2G NLV74VHC04DTR2G NLV27WZ00USG NLU1G86CMUTCG NLU1G08CMUTCG NL17SZ32P5T5G
NL17SZ00P5T5G NL17SH02P5T5G 74AUP2G00RA3-7 NLVVHC1GT00DFT2G NLV74HC02ADTR2G NLX1G332CMUTCG
NLVHCT132ADTR2G NL17SG86P5T5G NL17SZ05P5T5G NLV74VHC00DTR2G NLVVHC1G02DFT1G NLV74HC86ADR2G
74LVC2G86RA3-7 NL17SZ38DBVT1G NLV18SZ00DFT2G NLVVHC1G07DFT1G NLVVHC1G02DFT2G